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Top-down fabrication of ordered arrays of GaN nanowires by selective area sublimation†

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We demonstrate the top-down fabrication of ordered arrays of GaN nanowires by selective area sublimation of pre-patterned GaN(0001) layers grown by hydride vapor phase epitaxy on Al₂O₃. Arrays with nanowire diameters and spacings ranging from 50 to 90 nm and 0.1 to 0.7 μ m, respectively, are simultaneously produced under identical conditions. The sublimation process, carried out under high vacuum conditions, is analyzed *in situ* by reflection high-energy electron diffraction and line-of-sight quadrupole mass spectrometry. During the sublimation process, the GaN(0001) surface vanishes, giving way to the formation of semi-polar {1103} facets which decompose congruently following an Arrhenius temperature dependence with an activation energy of (3.54 \pm 0.07) eV and an exponential prefactor of 1.58 \times 10³¹ atoms per cm² per s. The analysis of the samples by low-temperature cathodoluminescence spectroscopy reveals that, in contrast to dry etching, the sublimation process does not introduce nonradiative recombination centers at the nanowire sidewalls. This technique is suitable for the top-down fabrication of a variety of ordered nanostructures, and could possibly be extended to other material systems with similar crystallographic properties such as ZnO.

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1 Introduction

Since the first reports on the self-assembled formation of singlecrystalline GaN nanowires (NWs) in the late nineties, 1,2 increasing efforts have been devoted to master the bottom-up fabrication of random and ordered arrays of group-III nitride NW heterostructures by metal-organic chemical vapor deposition and molecular beam epitaxy.3 The research in this field is motivated by the technological relevance of GaN, the material that has enabled the commercialization of solid-state lighting for general illumination, and the potential advantages promised by the NW architecture, specifically: (i) the absence of threading dislocations (TDs) in heteroepitaxial growth, (ii) the possibility of combining lattice mismatched compounds without introducing extended defects, (iii) the absence (reduction) of the quantum-confined Stark effect in radial (axial) NW heterostructures, (iv) the improved light extraction efficiency in comparison to as-grown planar devices, and (v) the opportunity to increase the size of the active region per area unit of the substrate when fabricating devices with a core-shell geometry.³⁻⁸ However, despite all these

Given that the epitaxial growth of group-III nitrides has reached a high level of maturity, it is appealing to combine this well-developed technology with some of the advantages offered by the NW architecture using a top-down approach. ^{7,14–17} It is important to note that top-down methods are also suitable to obtain arrays of largely dislocation free NWs despite the presence of a high density of TDs in the layer used for the top-down process. In fact, the average number of TDs per NW is determined by the product of the TD density of the initial GaN layer and the cross-sectional area of one NW. ¹⁵ For instance, for a substrate with a TD density on the order of 10^8 cm⁻², typical for GaN layers grown on Al_2O_3 , and a NW diameter of 100 nm, less than 1% of these objects will contain TDs. ¹⁸

advantages and worldwide research activities, the performance of NW based light-emitting diodes (LEDs) lags behind that of their state-of-the-art two-dimensional counterparts fabricated on Al₂O₃ and Si substrates, which nowadays exhibit wall-plug and external quantum efficiencies above 80% in the blue spectral range. The inferior performance of NW-based LEDs is mainly caused by the limitations and complexities inherent to their formation using bottom-up methods. Particularly, the nonideal growth conditions often required to promote either uniaxial or radial growth may favor the incorporation of higher concentrations of impurities and defects. Turthermore, the different chemical and physical properties of the precursors, which are simultaneously deposited on different crystal facets, can result in the formation of detrimental compositional and structural inhomogeneities. Turthermore, the original structural inhomogeneities.

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The top-down fabrication of NW arrays typically relies on dry etching of a lithographically patterned two-dimensional layer. This fabrication method has, however, not achieved much popularity within the NW community because, in addition to the difficulties in achieving vertical NW sidewalls, the dry etching process inevitably creates point defects acting as nonradiative recombination centers at the NW sidewalls. Significant progress has been made in recent years by introducing an additional anisotropic wet chemical etching step after the creation of NWs by inductively coupled plasma reactive ion etching (ICP-RIE). This additional step removes the damaged material and facilitates the formation of smooth and straight NW sidewalls. 15,16 Using this method, the fabrication of NW LEDs15 and optically pumped NW lasers has been demonstrated. As an alternative to this two-step etching approach, a new top-down fabrication method coined as selective area sublimation (SAS) was recently demonstrated by Damilano et al. 17,19 This approach avoids any damage to the NW sidewalls as it is based not on chemical etching but on material sublimation in vacuum, a process used before by different groups to decrease the diameter of as-grown NWs.20-22 Damilano et al. utilized this simple method to obtain random arrays of GaN NWs and (In,Ga)N/GaN NW heterostructures. 17,19

For device applications, it is almost mandatory that the NWs do not have a random arrangement, but form an ordered array with precisely tunable spatial arrangements, NW diameters, and NW-to-NW spacings. In the present work, we explore the fabrication of ordered arrays of Ga-polar²³ GaN NWs by SAS. We demonstrate that this method is suitable to simultaneously fabricate ordered arrays of NWs with various diameters and spacings on a single wafer. The sublimation process, carried out under ultra high-vacuum conditions, is further analyzed in situ by reflection high-energy electron diffraction (RHEED) and lineof-sight quadrupole mass spectrometry (QMS). A detailed analysis of the decomposition process allows us to assess the temperature dependence of the thermal etching rate, a crucial factor to control the formation by SAS of NWs as well as other types of nanostructures. Finally, we investigate the excitonic transitions from an ordered array of GaN NWs by lowtemperature cathodoluminescence spectroscopy (CL). The emission from the array is found to be more intense than that from the original layer, confirming that the sublimation process does not introduce any nonradiative defects at the NW sidewalls, and revealing an enhanced extraction efficiency of the NW array.

Experimental

As substrates for SAS, we use commercially available 2" GaN(0001) layers grown by hydride vapor phase epitaxy on Al₂O₃ (purchased from Suzhou Nanowin Science and Technology). The thickness of the GaN layer is (4.5 \pm 0.5) μm and its TD density 2×10^8 cm⁻². The backside of the substrates is coated with a 1.3 µm thick Ti layer for efficient thermal coupling during the sublimation process. To produce the mask for SAS, a 20 nm thick Si_xN_y layer is first sputtered on the substrate surface. Afterwards, the Si_xN_y layer is spin-coated with a 100 nm thick

positive resist. Subsequently, the pattern is written by electron beam lithography (EBL) in a RAITH system. The pattern comprises $100 \times 100 \ \mu\text{m}^2$ fields containing hexagonal arrays of circles. Upon writing the pattern, the resist is developed before depositing 25 nm of Ni. After a lift-off step, the substrates are diced in $10 \times 10 \text{ mm}^2$ pieces and the Si_rN_y layer is etched at the regions not protected by Ni using RIE with a CF₄/O₂ gas mixture. Finally, the pieces are further etched by ICP-RIE using a mixture of Ar, Cl₂ and BCl₃ gases. This final etching step is used to eliminate residual surface contaminants created after etching Si_xN_y as well as upon the chemical removal of the remaining Ni laver.

The sublimation of the GaN layer is carried out under ultra high-vacuum conditions in a molecular beam epitaxy system where the process can be monitored in situ by RHEED and OMS. The substrate temperature is measured with an emissivity corrected infrared optical pyrometer from Laytec. Customized Python routines are used to extract the angular intensity profiles around selected GaN diffraction spots of RHEED patterns. Because GaN decomposes congruently in vacuum,24 we employ QMS to directly assess the decomposition rate by measuring the desorbing Ga flux. The QMS system response to the Ga⁶⁹ signal is calibrated in units of atoms per cm² per s using the procedure described in ref. 25. Regardless of the surface morphology, we assume that Ga atoms desorb isotropically.

After the sublimation process, the samples are investigated by scanning electron microscopy (SEM) and low-temperature (14 K) CL spectroscopy and imaging. Scanning electron micrographs are acquired using either a Hitachi S-4800 or a Zeiss Ultra55 field-emission microscope. CL measurements are performed at acceleration voltages of 5 kV using a Gatan MonoCL4 system mounted to the Zeiss Ultra55 microscope. The system is equipped with a parabolic mirror for light collection and with both a photomultiplier and a charge-coupled device for detection.

3 Results and discussion

Top-down fabrication of ordered arrays of GaN nanowires

Ordered arrays of GaN NWs with different diameters and spacings in between NWs are produced simultaneously in an individual sample using a single sublimation step. Fig. 1 shows scanning electron micrographs of an exemplary NW array produced by annealing a patterned substrate at 825 °C for 20 min. The Si_xN_y patches used as a mask for the sublimation process can be distinguished at the top of the GaN NWs by their darker contrast. The nominal NW diameter and spacing (distance between adjacent patches) values for this particular array are 90 and 100 nm, respectively. The final average NW diameter, which roughly corresponds to the actual size of the Si_xN_y patches, is about 80 nm. As can be observed in Fig. 1(a), the NW array is homogeneous on a large scale. Inside the patterned region, the NWs have rather vertical sidewalls [see Fig. 1(b)]. The NW side facets are, however, not as flat and well defined as in the case of self-assembled GaN NWs produced by conventional bottom-up growth approaches.3,26-31 Specifically,

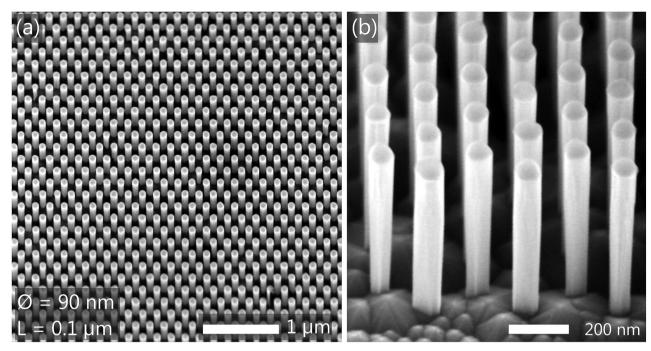


Fig. 1 (a and b) Bird's eye-view scanning electron micrographs with different magnifications of a GaN NW array produced by selective area sublimation. The nominal values of the NW diameter (\emptyset) and the spacing (L) are 90 nm and 0.1 μ m, respectively. The micrograph shown in (b) is acquired at one of the edges of the patterned region.

we do not observe pronounced M-plane sidewall facets. The NW sidewalls are instead rather roundish, most likely because the Si_rN_v patches do not have a hexagonal but a circular shape (a representative scanning electron micrograph of the Si_xN_v patches before the thermal sublimation process is provided as ESI†).

Fig. 2 presents scanning electron micrographs of four additional NW arrays produced at the same time as the one shown in Fig. 1. These arrays differ from the previous one in either the NW diameter or the spacing. In all cases we observe the formation of homogeneous NW arrays with a very high yield. The yield only decreases due to the presence of TDs. The latter are easily recognized after the sublimation process because of the formation of hexagonal pits which are randomly distributed with a density of about 0.5×10^8 cm⁻², lower than the nominal TD density of the parent GaN layer $(2 \times 10^8 \text{ cm}^{-2})$. This discrepancy originates likely from the fact that the sublimation process, analogously to chemical etching,³² preferentially etches screw TDs. Note that according to the nominal TD density of the GaN layer, the average number of TDs per NW (estimated as the product of the TD density of the original GaN layer and the NW cross-sectional area15) amounts to 0.004 to 0.012 when the NW diameter is varied from 50 to 90 nm. Consequently, even for the array with the larger NW diameter, approximately 99% of the NWs are expected to be free of TDs. 18 The results shown in Fig. 1 and 2 demonstrate that homogeneous arrays of GaN NWs with various diameters and spacings can be obtained with identical sublimation parameters.

As can be seen in Fig. 2(c) and (d), the morphology of the initially flat GaN layer in between the Si_xN_y patches is clearly

altered during the sublimation process. Fig. 3(a) presents a highly magnified scanning electron micrograph of an unpatterned GaN(0001) layer decomposed for 20 min at 825 °C. This micrograph reveals that the original (0001) surface gives way to the formation of three-dimensional islands with a six-fold symmetry and well-defined semi-polar facets. This result is in apparent contrast to the step-edge and layer-by-layer decomposition mechanisms reported in ref. 33 and 34, respectively. However, in these previous studies, where the decomposition process was analyzed at different temperatures, the GaN layer was not continuously decomposed (as in the present case), but a smooth (0001) surface was recovered prior to each (comparatively brief) decomposition step by depositing a thin GaN layer under conditions favoring step-flow growth. As a matter of fact, in ref. 33 it is also noted that continuous decomposition can result in surface faceting, a phenomenon that was tentatively ascribed to an enhanced sublimation rate near defects or/and grain boundaries. In the present study, the faceting under continuous decomposition is also detected in situ by RHEED. Fig. 3(b) and (c) present the RHEED patterns recorded along the [1120] and [1100] azimuths, respectively. In both azimuths, a transmission pattern is observed, accompanied by pronounced chevrons. These features, caused by the refraction and transmission diffraction of electrons entering and exiting crystal facets, can be used to derive the shape of the threedimensional objects from which they originate.35-40 As discussed in detail in the ESI,† the analysis of the vertex angles allows us to conclusively conclude that, in agreement with the results reported by Damilano et al. in ref. 17, the facets of the islands seen in Fig. 3(a) are formed by $\{1\bar{1}03\}$ planes.

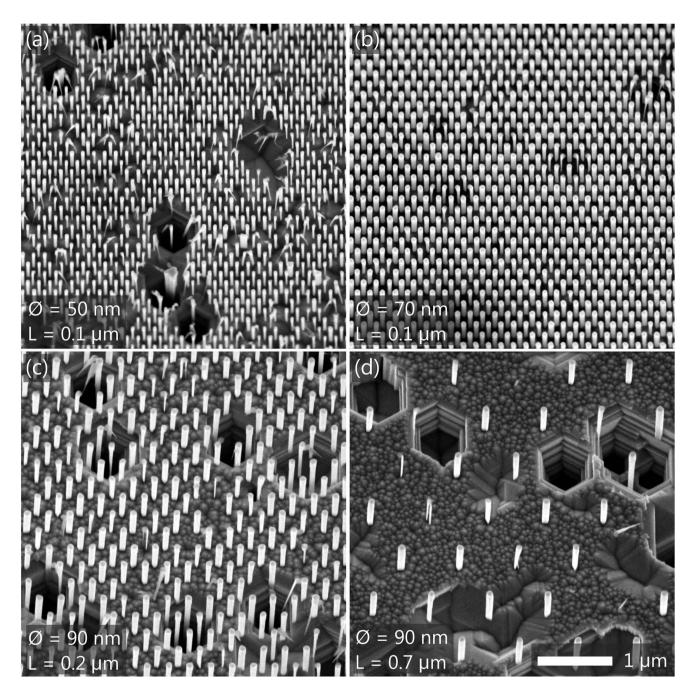


Fig. 2 (a–d) Bird's eye view scanning electron micrographs of NW arrays produced by selective area sublimation in the same run with varying nominal NW diameter (Ø) and spacing (L) values, as indicated in their corresponding labels. The magnification is the same for all micrographs and the scale bar is indicated in (d).

3.2 Thermal decomposition of GaN $\{1\bar{1}03\}$ facets: temperature dependence

To properly control the fabrication of nanostructures by SAS, it is essential to know the precise temperature dependence of the GaN decomposition rate in vacuum. The decomposition of GaN(0001) layers has been measured as a function of the temperature by different groups. $^{33,34,41-44}$ However, as discussed in Section 3.1, the (0001) surface is unstable against the formation of $\{1\bar{1}03\}$ facets, which are expected to decompose with a different rate. Hence, we next analyze the temperature

dependence of the decomposition rate of $\{1\bar{1}03\}$ facets by measuring *in situ* the desorbing Ga flux at different substrate temperatures.

Fig. 4(a) shows the desorbing Ga flux as measured by QMS during the congruent thermal decomposition of an unpatterned 2'' GaN wafer in vacuum. As shown in the graph, the substrate temperature T is first increased up to 870 °C with a rate of 20 K min $^{-1}$. Afterwards, the temperature is decreased in steps down to 786 °C. To assess the desorbing Ga flux under-steady state conditions, we wait for the stabilization of

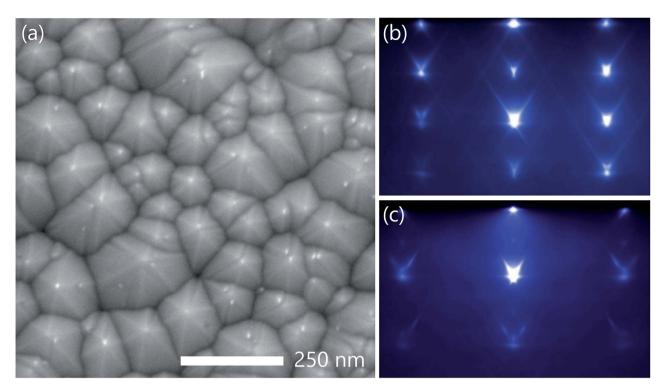


Fig. 3 (a) Bird's eye view scanning electron micrograph of a GaN layer after 20 min of annealing in vacuum at 825 °C. RHEED patterns acquired along the (b) [1120] and (c) [1100] azimuths after the thermal decomposition process.

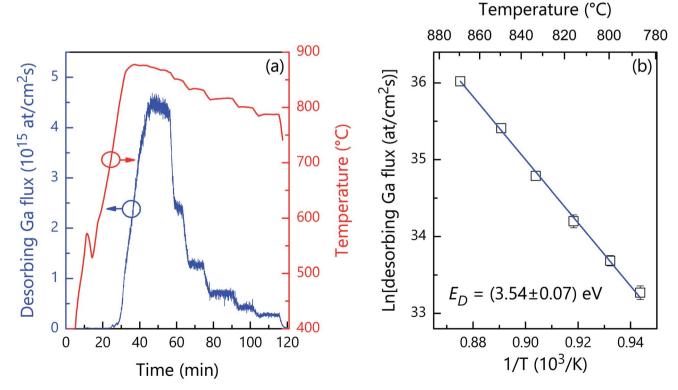


Fig. 4 (a) Change of substrate temperature and the resulting temporal evolution of the desorbing Ga flux (red and blue lines, respectively) during the decomposition of a GaN layer. (b) Arrhenius representation of the temperature dependence of the desorbing Ga flux during the decomposition of a $\{1\overline{1}03\}$ -faceted GaN layer. The line shows the best fit that yields an activation energy of $E_D = (3.54 \pm 0.07)$ eV.

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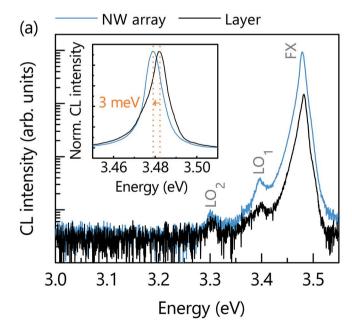
the substrate temperature after every temperature step. The measurements evidence that the desorbing Ga flux steadily decreases from 4.4×10^{15} cm⁻² s⁻¹ (≈ 60 nm min⁻¹) at 870 °C down to $0.3 \times 10^{15} \ \text{cm}^{-2} \ \text{s}^{-1} \ (\approx 4 \ \text{nm min}^{-1})$ at 786 °C. The Arrhenius plot of the steady-steady state values of the decomposition rate of $\{1\bar{1}03\}$ facets (Φ_D) is presented in Fig. 4(b). A fit of the data by

$$\Phi_{\rm D} = A \exp(-E_{\rm D}/k_{\rm B}T),\tag{1}$$

with the Boltzmann constant $k_{\rm B}$ yields a prefactor A of 1.8 imes 10 31 atoms per cm² per s and an activation energy $E_{\rm D}$ of (3.54 \pm 0.07) eV. When taking into account the increased surface area associated to the faceting, we find that the actual value of the exponential prefactor describing the thermal decomposition rate of $\{1\overline{1}03\}$ facets is reduced by a factor $(\tan(\theta)^2 + 1)^{1/2}$, where θ is the angle between the normal vectors of the $\{1\bar{1}03\}$ and (0001) planes (32.0°). The actual value of the exponential prefactor is thus 1.58×10^{31} atoms per cm² per s. The corresponding values of E_D and A measured by QMS in the case of a GaN(0001) plane are (3.1 \pm 0.1) eV and 1.17 \times 10²⁹ atoms per cm² per s.³⁴ Consequently, even though the energy barrier for thermal decomposition is slightly higher in the case of the $\{1\bar{1}03\}$ facets, in the temperature range of interest, these facets decompose faster than the (0001) one due to the much higher exponential prefactor. These results are consistent with the idea that, while during growth the morphology of the crystal is governed by slow growing facets, during dissolution/thermal decomposition the crystal shape is dominated by fast desolving/decomposing crystal facets. 45,46

3.3 Luminescence from ordered arrays of GaN nanowires produced by selective area sublimation

The luminescence from ordered arrays of GaN NWs is investigated by CL spectroscopy at 14 K. Fig. 5(a) presents the near band-edge CL spectrum of the NW array shown in Fig. 1. For comparison, we have also included a second CL spectrum recorded on an unpatterned region of the same sample. Both spectra are dominated by a high-energy line originating from the radiative decay of free excitons (FX), and its first- and second-order longitudinal-optical phonon replica (LO) at lower energies. The dominance of free over bound exciton recombination in this specific experiment is due to both the comparatively high excitation density (on the order of several 10¹⁷ cm⁻³) and the resulting high effective carrier temperature [amounting to about 60 K for the spectra in Fig. 5(a), as determined by a fit to the high-energy slope of the free exciton line]. Two observations are worth to be stressed. First, the luminous intensity measured from the NW array is notably higher than that from the adjacent parent layer. This observation shows, most importantly, that the sublimation process does not degrade the internal quantum efficiency of the structure created, and furthermore, that the extraction efficiency of light is enhanced significantly over that of the planar reference.7,8 Second, the spectrum of the NW array is rigidly red-shifted by 3-4 meV in comparison to the planar reference, as shown in the inset of Fig. 5(a) for the FX transition. This redshift results from the elastic relaxation of the residual compressive strain in the parent GaN(0001) layer grown on Al₂O₃. The same phenomenon, benefiting from the large surface-to-volume ratio of these nanostructures, was observed



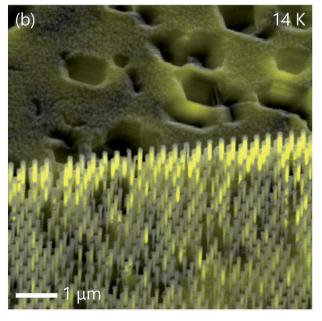


Fig. 5 (a) Near band-edge CL spectrum recorded at 14 K in bird's eye view geometry of the GaN NW array with a nominal NW diameter of 90 nm and a spacing of 0.1 μm. For comparison, the CL spectrum from the GaN layer surrounding the NW array (acquired under identical conditions) is also included. The emission from free excitons and its corresponding first and second order phonon replicas are labeled as FX, LO₁, and LO₂, respectively. The inset presents the normalized CL spectra of the NWs and the surrounding layer on a linear scale. (b) Superposition of a panchromatic CL intensity image recorded at 14 K (yellow) with the corresponding bird's eye view scanning electron micrograph for the same NW array as in (a).

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by different groups for pure GaN NWs as well as in the case of compressively strained (In,Ga)N quantum wells embedded into GaN NWs.^{14,17,19,47,48}

The CL spectra shown in Fig. 5(a) were acquired immediately after exposing the sample to the electron beam. After prolonged exposure, the CL intensity in the patterned areas invariably quenches strongly. As an example, Fig. 5(b) shows a panchromatic CL map superimposed with its corresponding bird's eye view scanning electron micrograph. The CL from the patterned region appears to be inhomogeneous with several NWs being apparently not emitting at all. However, this quenching of the CL intensity upon irradiation is solely the result of carbonaceous depositions that introduce nonradiative recombination channels at the NW sidewalls, as discussed in detail in ref. 49.

4 Summary and conclusions

We have demonstrated the fabrication of ordered arrays of GaN NWs by SAS of pre-patterned GaN(0001) layers grown on Al₂O₃. In a single sample, we simultaneously produced different arrays with NW diameters and spacings ranging from 50 to 90 nm and 0.1 to 0.7 µm, respectively. The resulting NW sidewalls are vertical, but do not exhibit well-defined M-plane facets. The roundish shape of the NWs is attributed to the use of SixNv patches with a circular shape as a mask for SAS. According to ref. 17, we expect the formation of M-plane sidewall facets when employing patches with a hexagonal shape properly oriented with respect to the GaN template underneath. During the sublimation process, the (0001) surface vanishes giving way to the formation of fast decomposing semi-polar $\{1\bar{1}03\}$ facets. The stability of these facets determines the thermal etching rate. We found that the $\{1\bar{1}03\}$ facets decompose following an Arrhenius-like temperature dependence with an activation energy of (3.54 \pm 0.07) eV and an exponential prefactor of 1.58×10^{31} atoms per cm² per s. Low-temperature CL experiments reveal a higher luminous intensity from the NW array thanks to an improved light extraction efficiency. The emission is red-shifted with respect to the one of the GaN layer because the large NW aspect ratio facilitates the elastic relaxation of residual strain. Although our CL experiments indicate that the sublimation process does not generate nonradiative recombination centers at the NW sidewalls, more conclusive results in this respect could be obtained by time-resolved photoluminescence spectroscopy. Such an analysis would be possible for GaN NWs containing (In,Ga)N quantum wells, for which the luminescence signals from the NWs and the GaN layer underneath would not spectrally overlap.

Selective area sublimation is, therefore, a suitable top-down approach to produce ordered arrays of GaN NWs with high luminous efficiency without the need of any elaborate chemical treatment. This fabrication method could be readily extended to other types of micro- and nanostructures as well as to additional material systems provided that they decompose congruently (to avoid the accumulation of constituent elements on the sample surface) and exhibit a marked anisotropy in the stability of its crystallographic planes. A prominent candidate for such experiments is ZnO, another wide-bandgap semiconductor of

interest for optical applications that shares many properties with GaN.

Conflicts of interest

There are no conflicts to declare.

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