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# Characteristics of atomic layer deposited Gd<sub>2</sub>O<sub>3</sub> on n-GaN with an AlN layer

Hogyoung Kim, 10 \*\* Hee Ju Yun and Byung Joon Choi\*\*

The interfacial and electrical properties of atomic layer deposited  $Gd_2O_3$  with an AlN layer on n-GaN were investigated. According to X-ray photoelectron spectroscopy spectra, the formation of Ga-O bonds that is significant near the  $Gd_2O_3/GaN$  interface was suppressed near the  $AlN/Gd_2O_3/GaN$  and  $Gd_2O_3/AlN/GaN$  interfaces. Larger amounts of oxygen atoms across the dielectric layers were observed for  $AlN/Gd_2O_3/GaN$  and  $Gd_2O_3/AlN/GaN$  junctions, which in turn produced the dominant peak corresponding to O-Al bonds. The flatband voltage shift in capacitance-voltage hysteresis characteristics was highest for the  $Gd_2O_3/AlN/GaN$  junction, indicating the highest interface and oxide trap densities. In addition,  $AlN/Gd_2O_3/GaN$  and  $Gd_2O_3/AlN/GaN$  junctions showed the highest interface state densities in the energy ranges of O.1-O.2 eV and O.4-O.6 eV, respectively. The reverse leakage currents were explained by Fowler-Nordheim (FN) for  $Gd_2O_3/GaN$  and  $AlN/Gd_2O_3/GaN$  junctions and by trap assisted tunneling (TAT) for the  $Gd_2O_3/AlN/GaN$  junction.

#### 1. Introduction

Among rare earth (RE) oxide based materials, gadolinium oxide ( $\mathrm{Gd_2O_3}$ ) has been considered as a promising material for application in next-generation complementary metal–oxide-semiconductor (CMOS) transistors, because  $\mathrm{Gd_2O_3}$  has a wide band gap ( $\sim$ 6.0 eV) with permittivity values of 10–14 for the epitaxial cubic phase, optimum band offset values with respect to Si, and high thermodynamic stability.<sup>1-3</sup>  $\mathrm{Gd_2O_3}$  has also been considered as a gate oxide in Ge-based MOS devices<sup>4</sup> and as a passivation layer for GaAs-based electronic devices.<sup>5</sup> Upon subjection to a forming gas treatment, the atomic layer deposition (ALD) grown  $\mathrm{Gd_2O_3}$  layer showed promising dielectric behavior, with no hysteresis and reduced interface trap density.<sup>6</sup>

Plasma-enhanced chemical vapor deposition (PECVD)-grown  $SiN_x$  has been used as an effective passivation material to suppress the current collapse in AlGaN/GaN metal–insulator-semiconductor heterostructure field-effect transistor (MIS-HFET).<sup>7</sup> High-k dielectric oxides such as  $Al_2O_3$  and  $HfO_2$  have also been employed as a passivation layer in AlGaN/GaN high electron mobility transistors (HEMTs).<sup>8,9</sup> However, it was shown that high density of deep (and slow) interface states exist near the  $Al_2O_3/(Al)GaN$  interface.<sup>10,11</sup> Hence, an alternative passivation method using an AlN layer has been considered for GaN-based devices.<sup>12–14</sup> It was demonstrated that the plasma enhanced atomic layer deposition (PEALD)-AlN as interfacial

passivation layer is beneficial to improve the quality of Al<sub>2</sub>O<sub>3</sub>(HfO<sub>2</sub>)/InGaAs interface, featuring good electrical characteristics and low interface trap density. <sup>15,16</sup> Wu *et al.* also observed similar passivation effect in Al<sub>2</sub>O<sub>3</sub>/GaN structure using a 2 nm thick AlN. <sup>17</sup> Due to the easy crystallization of AlN, however, the reverse leakage current become high when employing an AlN layer in the dielectric layer.

In our previous work, we found that the tunneling barrier height in  $Al_2O_3/GaN$  interface with a 1 nm thick AlN interlayer was lower than 1.0 eV, which could be due to the inferior  $Al_2O_3$  film quality. We also observed in Pt/AlN/n-GaN junctions with different AlN thicknesses that the formation of AlN layer near the AlN/GaN interface was observed clearly when the AlN thickness is above 1.5 nm. These suggest that at least 2 nm thick AlN grown on GaN will be beneficial to passivate the GaN surface and to improve the overgrown film quality. Although molecular beam epitaxy (MBE) grown  $Gd_2O_3$  on GaN has been investigated, there is little knowledge regarding the interface characteristics of ALD- $Gd_2O_3$  on GaN. In this work, we comparatively investigated the interfacial and electrical properties of  $Gd_2O_3$  on GaN with an AlN layer.

## 2. Experimental

Hydride vapor phase epitaxy (HVPE)-grown, n-type, c-plane (0001) GaN substrate (thickness: 300  $\mu$ m, carrier concentration:  $2 \times 10^{17}$  cm<sup>-3</sup>) purchased from Lumistal (Korea) was used in this work. After cutting the wafer into small pieces, some of them were loaded into a thermal ALD chamber (Atomic classic, CN-1, Korea) after cleaning process in a HCl: H<sub>2</sub>O (1:1) solution, and three different methods to

<sup>&</sup>lt;sup>a</sup>Department of Visual Optics, Seoul National University of Science and Technology (Seoultech), Seoul 01811, Republic of Korea. E-mail: hogyoungkim@gmail.com

<sup>&</sup>lt;sup>b</sup>Departmet of Materials Science and Engineering, Seoul National University of Science and Technology (Seoultech), Seoul 01811, Republic of Korea. E-mail: bjchoi@ seoultech.ac.kr

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Gd<sub>2</sub>O<sub>3</sub> 1.7 nm AIN 1.3 nm AIN 2.0 nm Gd<sub>2</sub>O<sub>3</sub> Gd<sub>2</sub>O<sub>3</sub> 1.7 nm 1.7 nm GaN substrate GaN substrate GaN substrate (C) (a) (b)

Fig. 1 Schematic layer structures of three samples denoted as (a) Gd<sub>2</sub>O<sub>3</sub>, (b) AlN/Gd<sub>2</sub>O<sub>3</sub> and (c) Gd<sub>2</sub>O<sub>3</sub>/AlN.

deposit dielectric layers were applied on the prepared GaN substrates. (i) 1.7 nm thick Gd<sub>2</sub>O<sub>3</sub> layer was directly deposited on the GaN surface at 250 °C with (RCp)<sub>2</sub>Gd(L) (Air Liquide, USA) as the Gd precursor and H<sub>2</sub>O as the oxidant (denoted as  $Gd_2O_3$ ). (ii) After depositing 1.7 nm thick  $Gd_2O_3$  on the GaN surface using the same method in (i), the temperature was ramped up to 350 °C to deposit 1.3 nm thick AlN layer using trimethylaluminum (TMA) and NH3 as precursors (denoted as AlN/Gd<sub>2</sub>O<sub>3</sub>). (iii) After depositing 2 nm thick AlN on the GaN surface using the same method in (ii), 1.7 nm thick Gd<sub>2</sub>O<sub>3</sub> was deposited using the same method in (i) (denoted as Gd<sub>2</sub>O<sub>3</sub>/ AlN). The schematic layer structures of three samples are presented in Fig. 1. The thicknesses of the films were measured using a FS-1 multi-wavelength ellipsometer (Film Sense, USA). Depth resolved X-ray photoelectron spectroscopy (XPS) measurements (PHI 5000 VersaProbe, ULVAC PHI, Japan) including sputter etch treatment were carried out using a monochromatic Al Ka X-ray source (beam energy: 1486.6 eV, spot size:  $100 \times 100 \text{ }\mu\text{m}^2$ ) to observe the chemical reaction near the dielectric/GaN interface. Due to charging effects that result in a shift in binding energy, the binding energy scale was calibrated by using the adventitious C 1s peak to 284.6 eV.

The collected XPS data were analyzed using Multipak software. To characterize the electrical properties, metal–insulator–semiconductor (MIS) diodes were fabricated with a Pt Schottky electrode (diameter:  $500 \mu m$ , thickness: 50 nm) and an Al back contact (thickness: 100 nm). Capacitance–voltage (C–V) and current–voltage (I–V) measurements were performed using a HP 4284A LCR meter and Keithley 238 current source, respectively. During the C–V measurements, an AC voltage of 30 mV with various frequencies was applied to the device so that small signal conditions were realized.

#### Results and discussion

After selecting the narrow scanned XPS spectra at one etch depth (thickness of the remained AlN:  $\sim$ 1.0 nm), we compared them each other to characterize the chemical reaction near the GaN surface. Fig. 2(a) shows the Ga  $2p_{3/2}$  core-level spectra. The peaks at  $\sim$ 1117.1,  $\sim$ 1117.4, and  $\sim$ 1118.1 eV are associated with  $Ga_2O$ ,  $^{22}$   $Ga_2O_3$   $^{23}$  and GaN,  $^{24}$  respectively. The integrated intensity ratios of Ga–O/Ga–N were calculated to be 1.98, 0.52, and 0.38, respectively, for the samples with  $Gd_2O_3$ ,  $AlN/Gd_2O_3$  and  $Gd_2O_3/AlN$  layers. This indicates that the suppression of Ga–O

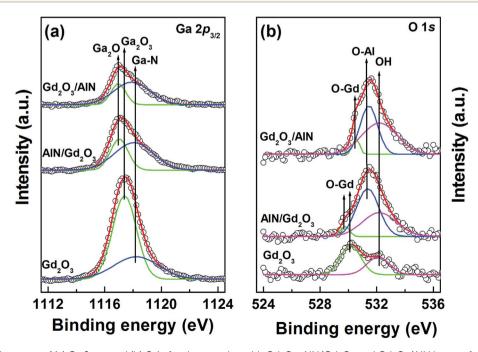


Fig. 2 XPS core level spectra of (a) Ga  $2p_{3/2}$  and (b) O 1s for the samples with  $Gd_2O_3$ ,  $AIN/Gd_2O_3$  and  $Gd_2O_3/AIN$  layers after sputter etching. The thick of remained layer was about 1 nm.

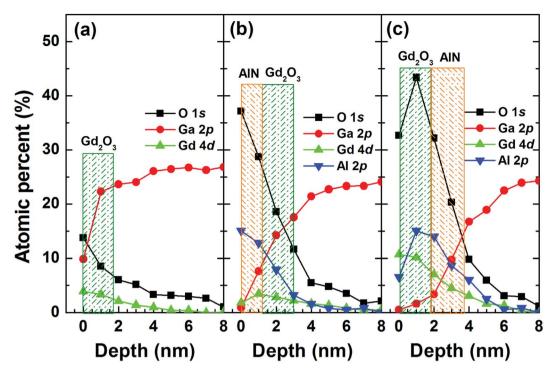


Fig. 3 XPS depth profiles for each element for the samples with (a)  $Gd_2O_3$ , (b)  $AIN/Gd_2O_3$  and (c)  $Gd_2O_3/AIN$  layers.

bond occurred more significantly with a Gd<sub>2</sub>O<sub>3</sub>/AlN layer. Not Ga<sub>2</sub>O<sub>3</sub> but Ga<sub>2</sub>O was mainly observed for the samples with AlN/ Gd<sub>2</sub>O<sub>3</sub> and Gd<sub>2</sub>O<sub>3</sub>/AlN layers, indicating that Ga<sub>2</sub>O<sub>3</sub> transferred into Ga<sub>2</sub>O with thicker layers (so called, cleaning effect). It was shown in (In)GaAs based devices that a stable suboxide Ga<sub>2</sub>O bond at the interface were not detrimental to device characteristics and the removal of Ga<sub>2</sub>O<sub>3</sub> resulted in the reduction of frequency dispersion in capacitance.<sup>25</sup> As shown in Fig. 2(b), all three samples revealed a peak at  $\sim$ 532.5 eV, corresponding to O in the H<sub>2</sub>O, OH<sup>-</sup> impurities<sup>26</sup> and Gd hydroxides.<sup>27</sup> The emission peak at 529.8-530.2 eV could be associated with O in Gd<sub>2</sub>O<sub>3</sub>,<sup>27</sup> which became weak for the samples with AlN/Gd<sub>2</sub>O<sub>3</sub> and Gd<sub>2</sub>O<sub>3</sub>/AlN layers. The integrated intensity ratios of [OH + O-Al]/O<sub>Total</sub> were found to be 0.40, 0.95, and 0.95, respectively, for the samples with Gd<sub>2</sub>O<sub>3</sub>, AlN/Gd<sub>2</sub>O<sub>3</sub> and Gd<sub>2</sub>O<sub>3</sub>/AlN layers. It was shown that Al-OH defects commonly observed in ALD-Al<sub>2</sub>O<sub>3</sub> films is the origin of acceptor-like border traps and positive fixed charges in Al<sub>2</sub>O<sub>3</sub>.<sup>28</sup>

Fig. 3 shows the XPS depth profiles for each element obtained from the samples. The diffusion of Ga atoms into the overgrown layer was significant. The amount of oxygen atoms near the GaN surface is similar. However, both the samples with AlN/Gd<sub>2</sub>O<sub>3</sub> and Gd<sub>2</sub>O<sub>3</sub>/AlN layers showed the larger amount of oxygen atoms across the overgrown layer as compared to the sample with Gd<sub>2</sub>O<sub>3</sub> layer. This indicates that a significant portion of it resulted from the ALD deposition process itself. Fig. 4 shows the narrow scanned core level spectra from the sample surfaces (i.e., without sputter etching). As shown in Fig. 4(a), the OH related peak observed for the samples with Gd<sub>2</sub>O<sub>3</sub> and AlN/Gd<sub>2</sub>O<sub>3</sub> layers is not observed for the sample with a Gd<sub>2</sub>O<sub>3</sub>/AlN bilayer. Along with the relatively weak peak at

~530.2 eV corresponding to O in Gd<sub>2</sub>O<sub>3</sub>, the additional peak at ~528.9 eV also appeared for the samples with Gd<sub>2</sub>O<sub>3</sub> and Gd<sub>2</sub>O<sub>3</sub>/AlN layers, which could be associated with more oxidized Gd-O due to excess O and lattice defects.29 Fig. 4(b) shows the Gd 4d core level spectra, which mainly originated from multiplet splitting of the 4d hole with 4f valence electrons to form 9D and 7D final ionic state.30 As shown in Fig. 4(c), the peak related with AlN at 73.6-73.9 eV 31 is dominant for the sample with a Gd<sub>2</sub>O<sub>3</sub>/AlN bilayer whereas the peak associated with Al-O at ~74.6 eV 31 is dominant for the sample with an AlN/Gd<sub>2</sub>O<sub>3</sub> bilayer. The additional peak at ~75.2 eV with a Gd<sub>2</sub>O<sub>3</sub>/AlN bilayer can be conformed to the Al-O chemical bond of Al<sub>2</sub>O<sub>3</sub> or Al(OH)3.32

Fig. 5(a)-(c) show the C-V curves measured at 1 kHz and 1 MHz. The frequency dispersion is hardly visible in the deep depletion region and small frequency dispersion is observed in the depletion, indicating the presence of interface states. The capacitance in accumulation at 1 kHz is much higher than that at 1 MHz, which can be due to current leakage through the dielectric layer which distorts the 1 kHz measurement.33 The frequency dispersion in the accumulation region can also be associated with the formation of an inhomogeneous layer at the interface. Fig. 5(d)–(f) show the C–V hysteresis plots by sweeping the voltage from deep depletion to accumulation (up) and then accumulation to deep depletion (down). From the flatband capacitance  $(C_{FB})$  obtained at each voltage sweep, the flatband voltage  $(V_{FB})$  was calculated and then, the average interface and oxide trap density  $(Q_T)$  along the GaN bandgap  $(E_g)$  were obtained through  $Q_{\rm T} = (C_{\rm OX} \Delta V_{\rm FB})/q E_{\rm g}^{34}$  The trapped charge densities were calculated to be  $3.68 \times 10^9$ ,  $1.82 \times 10^{11}$ , and 1.83 $\times$  10<sup>12</sup> cm<sup>-2</sup> eV<sup>-1</sup>, respectively, for the samples with Gd<sub>2</sub>O<sub>3</sub>, AlN/

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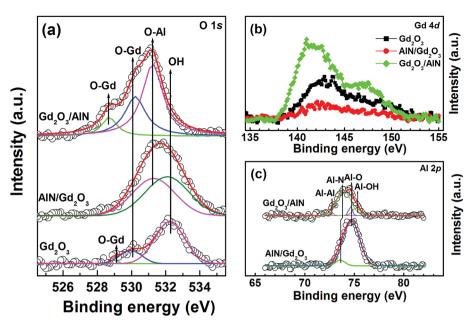


Fig. 4 XPS core level spectra of (a) O 1s, (b) Gd 4d and (c) Al 2p, which were obtained from the surface

 ${\rm Gd_2O_3}$ , and  ${\rm Gd_2O_3}/{\rm AlN}$  layers. The hysteresis can result from the interface states and oxide traps in the dielectric layer. Oxygen related defects are generated much with including an AlN layer (for example, see Fig. 2(b)). High density of Al–OH defects acting as acceptor-like border traps and positive fixed charges in  ${\rm Al_2O_3}$  may also increase the  $V_{\rm FB}$  shift. Fig. 5(f) also shows that the C-V curves reveal a plateau region, reflecting the presence of 2 dimensional electron gas (2DEG) at the AlN/GaN interface. With increasing the bias voltage, electrons start to accumulate at the channel interface, thus forming the 2DEG. An increase in capacitance is also observed at gate bias beyond +5 V. This is due to a charge spillover from the 2DEG channel, which brings

some of the carriers closer to the surface, decreasing the effective barrier layer thickness and increasing the capacitance.<sup>37</sup>

Frequency dependent conductance–voltage measurements were performed to differentiate the types of the traps. The parallel conductance  $(G_{\rm p}/\omega)$  can be derived by  $G_{\rm p}/\omega = [\omega G_{\rm m}(C_{\rm OX})^2]/[(G_{\rm m})^2 + \omega^2(C_{\rm OX} - C_{\rm m})^2]$ , where  $C_{\rm m}$  is the measured capacitance,  $G_{\rm m}$  is the measured conductance, and  $C_{\rm OX}$  is the oxide capacitance. The  $G_{\rm p}/\omega$  values as a function of radial frequency  $(\omega = 2\pi f)$  can be correlated with the trap density  $D_{\rm T}$  and trap response time  $\tau_{\rm T}$ , given by<sup>38</sup>

$$\frac{G_{\rm p}}{\omega} = \frac{qD_{\rm T}}{2\omega\tau_{\rm T}}\,\ln\!\left[1 + \left(\omega\tau_{\rm T}\right)^2\right] \tag{1}$$

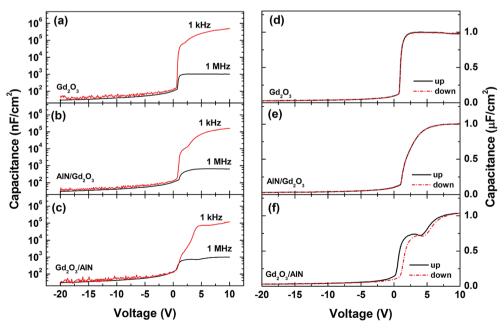


Fig. 5 (a)–(c) Capacitance–voltage (C–V) data measured at 1 kHz and 1 MHz and (d)–(f) C–V hysteresis plots measured at 1 MHz.

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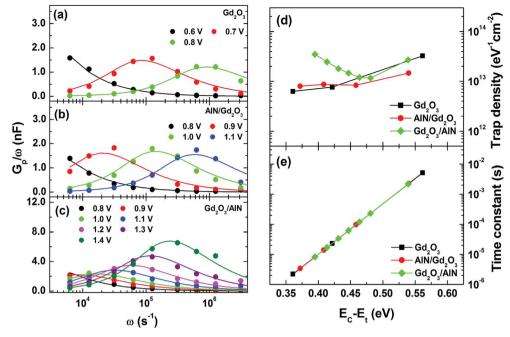


Fig. 6 Frequency dependent parallel conductance for the samples with (a)  $Gd_2O_3$ , (b)  $AlN/Gd_2O_3$  and (c)  $Gd_2O_3/AlN$ , and (d) the obtained trap density and (e) the time constant from the conductance method.

The experimental and fitting data are shown in Fig. 6(a)–(c). The energy level of traps below the GaN conduction band, denoted as  $E_{\rm C}$ – $E_{\rm t}$  (location of the trap energy) can be determined from  $\tau_{\rm T}$  according to the Shockley–Read–Hall statistics<sup>39,40</sup>

$$\tau_{\rm T} = \frac{1}{\nu_{\rm th} \sigma_{\rm n} N_{\rm C}} \exp\left(\frac{E_{\rm C} - E_{\rm t}}{kT}\right) \tag{2}$$

where  $\nu_{\rm th}$  is the thermal velocity,  $\sigma_{\rm n}$  is the electron capture cross section, and  $N_{\rm C}$  is the effective density of states in the GaN conduction band. Using the values presented in ref. 40, the trap density and time constant as a function of energy level were calculated, which are shown in Fig. 6(d) and (e), respectively. According to the calculated time constants, the samples with  ${\rm Gd_2O_3}$  and  ${\rm AlN/Gd_2O_3}$  layers showed the wider distributions compared to the sample with a  ${\rm Gd_2O_3/AlN}$  bilayer. Interestingly, the trap density at  $E_{\rm C}$ – $E_{\rm t}=0.38$ –0.47 eV showed the highest value for the sample with a  ${\rm Gd_2O_3/AlN}$  bilayer. Hence, these traps can be considered to be from the AlN layer in the  ${\rm Gd_2O_3/AlN/GaN}$  structure.

As shown in Fig. 7(a), an estimate of the interface state density  $(D_{\rm it})$  was made by applying Terman method to the experimental C-V curves measured at 1 MHz.<sup>38</sup> The obtained  $D_{\rm it}$  vs.  $E_{\rm C}-E_{\rm t}$  plots in Fig. 7(b) revealed the lowest interface state density for the sample with a  ${\rm Gd_2O_3}$  layer. Interestingly, the samples with  ${\rm AlN/Gd_2O_3}$  and  ${\rm Gd_2O_3/AlN}$  bilayers showed the highest interface state densities in the energy ranges of 0.1–0.2 eV and 0.4–0.6 eV, respectively. As shown in Fig. 3, large amount of Al atoms are present near the  ${\rm Gd_2O_3/GaN}$  interface for the sample with an  ${\rm AlN/Gd_2O_3}$  bilayer. This might lead to the formation of AlN, leaving many nitrogen vacancies at the GaN surface. Fang *et al.* reported that several kinds of deep levels can be increased at the GaN surface after  ${\rm Cl_2}$ -based inductively

coupled plasma (ICP) etching.41 In particular, they attributed a deep center located at  $E_C$  – 0.17 eV to nitrogen vacancy-related defect due to ICP etching. Yatabe et al. also showed in Al<sub>2</sub>O<sub>3</sub>/ AlGaN/GaN structure that the C-V curves in the depletion region are stretched out more significantly to the positive direction when the interface state density peaked at  $E_C$  – 0.1 eV is higher. 42 Significant stretch out to the positive direction and high interface state density in the range of 0.1-0.2 eV observed for the sample with an AlN/Gd<sub>2</sub>O<sub>3</sub> bilayer, thus, can be associated with the nitrogen vacancy-related defects. Meanwhile, Freedsman et al. showed in AlN/AlGaN/GaN MIS diodes that the trap density due to the AlN and AlGaN layers were located at 0.4-0.52 and 0.32-0.34 eV, respectively.43 The energy distribution corresponding to the AlN insulating layer is similar to our work at the Gd<sub>2</sub>O<sub>3</sub>/AlN interface. Furthermore, Fig. 7(a) shows that flatband voltage shifted negatively for the sample with a Gd2O3 layer as compared to the sample with an AlN/Gd<sub>2</sub>O<sub>3</sub> bilayer. It was shown that when Gd can only exist in the +3 oxidation state, the Gd<sub>2</sub>O<sub>3</sub> cannot supply oxygen effectively to the dielectric/Si interface, which produced oxygen vacancies causing the negative shift in flatband voltage.44 As shown in Fig. 2(b), the strong Gd<sub>2</sub>O<sub>3</sub> related emission observed for the sample with a Gd<sub>2</sub>O<sub>3</sub> layer was not observed for the sample with an AlN/Gd2O3 bilayer. Instead, the emission related with more oxidized Gd-O bond was observed. Therefore, it can be expected that oxygen vacancies have been reduced after depositing AlN layer with the

Fig. 8(a) shows the typical semi-logarithmic current density-voltage (*J-V*) curves measured at room temperature. Compared to the sample with a Gd<sub>2</sub>O<sub>3</sub> layer, the reverse leakage current increased for the samples with AlN/Gd<sub>2</sub>O<sub>3</sub> and Gd<sub>2</sub>O<sub>3</sub>/AlN layers. Forward current at high bias region increased most

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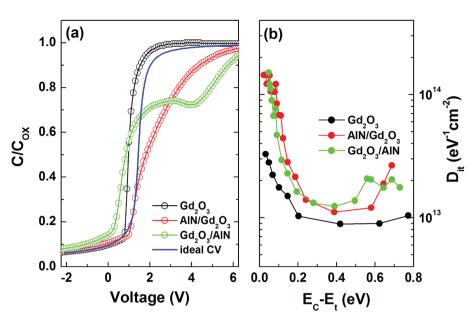


Fig. 7 (a) Comparison of experimental capacitance–voltage (C-V) data measured at 1 MHz and ideal C-V data and (b) interface state density  $(D_{it})$  distributions determined by applying Terman method.

slowly for the sample with a  $\rm Gd_2O_3/AlN$  bilayer, indicating the high series resistance. The reverse leakage current density at high bias region were analyzed using the Fowler–Nordheim (FN) tunneling model, given by 45

$$J \propto E^2 \exp(-\beta/E) \tag{3}$$

where  $\beta = 6.83 \times 10^{-7} (m^*)^{1/2} (\Phi_{\rm B})^{3/2}$ ,  $m^*$  is the effective electron mass in the insulator and  $\Phi_{\rm B}$  is the tunneling barrier height. As shown in Fig. 8(b), FN emissions were observed for the samples

with  $Gd_2O_3$  and  $AlN/Gd_2O_3$  layers. The tunneling barrier heights (*i.e.*, conduction band offset at  $Gd_2O_3/GaN$ ) were calculated to be about 1.95 and 1.36 eV, respectively, for the samples with  $Gd_2O_3$  and  $AlN/Gd_2O_3$  layers. Considering the reported value of 1.9 eV in  $Gd_2O_3/GaN$  interface, <sup>46</sup> the value for the sample with a  $Gd_2O_3$  layer is very similar. Relatively lower value for the sample with an  $AlN/Gd_2O_3$  bilayer may be due to the poorer interfacial quality as well as the interface and oxide traps.

Fig. 8(b) also shows that no FN emission was observed for the sample with a Gd<sub>2</sub>O<sub>3</sub>/AlN bilayer. Instead of FN emission,

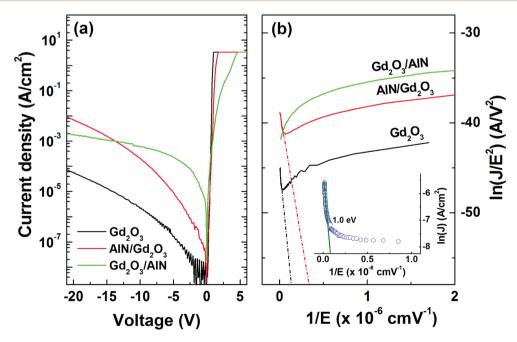


Fig. 8 (a) Typical semi-logarithmic current density-voltage (J-V) characteristics and (b) Fowler-Nordheim (FN) tunneling plots of  $\ln(J/E^2)$  vs. 1/E. The inset in (b) presents trap assisted tunneling (TAT) plot of  $\ln(J)$  vs. 1/E.

the trap assisted tunneling (TAT) was applied to the sample with a  $Gd_2O_3/AlN$  bilayer. In TAT emission, the reverse leakage current is given as  $J \propto \exp(-\gamma/E)$ , where  $\gamma = 6.83 \times 10^{-7} (m^*)^{1/2} (\Phi_t)^{3/2}$  and  $\Phi_t$  is the trap energy level, which can be obtained from the  $\ln(J)$  vs. 1/E plot. From the linear fittings to the data as shown in the inset in Fig. 8(b), the trap energy level was estimated to be 1.00 eV. Energetically most favorable point defects in AlN are substitutional oxygen for nitrogen  $(O_N)$  and aluminum vacancy  $(V_{Al})$ , with the energy levels of  $\sim 0.8$  and  $\sim 1.0$  eV, respectively. It was shown that dangling bonds in Al give rise to  $\sim 1.0$  eV in energy level. At present moment, it is suggested that Al related defects are responsible for TAT emission for the sample with a  $Gd_2O_3/AlN$  bilayer.

#### 4. Conclusions

In conclusion, we investigated the interfacial and electrical properties of atomic layer deposited Gd<sub>2</sub>O<sub>3</sub> with an AlN layer on n-GaN. X-ray photoelectron spectroscopy (XPS) spectra showed the dominant peaks associated with Ga-O and O-Al bonds, respectively, near the Gd<sub>2</sub>O<sub>3</sub>/GaN and AlN/Gd<sub>2</sub>O<sub>3</sub>/GaN and Gd<sub>2</sub>O<sub>3</sub>/AlN/GaN interfaces. Large amount of oxygen atoms across the dielectric layers were observed for the samples with Gd<sub>2</sub>O<sub>3</sub>/AlN and AlN/Gd<sub>2</sub>O<sub>3</sub> layers. The highest flatband voltage shift in C-V hysteresis characteristics were observed for the sample with a Gd<sub>2</sub>O<sub>3</sub>/AlN bilayer, indicating the highest interface and oxide trap densities. According to parallel conductance and Terman methods, the samples with AlN/Gd<sub>2</sub>O<sub>3</sub> and Gd<sub>2</sub>O<sub>3</sub>/ AlN layers were found to have the highest interface state densities in the energy ranges of 0.1-0.2 eV and 0.4-0.6 eV, respectively. The reverse leakage currents for the samples with Gd<sub>2</sub>O<sub>3</sub> and AlN/Gd<sub>2</sub>O<sub>3</sub> layers were explained by Fowler-Nordheim (FN) whereas trap assisted tunneling (TAT) was more appropriate for the sample with a Gd<sub>2</sub>O<sub>3</sub>/AlN bilayer.

#### Conflicts of interest

There are no conflicts to declare.

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