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Introduction

Molecular electronic devices can be divided into two classes: single molecule junctions¹⁻⁵ and junctions based on a layer of molecules with a thickness of exactly one molecule.⁶⁻⁸ Molecular junctions based on self-assembled monolayers (SAMs) are appealing because they operate in the tunneling regime and therefore have complementary properties with respect to conventional devices,^{6,9-11} and, unlike single molecule junctions,²⁻⁴ they can be fabricated on large scales and have found commercial applications,¹² and the SAM structure can be characterized in detail.^{9,13-15} These SAM-based junctions are

Bottom-electrode induced defects in selfassembled monolayer (SAM)-based tunnel junctions affect only the SAM resistance, not the contact resistance or SAM capacitance†

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In large area molecular junctions, defects are always present and can be caused by impurities and/or defects in the electrode materials and/or SAMs, but how they affect the electrical characteristics of junctions has rarely been studied. Usually, junctions are characterized by two-terminal current-voltage measurements where only the total current across the junction is measured, but with these methods one cannot distinguish how the individual components of the junctions are altered by the defects. Here we show that the roughness of the bottom-electrode is a crucial factor in determining the electrical properties of self-assembled monolayer (SAM)-based junctions. We used potentiodynamic impedance spectroscopy to reveal which components of the junctions are altered by defective bottom electrodes because this method allows for direct determination of all components that impede charge transport in the equivalent circuit of the junctions. We intentionally introduced defects via the roughness of the bottom electrode and found that these defects lower the SAM resistance but they do not alter the capacitance of the SAM or the contact resistance of the junction. In other words, defective junctions can be seen as "leaky capacitors" resulting in an underestimation of the SAM resistance of two orders of magnitude. These results help to improve the interpretation of data generated by SAM-based junctions and explain in part the observed large spread of reported tunneling rates for the same molecules measured across different platforms.

> usually fabricated by first forming a SAM on a metal bottomelectrode followed by the fabrication of a top-electrode on the SAM in a second step.14,16 Although most studies focus on optimizing the second step of the fabrication process,17-20 we²¹⁻²³ and Whitesides et al.²⁴ have shown that defects in the junctions induced by the defects in the bottom-electrode greatly affect the electrical properties of SAM-based junctions. For instance, by systematically investigating the electrical properties of SAM-based junctions as a function of the surface roughness of the bottom-electrode, we found that the quality of the tunneling barrier defined by the SAM,²² or the performance of a molecular diode,²¹ depends strongly on the topography of the bottom electrode (see below). Defect free junctions cannot be made and, to a certain degree, defects will be always present due to unavoidable imperfections caused by defects in the electrode materials, e.g., step-edges, grain boundaries, or impurities, or phase domains in the SAMs.^{21,22,24-26} We note that usually in single molecule experiments only ideal structures are considered and that the role of "defects" or non-ideal structures in these systems are difficult to determine.27-31 Thus understanding how defects affect the electrical properties of junctions, or cause junctions to fail, is important in the rational

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Fig. 1 Schematic illustration of an ideal junction without defects (a) and a junction with a disordered SAM on a defect in the bottom-electrode (b). (c) The equivalent circuit of the junction.

design of molecular junctions and interpretation of charge transport data they generate.

In previous works, we have only pointed out that defects are important and change the apparent values of β ,²² or introduce leakage currents reducing the function of molecular diodes,²¹ here we report which parts of the junctions are affected by defects revealed by impedance spectroscopy. The central question we wish to address here is: which part(s) of the junction, or more specifically, which circuit element(s), is (are) sensitive to defects inside the junction induced by the defects in the bottom-electrode and how will the presence of defects affect the interpretation of charge transport data obtained from SAMbased junctions? The key finding of this work is that imperfections in the bottom-electrode material only affect the SAM resistance whilst other observables associated with typical tunneling behavior (e.g., exponential decay of the current with molecular length) and good quality of the junctions (e.g., high yields), and other circuit components (the capacitance and contact resistance of the junctions) are not affected by defects. Our results show that defective junctions with bottomelectrodes obtained by commonly used direct deposition methods behave as "leaky capacitors" resulting in an underestimation of the SAM resistance by two orders of magnitude. Therefore, this work will be useful for future quantitative analyses of charge transport rates across platforms which has been notoriously difficult (see for a detailed review ref. 14).

Mostly, the electrical properties of SAM based-tunnel junctions are probed with DC conductance measurements which only determine the total current flowing across a junction as a function of applied bias.^{32–34} DC experiments alone cannot reveal the role of individual junction components (the two SAM–electrode interfaces and the SAM) and usually have to be combined with systematical studies involving changes of the electrode materials or chemical modifications to the SAMs.^{4,34–41} We note that here we measured the contact resistance directly, without the need for long extrapolations to n = 0, or fitting the low bias linear part of the f(V) curve. In previous works we have compared our values of the contact resistance, and that obtained by others,^{33,42} and recently we directly compared the contact resistance of EGaIn junctions and conductive probe atomic force microscopy based junctions.43 Here the major difference is that EGaIn junctions have a 1-2 orders of magnitude higher contact resistance due to the presence of the GaO_r layer which has indeed a 1-2 orders of higher resistance than bulk EGaIn^{33,42-44} Fig. 1 shows a SAM-based junction schematically along with an equivalent circuit that describes such a junction consisting of the contact resistance of the SAM $(R_{\rm C})$ with both electrodes in series with a parallel combination of the SAM resistance (R_{SAM}) and capacitance (C_{SAM}) .^{33,42} This equivalent circuit has been used to model other SAM-based junctions as well.45 We have shown before that frequency dependent impedance measurements make it possible to determine $R_{\rm C}$, R_{SAM} , and C_{SAM} , and thus to separate the individual components that contribute to charge transport from one and the other.33,46,47 As depicted in Fig. 1, the top-electrode in our studies was fabricated using the "EGaIn-technique" and consists of a eutectic alloy of Ga and In (EGaIn) coated with a self-limiting layer of predominantly Ga₂O₃ of 0.7 nm thick.^{33,48} Here, the top electrode forms an ohmic contact with the SAM and the resistance of the GaO_r layer and the SAM//top electrode contact (// is a van der Waals interface) are the main contributor to $R_{\rm C}$.^{33,42,48} The junction itself can be modelled as a parallel plate capacitor through which a tunneling current flows.23

The magnitude of the tunneling current density, J (in A cm⁻²), flowing across junctions with SAMs of, for instance, $S(CH_2)_{n-1}CH_3$ (or in short SC_n where n is the number of CH_2 units) is mostly analyzed with the general tunneling equation (eqn (1))

$$J = J_0(V) e^{-\beta d_{\text{SAM}}} = J_0(V) 10^{-\beta d_{\text{SAM}}/2.303}$$
(1)

where the value of J at a given bias is plotted against the thickness (or the length of the molecules) of the SAM, d_{SAM} (in n), and the graph is fitted to eqn (1). The slope of this fit gives the tunneling decay coefficient, β (n⁻¹), and the intercept with the *y*-axis gives the pre-exponential factor J_0 (in A cm⁻²). Recently, we have shown that the value of β decreases from 1.0 n⁻¹ (which is associated with a high quality tunnel junctions)

How defects alter the electrical properties of tunnel junctions have been often characterized in tunnel junctions based on metal oxides,⁵¹⁻⁵⁴ but not in SAM-based junctions. Based on eqn (1), defective sites in junctions can be classified into two categories: defects that increase (thick area defects) or decrease (thin area defects) the effective electrode-electrode distance.24,55 In EGaIn junctions, large parts within the junction area are thick area defects due to the surface roughness of the EGaIn top electrode;50 these areas do not contribute significantly to the measured value of I as the current flowing across such defect sites are very low. In contrast, the current flowing across thin area defects can be very high and even dominate the measured value of J⁵⁶⁻⁵⁹ and can be caused by, e.g., grain boundaries, chemi- or physisorbed impurities, or phase domains of the SAMs.^{21,23-26} Here, we used frequency dependent (ranging from 1 Hz to 1 MHz) impedance measurements on tunneling junctions with alkanethiolate SAMs (SC_n with n = 10, 14 or 18) formed on Ag substrates with different topographies. By varying the surface topography we effectively varied the root mean square (rms) surface roughness, the grain size, and area of grain boundaries (A_{gb}) . To capture all these variables in a single parameter, we used the bearing volume (BV) given by eqn (2) where $N_{\rm gr}$ is the number of grains.^{22,60}

$$BV = N_{gr} \times A_{gb} \times rms \tag{2}$$

In terms of BV values, the more defective the bottomelectrodes are, the higher the BV values are. Since we used a top-electrode with an area of $9.6 \times 10^2 \,\mu\text{m}^2$ for the electrical measurements of all the junctions, we effectively varied the number of defects inside the tunneling junction by changing BV. We found that defects inside the junctions only affect the SAM resistance, but not the capacitance or the contact resistance of the junction.

Experimental section

Preparation of Ag surfaces

We followed previously reported procedures to prepare the four types of Ag surfaces with different surface topographies.^{22,60} Generally, we used high grade silver with purity of 99.999%

(Super Conductor Material, Inc (USA)) and one-side polished silicon wafers (University Wafers, USA) as the template for templated-stripped (TS) Ag surfaces, or as substrates for Ag surfaces obtained by direct evaporation (DE). The Ag was deposited at a rate of 0.5 Å s^{-1} for the first 50 nm and followed by the deposition of 250 nm at a rate of 1 Å s^{-1} (at a base pressure of $2-5 \times 10^{-6}$ bar) in the case of Ag^{A-TS}, Ag^{TS}, and $Ag^{DE,1}$, surfaces. The $Ag^{DE,2}$ surfaces were obtained by deposition of Ag a rate of 4 Å s⁻¹. After deposition of Ag, the Ag^{DE,1} and Ag^{DE,2} substrates were used as such. To obtain the Ag^{A-TS} surfaces, after metal deposition the substrates were annealed at 200 °C (the temperature was increased from room temperature at a rate of 15 °C min⁻¹) for 30 min under vacuum inside the vacuum chamber of the thermal evaporate. After annealing, we cooled down the substrates to room temperature overnight inside the UHV chamber. We applied an optical adhesive (OA, Norland, no. 61) on the Ag substrates to glue the glass supports $(2 \times 3 \text{ cm}^2)$, cleaned by immersion into piranha solution (H₂- SO_4 : $H_2O_2 = 3: 1$) for 20 min, followed by washing with H_2O and dried in a stream of N2 gas. The size of the glass supports will be the final dimension of the bottom-electrode. Note, we used Ag surfaces in this study since we can generate surfaces with large changes in the BV values as reported previously.21,22,60,61 These glass supports were further cleaned with air plasma for 2 min and then glued to the metal Ag surfaces. We cured the OA/Ag/silicon substrates under UV light (100 watt using a ultraviolet light at a distance of 60 cm from the substrates to minimize heating) for 1 hour. After curing, the glass/AO/Ag stacks were stripped of the template just before use. The Ag^{TS} surfaces were obtained in the same way as described above to obtain the Ag^{A-TS} but without the annealing step.

Atomic force microscopy

Atomic force microscopy (AFM) images were recorded on a Bruker Dimension FastScan AFM by using tapping mode tips with intermittent contacts (model: FASTSCAN-A, resonant frequency: 14 MHz, and force constant: 18 N m⁻¹).

Preparation of the SAMs

We purified the *n*-alkanethiols (Sigma Aldrich) by recrystallization from ethanol (AR grade; three times) before use because their purity, as received, ranged from 95 to 98%.^{22,61} Since thiols can decompose and form disulfides in ambient conditions, we further purified the as received *n*-alkanethiols (Sigma-Aldrich) prior to use. The *n*-alkanethiols (n = 10, 14 or 18) were recrystallized from ethanol (AR grade) under an atmosphere of N₂ at -20 °C followed by a quick filtration. We repeated the recrystallization of each compound for 3 times, and we stored the purified thiols under N₂ at 4 °C. We followed a previously reported protocol to prepare the SAMs.²² First, we prepared 3 mM ethanolic solutions of each alkanethiol and purged the solutions with N₂ gas for at least 15 min prior to immersion of the Ag substrates. The SAMs were formed over a period of three hours in the ethanolic solutions under inert atmosphere to prevent oxidation of the M-S bond and avoid the formation of silver oxides. The $Ag^{\mathrm{DE},1}$ and $Ag^{\mathrm{DE},2}$ substrates were immersed into the solution immediately after they were removed from the vacuum chamber of the thermal evaporator and the Ag^{A-TS} and Ag^{TS} substrates were immersed into the solution immediately (within a few seconds) after template-stripping.

Fabrication of the junctions

We formed top-contacts on the SAM using $GaO_x/EGaIn$ confined in a through-hole in poly(dimethylsiloxane) (PDMS) using a method which has recently been reported by us.⁶¹ Here, the liquid-metal is injected in a micro-channel in PDMS; this microchannel was aligned over a through-hole which was connected to a second channel to which vacuum was applied so that the liquid-metal was forced into the though-hole. The slab of PDMS with the $GaO_x/EGaIn$ stabilized in the through-hole was gently placed on top of the SAM. After electrical characterization, we removed the top-electrode from the SAM to form a new junction. The same top-electrode could be used to form 20–25 junctions.

Electrical characterization of the junctions

The J(V) measurements were carried out using a Keithley 6430 source meter and data were acquired using LabView 2010 following previously reported methods.⁶¹ Before we started the impedance measurements, we determined the J(V) characteristics of the devices and recorded the values of J over the range of biases of -0.50 to 0.50 V at intervals of 20 mV. We selected those junctions that had their electrical characteristic within one log-standard deviation from the Gaussian log-mean values of J (which are reported in ref. 22) for the impedance measurements. All impedance measurements were repeated 3 times using 3 different junctions following previously reported procedures reported elsewhere. We measured the impedance of these junctions using a Solartron impedance analyzer (model Solartron 1260A with 1296A dielectric interface) in reference mode with a standard 10 pF capacitor as the external reference (see ref. 33 and 42 for all the details). The potentiodynamic impedance measurements were conducted by applying an AC voltage of 30 mV super-imposed on a DC bias voltage and the frequency was varied from 1 Hz to 1 MHz with 10 frequencies per decade. The geometrical junction areas was 9.6 \times 10² μ m² in all impedance measurements.

Results and discussion

The bottom electrodes

We fabricated four different types of bottom-electrodes with varying surface topographies to investigate how defects induced by the bottom-electrode alters the electrical characteristics, more specifically, the complex impedance of the junctions following previously reported methods (see experimental section for details).^{22,60} Fig. 2 shows the atomic force microscopy (AFM) images of the four Ag surfaces. These surfaces have different values of the root mean square (rms) surface roughness, number of grains ($N_{\rm gr}$), and area of exposed grain boundaries ($A_{\rm gb}$), Table S1[†] summarizes these parameters, and we used the bearing volume (BV in nm³; eqn (2)) to compare the topography of these bottom-electrodes. Low BV values indicate the surface is smooth and has low density of defects, while large BV values indicate the surface is dominated by the defects. In agreement with our previous



Fig. 2 AFM images of the Ag^{A-TS} (a), Ag^{TS} (b), Ag^{DE,1} (c), and Ag^{DE,2} (d) bottom electrodes.



Fig. 3 The arithmetic average J(V) characteristics of the junction with SAMs of SC₁₀, SC₁₄, and SC₁₈, supported by Ag^{A-TS} (a), Ag^{TS} (b), Ag^{DE,1} (c), or Ag^{DE,2} (d), bottom electrodes. The error bars represent the standard deviation obtained from three different junctions.

work, Ag^{A-TS} and Ag^{TS} surfaces have low BV values of 8×10^4 nm³ and 1×10^5 , respectively, while that of the $Ag^{DE,1}$ and $Ag^{DE,2}$ surfaces are more than two orders of magnitude larger ($\sim 2 \times 10^6$ nm³ and $\sim 3 \times 10^6$ nm³, respectively).

This large difference in the BV values is also visible by eye in Fig. 2. The Ag^{A-TS} surface only consists of large grains separated by wide grain boundaries, while the Ag^{TS} surface consists of large grains separated by small grains. Here the effect of annealing is clearly visible as the small grains are effectively removed. In addition, since both types of surfaces were obtained from template-stripping, all the grains are in the same plane defined by the template. In sharp contrast, both types of Ag^{DE} surfaces have a large surface roughness because the grains are small and are out of plane with respect to each other. Therefore, the Ag^{DE} surfaces have a large surface fraction of exposed grain boundaries at which SAMs cannot pack well while both types of Ag^{TS} surfaces have a relatively small amount of exposed grain boundaries.

DC characterization of the junctions

We formed the junctions using a previously reported procedure.⁶¹ Briefly, we formed SAMs of *n*-alkanethiolate SAMs $(S(CH_2)_{n-1}CH_3 \equiv SC_n \text{ with } n = 10, 14, \text{ or } 18)$ supported by the four types of silver surfaces. Next, we formed electrical contacts to these SAMs with EGaIn. We used $GaO_x/EGaIn$ top-electrodes stabilized in a through-hole in PDMS because these junctions are mechanically stable and suitable to perform the potentiodynamic impedance spectroscopy measurements which took about 2 h per junction to complete.

Before we started the impedance measurements, we recorded I(V) curves to confirm the junctions had their electrical characteristics within one log-standard deviation from the Gaussian log-mean J(V) curves reported previously.²² Fig. 3 shows the J(V) curves recorded from the junctions. Here, we repeated our experiments three times and the I(V) curves in Fig. 3 are the arithmetic mean and the error bar represents the standard deviations. From these data, we derived for each type of junction the value of β and J_0 by plotting the values of $\langle |J| \rangle$ determined at -0.50 V against the number of carbons in the backbone, n, of the SAM followed by fitting the general tunneling equation (eqn (1)) to these graphs. Fig. S1[†] shows the results and that indeed the current exponentially decays with increasing *n*. The values of β are 1.03 \pm 0.04 and 1.05 \pm 0.05 n^{-1} for junctions with $Ag^{A\text{-}TS}$ and Ag^{TS} bottom electrodes which are close to the consensus value of 1.0 n⁻¹ associated with high quality junctions. In contrast, junctions with Ag^{DE,1} or Ag^{DE,2} bottom electrodes have low β values of 0.54 \pm 0.05 and $0.58 \pm 0.06 \text{ n}^{-1}$ which is associated with defective junctions and in agreement with our previous findings.²² Despite the different values of β , all junctions with SC₁₀ SAMs have similar values of J. The difference in values of J at 0.5 V as a function of BV, however, is large (two orders of magnitude) for junctions with SAMs of n = 18 as function of the type of surface. These observations have been recently explained by us and we found that junctions with thin liquid-like SAMs (here with n = 10) are capable of self-repair and compensate for defects in the bottom-electrode material, but thick crystallinelike SAMs are not capable of this self-repair and therefore



Fig. 4 |Z| vs. frequency at 0.50 V obtained from junctions with SAMs of (a) SC₁₀, (b) SC₁₄, and (c) SC₁₈, with different bottom electrodes as indicated. The black solid line is a fit of eqn (3) to the data. The values of RSAM for junction with SC₁0 (d), SC₁₄ (e), and SC₁₈ (f) vs. VB. Error bars represent the standard deviation obtained from three different junctions.

result in defective junctions with large leakage currents flowing at defect sites.²⁵

The equivalent circuit

We have shown before that our junctions can be modelled with a simple equivalent circuit (Fig. 1c) consisting of R_{SAM} in parallel with C_{SAM} both in series with R_{C} .^{33,42} The complex impedance of this equivalent circuit is given by

$$Z = \left(R_{\rm C} + \frac{R_{\rm SAM}}{1 + \omega^2 R_{\rm SAM}^2 C_{\rm SAM}^2}\right) - j \left(\frac{\omega C_{\rm SAM} R_{\rm SAM}^2}{1 + \omega^2 R_{\rm SAM}^2 C_{\rm SAM}^2}\right)$$
(3)

This equivalent circuit implies that SAMs behave as a dielectric placed between two parallel plates (the electrodes). The SAM impedes the current flowing through the junction with a complex impedance which is a parallel combination of R_{SAM} and C_{SAM} . The SAM is coupled to the electrodes through a certain contact resistance R_{C} which is in series with the parallel RC circuit element.

The physical meaning of the equivalent circuit has been discussed before and is summarized here briefly.^{33,42} In EGaIn based molecular junctions the value of the contact resistance $R_{\rm C}$ is dominated by the SAM//GaO_x resistance. In our earlier work we reported that the contact resistances of the contact probes with the electrodes, the Ag–thiolate interface and the low



Fig. 5 The SAM resistance (R_{SAM}) and contact resistance (R_C) vs. DC bias voltages for junction with SC₁₀ SAMs (a), SC₁₄ SAMs (b), and SC₁₈ SAMs (c). The error bars represent the standard deviation of three independent impedance measurements.

resistance of the GaO_x layer only adds about 2% to the measured value $R_{\rm C}$.⁴² The resistance of the SAM is given by eqn (4) which relate $d_{\rm SAM}$ to $R_{\rm SAM}$, where $R_{\rm SAM,0}$ is the hypothetical resistance across the junction for $d_{\rm SAM} = 0$ nm.

$$R_{\rm SAM} = R_{\rm SAM,0} e^{\beta d_{\rm SAM}} \tag{4}$$

We interpret the impedance results and equivalent circuit modeling using Landauer tunneling model modified by including the contact resistance term which is associated with the coupling of the molecules to electrodes (eqn (5))⁶²

$$R_{\text{junction}} = \frac{h}{2e^2M} + \frac{h}{2e^2M} \frac{1-T}{T} = R_{\text{C}} + R_{\text{SAM}}$$
(5)

where *e* is the electron charge, *h* is the Planck's constant, *T* is the transmission probability, and *M* is the number of conduction channels.⁶² For an ideal point contact (T = 1), the contact resistance is the inverse of the universal quantum conductance $G_0 = 2e^2/h$. Eqn (5) explains the series combination of R_C and R_{SAM} in the equivalent circuit. The value of C_{SAM} can be described by the expression (eqn (6)) for capacitance in parallel plate geometry, where d_{SAM} is the distance between the metal surfaces, ε_0 is the permittivity of the free space, ε_r is the dielectric constant of the SAM, and A_{geo} is the geometrical area of the capacitor surfaces.⁴²

$$C_{\rm SAM} = \varepsilon_0 \varepsilon_{\rm r} A_{\rm geo} / d_{\rm SAM} \tag{6}$$

Potentiodynamic impedance spectroscopy

Impedance (*Z*) is a more general concept resistance than since it contains both amplitude and phase information of the signal.³³ As mentioned in the Introduction, impedance spectroscopy makes it possible to determine all circuit elements of our junctions. Here we determined the impedance spectra as a function of bias, *i.e.*, potentiodynamic impedance spectroscopy, how the defects inside the junctions affect all circuit elements. The impedance measurements were carried out by superimposing a sinusoidal signal (in the frequency range of 1 Hz to 1 MHz) on a DC bias ranging from -0.5 V to 0.5 V at intervals of 0.1 V. To improve the signal-to-noise ratios, we took the average of five impedance measurements at each given bias. This procedure was repeated three times with different junctions and the error bars represent the standard deviation of these three measurements.

Fig. 4 shows the frequency dependency of the modulus of the complex impedance (|Z|) for all junctions at -0.5 V (so-called Bode plots). The value |Z| is nearly constant at low frequencies (dominated by the resistance of the SAM), but decreases with increasing frequency as the capacitive reactance $X_{\rm c}$ decrease with frequency ω (capacitive reactance $X_{c} = 1/\omega C_{SAM}$). The Nyquist plots (Fig. S3^{\dagger}), the imaginary part of *Z*(*Z''*) plotted against the real part of Z(Z'), show only one semicircle which corresponds to the presence of one capacitor element in the equivalent circuit (the phase is 90° for a capacitor and appears as a semi-circle in the Nyquist plot). At high frequencies, the capacitive reactance becomes negligible and the impedance will saturate to the total contact resistance $R_{\rm C}$ which has a phase of 0° and is independent of the frequency (ohmic behavior). Fig. 4 shows that |Z| of the junctions with SAMs of SC₁₈ decreases over two orders of magnitude when bottom-electrodes with large BV values are used (the $Ag^{DE,1}$ and electrodes $Ag^{DE,2}$) while |Z| does not change significantly as function of BV for junctions with SC_{10} SAM. These observations agree with the J(V) behavior described above and follow our recent findings that liquid-like SAMs smoothen out the thin area defects easily compared to the long chain crystalline like SAMs.25



Fig. 6 C_{SAM} vs. DC bias voltage for the junctions with SAMs on by Ag^{A-TS} (a), Ag^{TS} (b), Ag^{DE,1} (c), or Ag^{DE,2} (d), bottom electrodes. Error bars represent the standard deviation obtained from three different junctions.

Before we fitted our data to the equivalent circuit, we determined the quality of the impedance data using the Kramers-Kronig transformation, or simply the so-called KK-test.⁶³ The low χ_{KK}^2 values $(0.9 \times 10^{-3} \text{ to } 1.2 \times 10^{-3})$ indicate that the data are of good quality, no higher harmonics are present, and the junctions were stable during the measurements. Next, we fitted our data to eq. to extract the values of R_C , R_{SAM} , and C_{SAM} . The solid black lines in Fig. 4 are fits to eqn (3) (with χ_{fit}^2 in the range of 1.1×10^{-3} to 1.5×10^{-3}) from which we conclude the equivalent circuit describes our junctions well. In the next section, we describe the circuit elements in more detail.

Fig. 5 shows the variation of R_{SAM} and R_C with DC bias voltage as a function of BV for different SAMs. The contact resistance R_C is independent of the applied DC bias and BV. These observations indicate that in all cases the contact resistance is ohmic in nature⁶¹ and dominated by the van der Waals SAM//top electrode contact which includes the resistance across the GaO_x layer. Our previous temperature dependent impedance studies on junctions with alkanethiolate SAMs on smooth electrodes showed that R_{SAM} , R_C , and C_{SAM} , do not vary with temperature which indicates that the mechanism of charge transport in this system is to coherent tunneling.⁴² In the present study, in all cases the value of R_{SAM} decreases exponentially with increasing bias, in agreement with previously reported results.^{14,33} Therefore, regardless of the number of defects, both defective and good quality junctions show typical tunneling characteristics.

The value of R_{SAM} does not vary significantly as a function of BV for junctions with SC₁₀ SAMs while it varies over two orders of magnitude for SC₁₈ junctions. This observation can be explained as follows. We have suggested before that the thin liquid-like character of SC10 SAMs smoothen out the defects induced by the surface topography of the bottom electrode because the intramolecular van der Waals interactions are weak and so the molecules can relatively easily "fall over" and "fill in" defects. In contrast, thick crystalline-like SC18 SAMs cannot selfrepair as efficiently due to the high packing energy of the SAM (due to strong van der Waals interactions between the long alkyl chains).25 Therefore, the latter suffer from thin area defects that reduce the effective thickness of the SAM (d_{SAM}) causing a large leakage current through the junction (see eqn (1)) while the former still forms good tunneling barriers even. Thus, the decrease in R_{SAM} with BV shows that the tunneling junctions formed on rough surfaces are dominated by thin area defects and that thin SAMs can compensate for defects over a wide range of BV values. The contact resistance is not noticeably changed because the area of defects is very small and charge conduction can be dominated even by a single pinhole.64 The presence of the defects in thick junctions is also apparent from the shape of the R_{SAM} vs. V curve which is "flatter" for junctions

Paper

with SC_{18} than SC_{10} SAMs. This change in the shape can be related to conduction through pinholes where the current is concentrated and cause local Joule heating.²³

Fig. 6 shows the values of $C_{\rm SAM}$ as a function voltage for all four junctions with different bottom electrode topographies and SAM thicknesses. The value of $C_{\rm SAM}$ shows no significant variation over the investigated DC bias range or value of BV. By fitting a plot of $C_{\rm SAM}$ against $1/d_{\rm SAM}$ to eqn (6) (Fig. S2†), we found that the value of $\varepsilon_{\rm r}$ of 3.2 ± 0.3 was the same within error for all junctions, in agreement with previously reported values.^{33,45,65} Thus, the defects induced by the topography of the bottom electrode did not alter the dielectric constant of the SAMs inside the junctions.

Conclusions

Impedance spectroscopy is useful method to characterize how defects affect the junction characteristics

By far most studies of molecular junctions are based on twoterminal electrical measurements using DC methods.16,66,67 Previously, we have shown that the yield in non-shorting junctions and reproducibility of the measurements (i.e., the log-standard deviations of the values of I) are remarkably insensitive to the topography of the bottom-electrode and hence these are poor indicators of the quality of these junctions.²³ Therefore it is difficult to determine the quality of the junctions (i.e., whether the junctions are dominated by defects are not) solely based on J(V) characterization.^{64,68} Recently, we showed that the quality of the junctions can be qualitatively determined by examination the curvature of the dJ/dV curves.²³ Here we show that impedance spectroscopy makes it possible to examine the role of defects in molecular junctions and which (circuit) elements are affected. We note, however, that this method only works for large area junctions (junctions with \gg 1000 molecules) since small area junctions (or single molecule junctions) have too small capacitances ($\ll 1 \text{ pF}$) to be measurable reliably.69

Defects induced by the electrode roughness results in an underestimation of the resistance of the SAM complicating quantitative analyses of data obtained from different platforms

The potentiodynamic impedance measurements and equivalent circuit modelling show that only the resistance of the SAM (R_{SAM}) is affected due to leakage currents across the SAMs. The contact resistance (R_{C}) and capacitance of the junction (C_{SAM}) are not sensitive to the surface roughness of the bottomelectrode while the value of R_{SAM} changed by two orders of magnitude. In other words, defective junctions behave as if they were leaky capacitors and this observation can explain, at least in part, the large spread of current densities measured for the same type of molecules across different test beds.¹⁴ More specifically, often metal electrodes are used to support SAMs that were obtained by direct deposition techniques, and/or the morphology of the electrodes is not reported. Our results, however, indicate that for quantitative analyses and comparisons of charge transport data across different types of junction platforms, it is important to report the morphology of the electrode materials.

The dielectric constant is insensitive to defects

The SAM based tunneling junction can be modelled as a parallel plate capacitor with a resistor connected in parallel across which the leakage current through the capacitor flows. For clarity, in this view of the junctions the leakage current across the capacitor is the same as the tunneling current across the SAMs. For a high quality capacitor, the capacitance should be high with a minimal leakage current flowing across it. Here we show that the leakage current through the SAM-based capacitors is sensitive to defects but the dielectric constant is not. This observation explains why the dielectric constant of aliphatic SAMs in other junction platforms based on Hg-drop and PEDOT:PSS electrodes are essentially the same as to the value we report here despite the large difference of the R_{SAM} and electrode materials between these platforms.

Grain boundaries are the main cause of leakage currents in EGaIn junctions

The TS surfaces have very low amount of step edges or pinholes.⁶⁰ The primary source of defects in our junctions with alkanethiolate SAMs originate from the grain boundaries. Therefore, ultra-flat surfaces (rms surface roughness < 1 nm) with large grains help to reduce the density of defects inside the junctions. Vilan *et al.* achieved grain boundary-free Al films by ultra-fast thermal deposition method.⁷⁰ These surfaces can support alkylphosphonate monolayers with almost no *gauche* defects.⁷¹ However, in case of the pre-existing defects due to the fabrication procedures (as is the case for commonly used metal electrodes), the choice of the SAM can also help to reduce the effect of the defects induced by the topography of the electrodes due to self-repair.²⁵

Conflicts of interest

There are no conflicts to declare.

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