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Defect-concentration dependence of electrical transport mechanisms in CuO nanowires†

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Investigations of the transport mechanisms of individual nanowires are important for advancing their use in applications. Based on statistical results for the temperature-dependent electrical characteristics of individual CuO nanowires, and by characterizing them using transmission electron microscopy, we have found that the defect concentration is the most important parameter affecting electron transport in nanowires. Space-charge-limited currents can be observed for sufficiently high applied voltages, for example about 10 V. In the ohmic regime, before the current–voltage curves of nanowires enter the trap-filling stage, three main transport mechanisms have been proposed. They are related to the defect concentrations and include combinations of defect-induced nearest-neighbor hopping, trap activation, and intrinsic excitation. Numerical calculations using the model to fit the experimental data agree very well, confirming the proposed transport mechanisms.

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Introduction

One-dimensional (1D) semiconductor nanostructures have attracted the attention of numerous scientists over the past two decades because of their unique electronic, optical, photo-electrochemical, and mechanical properties^{1–6} and due to their important prospects for applications in nanoelectronic devices.^{7–10} However, there are still many problems to be solved, such as how to improve the electrical uniformity of nanowire electronic devices. The electrical transport mechanisms of nanowires are critical for resolving these problems. It is also very important to understand the special characteristics of nanowires for targeted applications in nanoelectronic devices.

Much effort has been devoted to studies of the transport properties of oxide semiconductor nanowires.^{11–16} Various transport mechanisms have been proposed to explain the

experimental results, but no unified mechanism exists, even for the same kinds of nanowires. Proposed transport mechanisms include defect-induced thermal activation, variable-range hopping, nearest-neighbor hopping (NNH), and space-charge-limited currents (SCLC).^{11,12,17–21} Further, most of these studies have focused on n-type nanowires, with only a few dealing with p-type nanowires, which enable complementary devices.

Cupric oxide (CuO) is a representative natural p-type semiconductor, and CuO nanowires can be synthesized on copper substrates *via* thermal oxidation.²² Such nanowires have attracted much attention for applications in memristors, in heterojunctions, as electrodes for lithium-ion and lithium–air batteries, and in sensors.^{23–29} Understanding the electrical transport mechanisms in CuO nanowires is essential for realizing their applications in devices and for improving their performance. Until now, very little work has been carried out on this topic. Based on their temperature-dependent current–voltage (*I–V*) characteristics, Wu *et al.* proposed that thermal activation, phonon scattering, and polaron hopping may all contribute to the transport process, depending on the applied voltage and temperature.²¹ Florica *et al.* discovered that NNH dominates at low temperatures, compared with thermal activation at high temperatures.¹⁹ The results of these two studies differ significantly from each other, and statistically significant results are lacking. Thus, more detailed studies of the transport mechanisms are needed to discover the reasons for these differences and to pinpoint the factors that determine the actual transport mechanisms.

In the present work, we used CuO nanowires to look for insights into the transport mechanisms of p-type semiconductor nanowires. By analyzing statistical results for the

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temperature-dependent electrical characteristics of individual CuO nanowires, and by characterizing them using transmission electron microscopy (TEM), we have identified three types of mechanisms, which depend upon the defect concentrations. The results provide a guide for improving the properties of electronic devices. Specifically, to improve the uniformity of nanowire electronic devices, our results show that it will be necessary to employ nanowires with a small range of defect concentrations. Furthermore, we can choose nanowires with specific defect concentrations for special applications.

Experiment

CuO nanowires were synthesized using thermal oxidation method.^{21,22} In brief, Cu foils (5 mm × 8 mm, Alfa Aesar, 99.998%) were etched by an aqueous HCl solution (10%) to remove the contaminations and native oxide. Then the foils were cleaned in acetone, alcohol and deionized water under ultrasonic bath for 15 min, respectively. After dried by the nitrogen gas flow, the foils were loaded in a quartz plate and placed in the center of a muffle furnace. The heating rate was ~ 2 °C min⁻¹ and the oxidation temperature was 450 °C. The foils were oxidized in air isothermally at 450 °C for 360 min. After oxidation, they were cooled down to room temperature naturally. We employed scanning electron microscopy (SEM, Zeiss SUPER-60), X-ray diffraction (XRD), and transmission electron microscopy (TEM, TitanTM G2 60-300) to characterize the prepared nanowires.

To measure the electrical transport properties of a single CuO nanowire, we used a two-electrode configuration fabricated using UV (ultraviolet) photolithography. In brief, the CuO nanowires were first dispersed in alcohol and were then transferred to a quartz-glass substrate by spin-casting. We employed a lift-off process using UV photolithography to form the electrodes. Ni or Au films approximately 200 nm thick were deposited on the dispersed CuO nanowires by magnetron sputtering. Both the widths of the electrodes and the gap between the two electrodes were approximately 2 μ m. We chose Ni as the main electrode material because the work functions of Ni ($\phi = 4.99$ eV) and CuO ($\phi = 4.78$ eV (ref. 30)) enable ohmic contact to be obtained. Moreover, the adhesion property of the Ni electrode is excellent, and the lift-off process is easy to carry out. Furthermore, the oxidation layer at the surface of the electrode has a negligible influence on the results, as we confirmed by electrical measurements and X-ray photoelectron spectroscopy (see ESI Fig. S1 and S2† online) of the Ni electrodes. In our experiments, the electrical properties of the samples that use Ni as the electrode were repeatable and stable even after the samples were kept for several months. We note that Au or Pt are usually employed as contact electrodes for measurements of such nanostructures, and we also used Au as the electrodes for some samples. We found that the conductivities of the samples measured using Au electrodes have the same distribution as those obtained using Ni electrodes (see ESI Fig. S3† online), but the lift-off process when using Au as the electrode material is much more difficult.

We measured the I - V properties on a Cascade probe station using a semiconductor parameter analyzer (Agilent Technologies B1500A). We swept the applied voltage from -10 to 10 V, in steps of 0.1 or 0.02 V. Before each test, we annealed the samples to be measured at 413 K for 240 min in order to minimize the effects of gaseous absorption on the surfaces. All of the I - V curves were measured in thermal equilibrium. They were measured at different temperatures by varying the temperature of the sample stage from 213 to 393 K (except for three samples that were varied from 293 K to 353 K). The samples were immersed in a nitrogen atmosphere during the measurements, and all of the measurements were carried out in the dark.

Results and discussion

Morphology and structure

The top-view SEM image in Fig. 1(a) shows the morphology of the as-grown CuO nanowires; a magnified image of one of the nanowires is presented in the inset. The prepared nanowires are approximately 2 – 20 μ m in length and 30 – 300 nm in diameter.

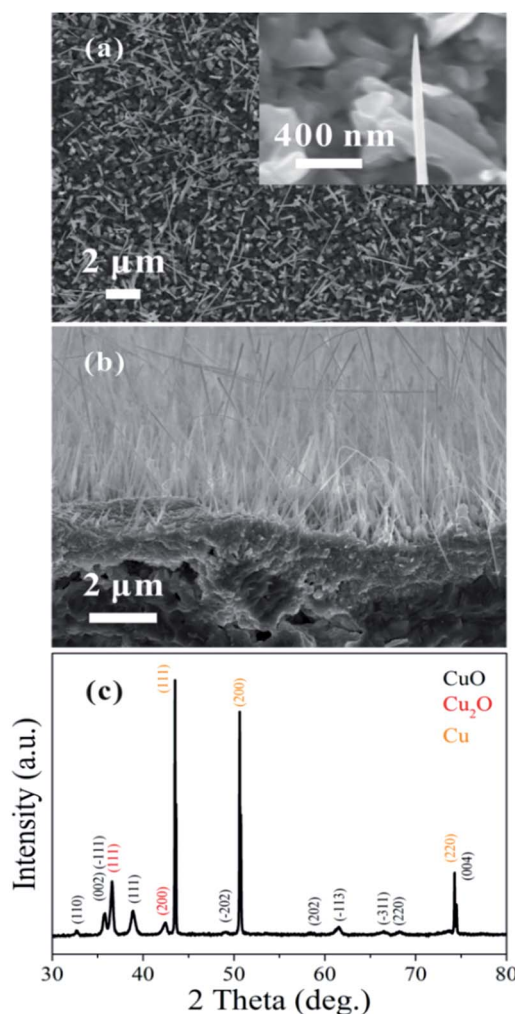


Fig. 1 (a) Top-view SEM image of the prepared CuO nanowires; the inset shows a magnified SEM image. (b) Cross-sectional SEM image. (c) XRD measurement of the CuO nanowires.



Fig. 1(b) shows a cross-sectional SEM image and (c) shows an XRD measurement of the CuO nanowires grown on the Cu substrate. The Cu₂O phase that appears in the XRD data is from an interlayer between the CuO nanowires and Cu substrate.³¹

I-*V* characteristics of CuO nanowires at room temperature (RT)

A representative SEM image of the fabricated structure is shown in the upper right inset of Fig. 2(a). Approximately 200 single nanowires were measured, and most of the samples exhibited similar symmetric and nonlinear behaviors [see the bottom-right insets of Fig. 2(b) and (c)] over a voltage range of -10 to 10 V. Several conduction mechanisms are commonly used to explain the transport properties of materials, and the relationship between *I* and *V* differs for each mechanism. We can therefore use those characteristics to distinguish among the various transport mechanisms. From fitting to the measurements, we can easily exclude Fowler–Nordheim tunneling, direct tunneling, thermionic field emission, and the Poole–Frenkel emission mechanisms.³² Symmetric *I*-*V* characteristics are expected from an ideal back-to-back Schottky diode configuration. However, we can exclude this mechanism because the back-to-back Schottky model²¹ does not fit our data (see ESI Fig. S4†).

According to the *I*-*V* curves, the experimental results can be divided into three categories, as shown in Fig. 2, which we refer to as type I, type II, and type III; there were, respectively, 47, 121, and 41 samples of each type. We believe that the conduction mechanism in these nanowires is SCLC. For low applied voltages that do not exceed V_{on} (the threshold voltage, at which the number of injected carriers exceeds that of thermally generated carriers), the conduction is ohmic, as can be seen in the insets in the upper-left corners of Fig. 2(a)–(c). Hereafter, we refer to this low-voltage region as the “ohmic-conduction region”. If the applied voltage exceeds V_{on} , then the *I*-*V* curves become nonlinear, and conduction shifts from ohmic conduction to a trap-filling process. This is indicated by the symmetric, nonlinear characteristics that appear in the *I*-*V* curves [as shown in the insets in the bottom-right corners of Fig. 2(a)–(c)]. The transport mechanisms have different values of V_{on} for the three categories of nanowires, as shown in Fig. 2(a)–(c); the value of V_{on} for type I is the largest (10 V), while type III has the smallest value (0.51 V).

The different conductivities are not caused by different diameters or contacted lengths of the nanowires (see ESI Fig. S5†). We propose that the differences in the defect concentrations are the cause of the different conductivities of nanowires synthesized under the same conditions. Defects are expected to exist widely in nanomaterials,³³ and different defect concentrations may relate to different growth mechanisms. As reported in the literature, a stress-induced growth mechanism occurs for CuO nanowires prepared by thermal oxidation.^{31,34–36} Stress accumulates during oxidation of the thin copper film. It is possible that the stress-nucleation sites may differ from each other due to differences in the stress distributions at a microscopic scale. Furthermore, in the process of growth, both stresses and oxygen transport may differ among the nanowires, which inevitably leads to differences in defect concentrations in the nanowires.

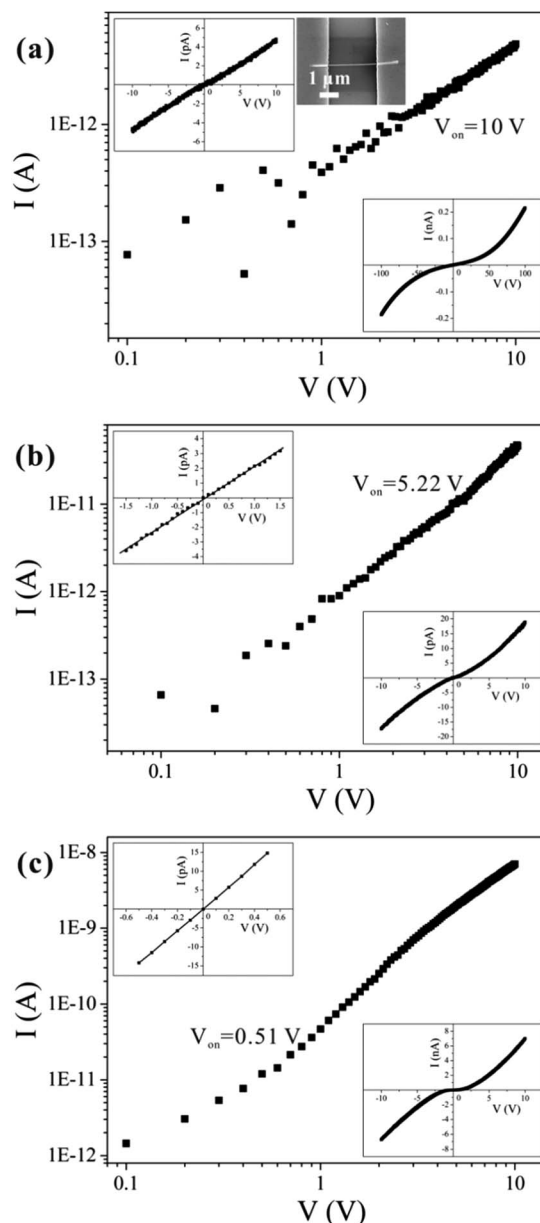


Fig. 2 Three representative *I*-*V* characteristics are shown here. (a) Type I: the *I*-*V* curve is linear between 0 and 10 V. The inset at the upper left shows the *I*-*V* curve for -10 to 10 V, and the upper right shows an SEM image of the measurement structure. The *I*-*V* curve for -100 to 100 V—for which the applied voltage exceeds V_{on} (see text)—is given at the bottom right. (b) Type II: the *I*-*V* curves deviate from linearity between 0 and 10 V. The inset at the upper left shows the *I*-*V* curve for -1.5 to 1.5 V, when the applied voltage does not exceed V_{on} , and the bottom right shows the *I*-*V* curve for -10 to 10 V. (c) Type III: the *I*-*V* curve shows three distinct regimes in the voltage range of 0 to 10 V. The inset at the upper left shows the *I*-*V* curve between -0.5 and 0.5 V, at low voltages that do not exceed V_{on} ; the bottom right shows the *I*-*V* curve between -10 and 10 V.

TEM characterization of nanowires with different conductivities

We chose several CuO nanowires (type I and type III samples) for TEM characterization, picking them out using a modified



SEM system (Zeiss Supra 55). The system is equipped with two tungsten probes that can be precisely manipulated. In our experiment, one of the tungsten probes was moved to touch the bottom part of a nanowire, and a thin layer of carbon was deposited on the contact point. The probe was then pulled in a direction opposite to that of the substrate, separating the nanowire from the substrate. The tip of the nanowire was fixed on the second probe using the same procedure. Then we carried out our I - V property measurements, and we chose the nanowires according to their measured I - V properties.

The morphology and structural characterizations of the nanowires are shown in Fig. 3. The images show that some nanoparticles are present on the surfaces of the nanowires. The diameters of all the nanoparticles are less than 10 nm, and the numbers of nanoparticles differ greatly between these two samples. Fig. 3(a) shows a low-resolution TEM image, a selected-area electron-diffraction pattern (SAED), and a high-resolution TEM image of a type I sample, for which the conductivity is the lowest. The good crystalline nature of the nanowire can be seen from the TEM image, except a few nanoparticles. Some of the nanoparticles are outlined by white circles in the high-resolution TEM image, and the inset shows a magnified image of one of the nanoparticles. The SAED pattern also indicates the excellent crystal structure of the nanowire. Fig. 3(b) shows the corresponding micrographs for a type III sample, for which the conductivity is the highest. Note that the TEM image of the type III sample is blurry, in contrast to the corresponding type I image. The uneven distribution and rough surface can be seen in the low-resolution TEM image, and the

structure of the nanowire is a bit difficult to distinguish in the high-resolution micrograph. Many nanoparticles can be seen on the sample surface. The SAED pattern shows the good crystal quality of the nanowire, with extra diffraction spots caused by diffraction from the nanoparticles (see the simulation results of the SAED pattern in ESI Fig. S5†). The zone axis of the diffraction spots indicated by the red arrows is different with the white ones and the blue lines show the ring patterns. Some of the nanoparticles have been outlined by white circles, and the inset shows a magnified image of one of the nanoparticles. In the SAED patterns from both samples, ring patterns can be seen faintly, which are ascribed to the existence of the nanoparticles.

We attribute the different defect concentrations in the different nanowires to the existence of the nanoparticles. Copper vacancies are the main defects in CuO, as has been reported in previous work, with the defect states located at an energy about 300 meV above the valance band.³⁷ Because of their small sizes and specific surface areas, the nanoparticles have much higher Cu vacancy concentrations than the bulk of the nanowire.³³ Therefore, higher defect concentrations are expected for type III samples than for type I.

Temperature dependence of the I - V characteristics of individual CuO nanowires

We chose approximately 30 single nanowires that showed perfectly linear properties in the low-applied-voltage region for these measurements, and the current increased in all of them with increasing temperature. It is worth noting that there are two reasons why the contact resistance between the nanowire and electrode in the temperature-dependent I - V measurement may be negligible. First, CuO nanowires have intrinsically high resistance, so the contribution of the contact resistance is not very prominent. Second, the contact resistance does not change significantly with increasing temperature. Some reports have indeed pointed out that the influence of the contact resistance on the value of the activation energy is negligible.¹⁹

Fig. 4 shows the three typical types of temperature-dependent I - V characteristics. According to the SCLC theory, the defect concentration can be determined using eqn (1),^{21,38,39}

$$N_T = 2\varepsilon V_c / qL^2 \quad (1)$$

where N_T is the defect concentration, ε is the dielectric constant of the CuO nanowires, L is the length of the nanowire between the two contact points, q is the elementary charge, and V_c is the critical voltage. The value of V_c can be determined by extrapolating the temperature-dependent I - V curve to higher voltages; the voltage of the intersection point is then V_c .

For the nanowires we studied in this work, ε is a constant; thus, the value of N_T depends on V_c/L^2 . The lengths of the contacted nanowires presented in Fig. 4(a)–(c) are 8.029 μm , 2.626 μm , and 2.95 μm , respectively. The calculated values of the defect concentrations for each type in this study are $4.83 \times 10^{16} \text{ cm}^{-3}$, $7.01 \times 10^{17} \text{ cm}^{-3}$, and $1.06 \times 10^{18} \text{ cm}^{-3}$, respectively; therefore, the defect concentration is different for different nanowires. It is lowest in type I and highest in type III

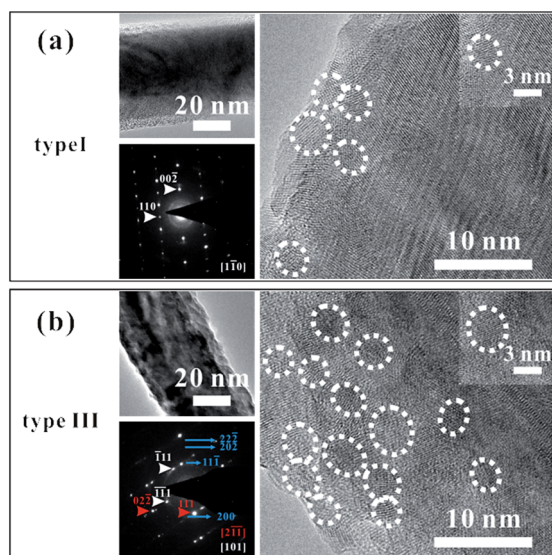


Fig. 3 (a) Low-resolution TEM image (upper left), selected-area electron-diffraction (SAED) pattern; (bottom left), and high-resolution TEM image (right) for a type I sample. The inset shows a magnified image of one of the nanoparticles. (b) Low-resolution TEM image (upper left), SAED (bottom left, which has contributions from both [101] and [2-1-1] axes, the existing diffraction rings can be attributed to the arbitrary orientations of CuO nanoparticles), and high-resolution TEM image (right) for a type III sample. The inset shows a magnified image of one of the nanoparticles.



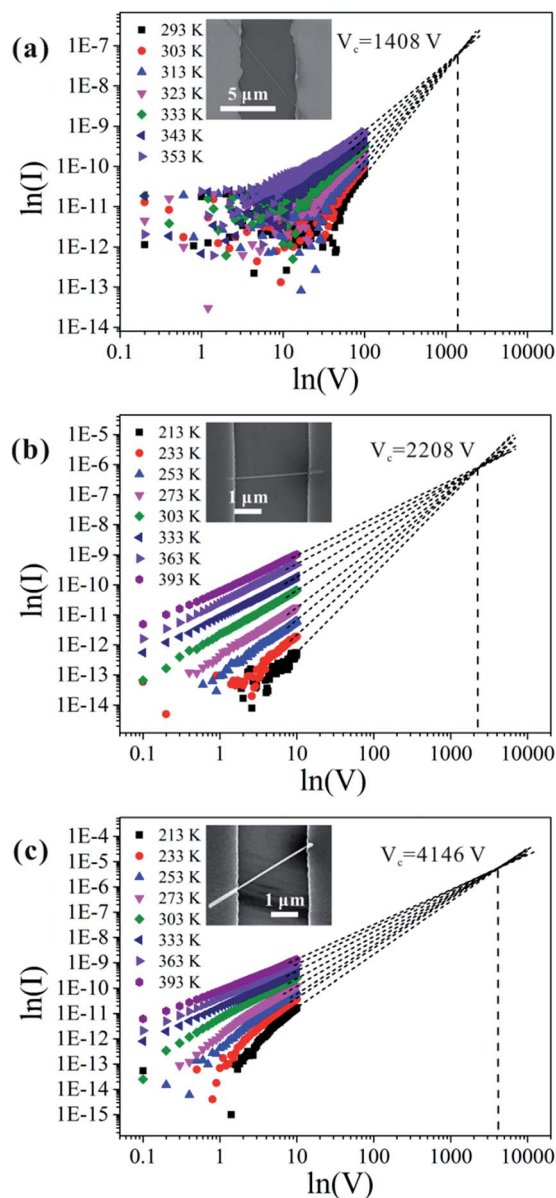


Fig. 4 Measured I - V characteristics at different temperatures, and linear extrapolations of the I - V characteristics to obtain the crossover voltage.⁴² Panel (a) corresponds to type I results, (b) to type II results, and (c) to type III results. The current I is in ampere, and the voltage V is in volt. The insets show the corresponding SEM images of each nanowire.

samples, which agrees well with the conclusion drawn in the preceding section.

Over the temperature range we investigated, if all of the nanowires were to undergo the same thermal activation process in the ohmic-conduction region (which reveals the native property of the nanowires), then we would expect type III samples to have the largest values of V_{on} , because the carrier density increases with increasing N_T .^{40,41} However, this conclusion disagrees with the experimental results we obtained in the RT measurements. The most probable reason for this is that the transport process in the ohmic-conduction region is not consistent in this temperature region. Defect concentrations

can influence the critical temperature at which a semiconductor changes from one conductivity regime to another, and if the range of temperature variation remains unchanged, then nanowires with different defect concentrations may have different transport processes.

Discussion

To analyse the different transport processes in detail, we fitted our experimental conductivity data using an Arrhenius plot in the ohmic-conduction region. In an Arrhenius plot, the activation energy is a constant, and the conductivity is given by¹¹

$$\sigma = \sigma_0 \exp(-E_a/k_B T) \quad (2)$$

where σ_0 is a constant, E_a is the activation energy, and k_B is Boltzmann's constant.

Fig. 5(a) shows a typical Arrhenius plot for one of our samples, which can be fitted well with a straight line. The activation energy can be determined from the slope of the Arrhenius plot.^{17,18} For example, the calculated value of the activation energy for the sample presented in Fig. 5(a) is approximately 327 meV, which is related to the central level of the defect.

The frequency distribution of the activation energies for the samples we studied is shown in Fig. 5(b), which reveals three clusters of values centered near 240, 320, and 450 meV. The

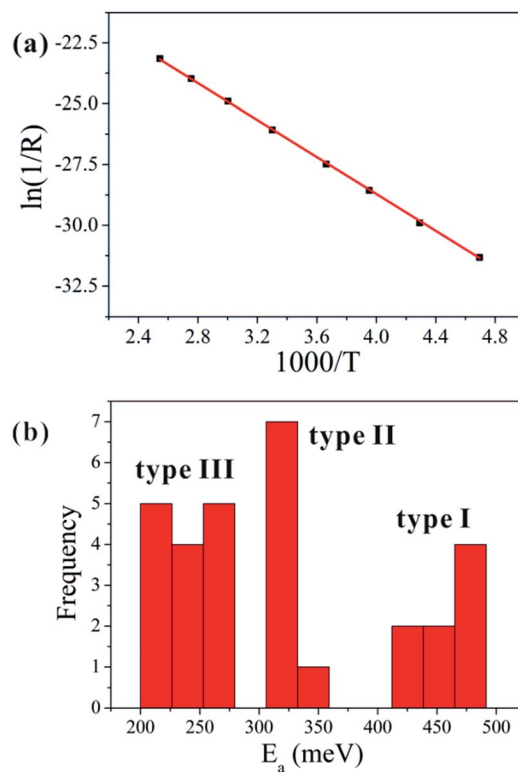


Fig. 5 (a) Logarithm of the reciprocal resistivity as a function of reciprocal temperature (Arrhenius plot) for a single CuO nanowire sample. The resistivity R is in ohms, and the temperature is in K. (b) Frequency distribution of the activation energy.



activation energy of the type I nanowires is the largest, and it is smallest for the type III nanowires. Furthermore, the activation energy of type II is similar to the reported defect level of Cu vacancies in the p-type conduction band of CuO, which is located at an energy 300 meV above the valance band.

As all the CuO nanowires were prepared under the same conditions, there should not be much variation in the defect type and defect energy levels. Therefore, the activation energy should be a fixed value related to the defect energy level, if thermal activation were the only transport mechanism. However, the calculated values of the activation energy are not fixed, which points to the existence of different transport processes in the ohmic-conduction region. As shown in Fig. 5(b), the activation energies are distributed in three clusters, which agree with the three types of nanowires categorized from the I - V curves.

We propose that electron transport in a nanowire is a mix of different transport mechanisms. It is known that two kinds of defect-related conduction mechanisms may be responsible for temperature-dependent conductivity, *i.e.*, thermal activation and NNH, which obeys the Arrhenius relationship and shows $I \propto V$ at RT. Thus, the three transport types can be described as follows: trap activation mixed with intrinsic excitation (referred to as type I); trap activation only (referred to as type II); and NNH mixed with trap activation (referred to as type III).

As we have seen, the activation energy of the type I nanowires is the largest. It is close to the intrinsic Fermi level (0.6 eV) of the CuO nanowires (bandgap ~ 1.2 eV).⁴² Therefore, intrinsic excitation can be assumed to be a component of the transport process for these nanowires. The number of surface nanoparticles for this kind of sample is the lowest, and consequently the defect concentration also is the lowest. Intrinsic excitation is more likely to occur at a low temperature. With the inclusion of intrinsic excitation, the carrier concentrations of the nanowires should increase dramatically at RT, which is likely to cause this type to have the largest V_{on} . A schematic energy diagram for this process is represented in the inset at the upper left of Fig. 6, while the bottom left shows a schematic view of structure we measured. The conductance of the nanowires can be expressed as

$$G = G_1 + G_2 = G_{01} \exp(-E_{a-trap}/k_B T) + G_{02} \exp(-E_g/2k_B T) \quad (3)$$

where G_{01} and G_{02} are constants, E_{a-trap} is the activation energy of the trap-activation process, and E_g is the bandgap of the CuO nanowires.

Fig. 6 shows the fitted result obtained using eqn (3) for the type I mechanism for one of the CuO nanowires. The values of the fitted parameters are as follows: $G_{01} = 31.26$ S, $G_{02} = 5.2 \times 10^5$ S, $E_{a-trap} = 320$ meV, and $E_g = 1.2$ eV.

As noted above, the activation energy for type II nanowires is approximately that of the Cu vacancy energy level (300 meV), which is in agreement with trap-activation theory. The conductance of the nanowire is described by

$$G = G_0 \exp(-E_{a-trap}/k_B T) \quad (4)$$

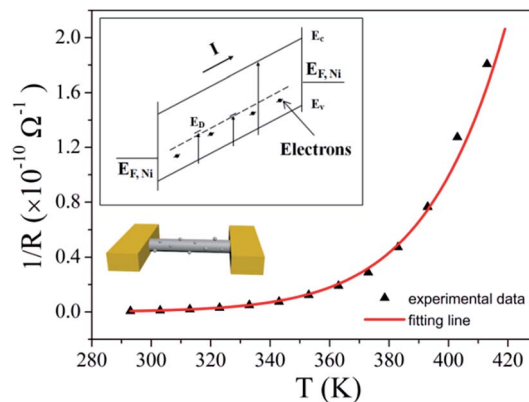


Fig. 6 Plot of experimental data and fitted curve for the conductance using the type I transport model: trap activation mixed with intrinsic excitation. The inset at the upper left shows the schematic energy diagram for this transport process, while the bottom left shows a schematic view of structure measured.

where G_0 is a constant and E_{a-trap} is the activation energy. Fig. 7 presents the fitted results, which match the experimental data almost perfectly. The fitted value of G_0 is 162.1 S.

From the probability distribution shown in Fig. 5(b), we find that the defect energy level lies at approximately 320 meV above the valance band. For this case, the transport process is as illustrated in the inset of Fig. 7. When extra voltage is applied to the nanowire, the electric field along the nanowire increases. The presence of defects makes it easier for electrons in the nanoparticles to move forward, leaving a vacancy at the original position. These electrons are injected into the core of the nanowire for transport, and other electrons move in to occupy the vacancy. From the viewpoint of band theory, the electrons increasingly gain in energy with increasing temperature, and they are thus excited from the valance band to the defect energy level, leaving more vacancies in the valance band and promoting conductivity.

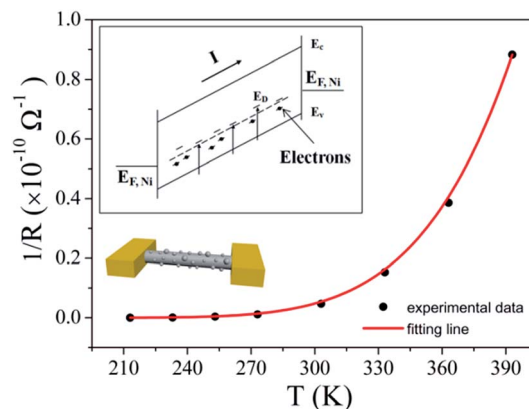


Fig. 7 Experimental data and fitted curve for the conductance using the type II transport model: trap activation. The inset at the upper left shows the energy diagram, which represents the energy and the spatial distribution of defect states and illustrates the transport process. The inset at the bottom left is a schematic view of the structure measured.



The calculated value of the activation energy for the type III samples is very low, *i.e.*, approximately 225 meV, if the trap-activation model is applied. There are many nanoparticles on the surfaces of the type III nanowires, some of them in contact with each other. Electrons can transfer from one nanoparticle to another by hopping, which is similar to the transport process in multicrystalline or amorphous structures. Other electrons are injected into the core of the nanowire for transport, just as for type II. Due to the high defect concentration, type III nanowires require a higher temperature to transition from the NNH region to the trap-activation region. From the viewpoint of band theory, the activation process in this temperature range actually includes two processes: (i) NNH, in which electrons are ionized from an occupied state to the nearest empty state, producing vacancies for hopping,⁴³ and (ii) trap activation, in which electrons transition from the valance band to the defect level, thereby supplying electrons to, and leaving holes in, the valance band, which promotes charge transport. In this case, the carrier concentration is determined by carriers generated both through NNH and through trap activation. At RT, the defects are weakly ionized, and NNH dominates, so the carrier concentration for this type is lower than for cases in which trap-activation dominates. Therefore, the smallest values of V_{on} are obtained for nanowires of this type. With increasing temperature, the contribution of trap activation for this type of nanowire becomes more apparent. The conductance of the type III nanowires can be expressed as

$$G = G_1 + G_2 = G_{01} \exp(-E_{a-NNH}/k_B T) + G_{02} \exp(-E_{a-trap}/k_B T) \quad (5)$$

where G_{01} and G_{02} are constants, while E_{a-NNH} and E_{a-trap} are the activation energies of the NNH and the trap-activation process.

Fig. 8 shows the fitting plot of the conductance using NNH combined with the trap-activation model. The values of the fitted parameters are $G_{01} = 9.34 \times 10^{-5}$ S, $G_{02} = 94.93$ S, $E_{a-NNH} = 30$ meV, and $E_{a-trap} = 320$ meV. The insets in Fig. 8 show the schematic energy diagram for this mixed-transport process.

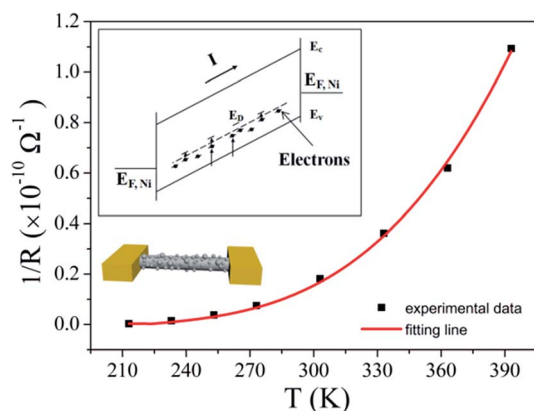


Fig. 8 Fitting plot of the conductance using the type III transport model: NNH mixed with trap activation. The inset at the upper left shows the schematic energy diagram for this transport process, while the bottom left shows a schematic view of the structure measured.

Conclusions

In summary, different defect concentrations induced three types of transport mechanisms in our CuO nanowires samples. In the temperature range 213–393 K, nanowires with activation energies of ~ 320 meV exhibited type II transport processes. Those with large defect concentrations underwent a type III transport process, and intrinsic excitation may be involved for those with the fewest defects (type I).

The numerical calculations further confirm these transport mechanisms. According to this theory, the defect concentration is an important parameter in semiconductor conduction, as it determines the dominant mechanism in the transport process. With well-established experimental facts, numerical calculations, and corresponding band-theory explanations, our work has increased the understanding of the transport mechanisms in CuO nanowires and provides a guide for improving the uniformity of electronic devices, which is important for applications of semiconducting nanowires in electronic devices.

Conflicts of interest

There are no conflicts to declare.

Author contributions

J. C. planned the project. Z. L. carried out the measurement work, theoretical deduction and data analysis. R. Z. carried out the TEM characterization. S. T. took part in the TEM characterization. L. L., H. L., S. J. carried out the simulation and analyse of the TEM results. J. C., H. C., J. S., S. D. and N. X. took part in the data analysis. The manuscript was written through contributions of all authors.

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