Nanoscale

PAPER

Check for updates

Cite this: Nanoscale, 2018, 10, 20772

Defect-free SnTe topological crystalline insulator nanowires grown by molecular beam epitaxy on graphene

Janusz Sadowski, a,b,c Piotr Dziawa, a Anna Kaleta, a Bogusława Kurowska, a Anna Reszka, a Tomasz Story b and Sławomir Kret* a

SnTe topological crystalline insulator nanowires have been grown by molecular beam epitaxy on graphene/SiC substrates. The nanowires have a cubic rock-salt structure, they grow along the [001] crystallographic direction and have four sidewalls consisting of (100) crystal planes known to host metallic surface states with a Dirac dispersion. Thorough high resolution transmission electron microscopy investigations show that the nanowires grow on graphene in the van der Waals epitaxy mode induced when the catalyzing Au nanoparticles mix with Sn delivered from a SnTe flux, providing a liquid Au–Sn alloy. The nanowires are totally free from structural defects, but their (001) sidewalls are prone to oxidation, which points out the necessity of depositing a protective capping layer in view of exploiting the magneto-electric transport phenomena involving charge carriers occupying topologically protected states.

1. Introduction

SnTe belongs to a very well-known family of narrow bandgap IV–VI semiconductors, which have been studied because of their interesting electronic properties and applications, e.g. in thermoelectric energy harvesting and as mid-infrared detectors and emitters. Quite recently SnTe and some of its ternary (multinary) alloys with Se and/or Pb were identified as topological crystalline insulators (TCI). Indeed IV–VI TCIs host topologically protected Dirac states in the vicinity of the distinct points of the Brillouin zone (in this case projection of the L points on the surface Brillouin zone). These states occur due to the coexistence of specific crystalline symmetries, i.e. {110} mirror-plane of a rock-salt crystal structure and electronic band inversion of a corresponding bulk crystal. The topological protection has been experimentally verified for two high symmetry surfaces of SnTe, namely {100} and {111}, first for the {100} surfaces of bulk crystals, either after cleaving (in a high vacuum) or for naturally grown crystal facets and then for the respective surfaces of thin films grown by molecular beam epitaxy (MBE) or other methods such as hot-wall epitaxy and e-beam evaporation. With epitaxially grown crystalline layers it is possible to access the topologically protected states on surfaces which are not easy cleavage planes, only reachable when bulk crystals are used. Moreover in thin layer geometry, at some thickness limit, the wavefunctions of topologically protected carriers (Dirac states) start to overlap, leading to a new phenomenon, e.g. occurrence of a two-dimensional topological crystalline insulator and a quantum spin Hall insulator in the same material.

In this letter we report on an interesting kind of low-dimensional structure fabricated from a SnTe TCI material, quasi one-dimensional nanowires (NWs) deposited on graphene/SiC(6H). The graphene-TI heterostructures are foreseen to exhibit interesting properties due to the combination of high mobility 2D graphene charge carriers and the spin-texture of carriers in a TI material. To our knowledge the MBE growth of SnTe NWs has not been reported before neither on graphene nor on any other substrate. Several literature reports on SnTe NWs and nanocrystals published already concern the nanostructures obtained via the chemical vapor deposition technique which cannot compete with MBE in terms of controlling the NW geometrical parameters (lengths and diameters) and the ability to grow NW heterostructures.

2. Experimental methods

The NWs have been grown by MBE on graphene/SiC(6H) substrates using a gold assisted growth mode. Graphene was deposited on a C-face of SiC(0001) by the chemical vapor
deposition technique, according to the procedure described in ref. 24. As the source of gold nanocrystals, 10 nm Au nanoparticles (NPs) suspended in H2O (BBI solutions) have been used. After wetting graphene/SiC substrates with an Au nanoparticle solution, the sample was dried using gaseous nitrogen, mounted on the MBE sample holder and introduced to the load lock of the MBE system. After initial degassing at about 200 °C the Au(NP)/graphene/SiC substrate was heated to about 700 °C, and then the substrate temperature was decreased to 400 °C–440 °C and the deposition of SnTe was started using a compound SnTe source (Knudsen effusion cell) with a beam equivalent pressure of about 4.5 × 10−8 mbar. The growth has been monitored by using a reflection high energy electron diffraction (RHEED) system. Nanoscale morphologies of the as-grown samples have been investigated by using a scanning electron microscope (Hitachi SU-70) operating at an acceleration voltage of 15 kV. The structural properties of NWs and of the SnTe/graphene/SiC interface have been investigated with an FEI-Titan 80–300 transmission electron microscope operating at 300 kV, equipped with an image corrector. For TEM investigations NWs are transferred to a holey carbon film supported by a Cu mesh mechanically, without using any liquid with dispersed NWs. The Helios Nanolab 600 FIB was used to prepare the SnTe/graphene/SiC interface cross-section specimen by means of the focused ion beam technique and lift-off procedure with an OmniProbe nanomanipulator. The electron deposited platinum was used first to protect NWs and then the thicker ion deposited layer was supplied. The low loss spectra were acquired in STEM mode using the Quantum 966 Gatan image filter with an energy resolution of about 0.95 eV. EDS was performed using the EDAX system. All the TEM investigations have been done at room temperature.

3. Results and discussion

Fig. 1 shows the scanning electron microscopy (SEM) images of SnTe deposited on the graphene/SiC substrate at a substrate temperature of 440 °C (sample 1) and 400 °C (sample 2).

The majority of NWs visible in Fig. 1 are about 0.5 μm long and 50 nm thick; occasionally much longer and thinner NWs occur. Apparently, besides the SnTe NWs, the graphene/SiC substrate is also partially covered with planar SnTe. The Au nanocrystals which coagulate and do not catalyze SnTe NWs are visible as brighter spots on the irregular planar SnTe deposits. In spite of slightly lower surface NW densities there are no significant differences in the structure of both samples, but the planar MBE growth of SnTe is slightly more pronounced for sample (2) grown at a lower substrate temperature than sample (1).

SEM images taken with e-beam perpendicular to the substrate (parallel to the NWs) – Fig. 1c and d indicate that NWs have square cross sections, which correspond to the [001] NW growth axes. We have observed previously that, in contrast to III–V and II–VI semiconductor NWs which usually grow along the [111] crystallographic direction, the IV–VI NWs naturally grow along the [001] direction,25 which implies their 4-fold axial symmetry and square cross section.

In Fig. 1c and d it can be seen that there is lack of correlation of azimuthal orientation of the NWs. However almost all the NWs grow perpendicularly to the substrate. Fig. 2 shows the focused ion beam (FIB) cross-section acquired from sample (1). Two SnTe NWs embedded in a thick platinum layer can be identified in the image.

In the FIB cross-section, the blocks of planar SnTe nanocrystals (NC) are also visible. All the NCS have flat surfaces parallel to the interface plane (shown in more detail in Fig. 2). The two SnTe NWs indicated on the image are perpendicular to the substrate plane.

Fig. 3 shows the high resolution HRTEM image of the FIB cross-section of the SnTe/graphene/SiC hetero-interface (the same specimen measured in different zone axes). The negative spherical aberration imaging (NCIS) mode26 helps in visualizing the carbon atoms in SiC as well as in the graphene interlayer. With the spherical aberration correction settings used
(Cs = −40 μm) the atomic columns in SiC and graphene appear bright against a darker background. Due to the low density of NWs we were unable to find a cross section with the SnTe (NW)/substrate interface in the specimen suitable for HRTEM investigations. Only planar SnTe NCs could be identified.

In the interface region with a graphene bi-layer we observe the distances between the lattice planes which differ from both SiC and SnTe (see Fig. 3c). Analyzing thoroughly the HRTEM image of this interface we can conclude that SiC is terminated by the carbon layer spaced 1.8 Å from the Si-plane of SiC(6H), similarly to that reported by Norimatsu and Kusunoki.27 Next we observe a distance of 3.6 Å between two graphene planes and the first monolayer of SnTe which is directly placed over the second layer of graphene. The SnTe crystal shown in Fig. 3 is slightly twisted around the [001] direction, so the atomic structure of SnTe is not perfectly resolved. The visible degradation of the graphene interlayer under an electron beam was detected after a 5 minute exposure to 300 kV electrons and total amorphization appeared after about 20 minutes under the normal HRTEM observation conditions at high magnification.

Even though the cross section shown in Fig. 3 applies to the interface between planar SnTe and the graphene/SiC(6H) substrate we believe that the interface of SnTe NW and graphene/SiC(6H) looks the same since similarly to planar SnTe NCs, the rock-salt SnTe NWs grow along the [001] direction. The planar growth of very thin SnTe (1 to 4 ML) on the graphene/SiC substrate has been investigated previously in the context of robust in-plane SnTe ferroelectric properties.28 The authors of ref. 28 also observed only the (001) orientation of thin SnTe islands deposited by MBE on graphene/SiC. On the other hand, for SnTe nanostructures (nanoplates and planar nanowires) deposited on silicon by the CVD technique, growth on both the (100) and (111) planes of SnTe has been observed.21

In spite of the dissimilarities between the (001) planes of cubic SnTe, with the 4-fold symmetry and the 6-fold symmetry of the graphene/SiC(6H) substrate there is some degree of correlation between SnTe(100) and graphene/SiC(6H), evidenced in the FFT features [see Fig. 3d]. In the case of self-catalyzed GaAs NWs growth of graphene29 such correlation was even more pronounced and also concerned azimuthal correlations between GaAs NWs with the [111] growth axes and graphene, which is not observed in our case. For GaAs[111] NWs both the NWs and the substrate share common 6-fold symmetries, which do not occur for (001)SnTe and graphene/SiC(6H).

Fig. 4 shows the TEM images of a nanowire picked up from sample (2). It can be seen that the surface of the upper NW part (under the catalyst droplet) is smooth as opposed to the bottom where we observed pronounced surface roughness due to oxidation.

The catalyst nanocrystal at the NW tip shown in Fig. 4d is inhomogeneous; the part visible in the upper section of the image is Sn-rich, as evidenced by the EDS composition scans described further in the text. The spacing between the crystallographic planes clearly visible in the upper part of the solidified
A thorough inspection of the TEM images of the top part of SnTe NWs shown in Fig. 5 reveals that the NW sidewalls are oxidized (see the bottom part of Fig. 5a and d). Amorphous oxide is manifested by slightly darker contrast of the close-to-sidewall region, with respect to that of the NW body. It has also been confirmed by the EDS scan through this region (Fig. 5e). Apparently only the very top part of the NW down to about 40 nm below the interface with the catalyst droplet is free from oxidation. In some NWs this oxidation is very pronounced, but in all the cases the very top NW parts are protected against oxidation by a thin (2–3 ML) film of Au, or an Au–Sn alloy (this region is too thin to unequivocally determine its composition). The problem of SnTe oxidation has recently been investigated by Berchenko et al. The authors of ref. 31 show that the amorphous oxide at the SnTe(100) surfaces consists of a mixture of Sn and Te oxides and builds up during the first 10 minutes exposure of SnTe to air, then its thickness increases slowly in the time-scale of months, and can reach up to 20 nm after 1–2 years. In the NWs studied by us we have noticed different degrees of oxidation of NWs picked up from different samples, which have been stored in air for a similar time. We cannot directly infer what causes the differences in the oxidation rate; tentatively we attribute that to the different stoichiometries of SnTe NWs. An example of NW oxidation presented in Fig. 5d and the corresponding EDS profile (Fig. 5e) shows the significant contribution of oxygen at the sidewall. The sidewalls of the NWs collected from sample (1) grown at 440 °C are much more oxidized than those collected from sample (2), grown at 400 °C; and due to the known thermodynamic properties of SnTe we can infer that sample (1) is more Sn-rich than sample (2).
As can be seen in Fig. 5b and c the Au rich part of the droplet at the NW tip still contains a substantial amount of Sn. To identify which phase of the Au–Sn alloy is present in the droplet we have analyzed the two-dimensional Fast Fourier Transform (2D FFT) images of the different NW tip and droplet areas (see Fig. 6). The 2D FFT patterns obtained in three different zone axis directions correspond well to: Au$_{0.5}$Sn$_{0.5}$ – the main part of the droplet (light blue) and Au$_{0.85}$Sn$_{0.15}$ (red). The patterns corresponding to SnTe in the NW body are marked with yellow. Both Au–Sn intermetallic compounds are liquid at the SnTe NW growth temperature applied here (400 °C–440 °C), which indicates that the metal nanoparticle at the NW tip is in the liquid state during the NW growth.

Fig. 7 shows two STEM-HAADF images of the SnTe NW grown on graphene/SiC collected from sample (3) grown at a substrate temperature of 450 °C for a much longer time (3 h) than samples (1) and (2), (1 h). Here the NWs are longer; however they have similar diameters as those picked up from samples (1) and (2) (see Fig. 1, 2 and 4) and are also totally free from structural defects across the whole NW lengths.

Similarly to the NWs shown in Fig. 4–6, here the NW axis is also parallel to the SnTe[001] crystallographic direction. Likewise, the solidified metal droplet at the NW tip has an inhomogeneous composition.

As can clearly be seen in Fig. 7b the outer parts of the NW sidewall edges appear slightly brighter than the NW body (visible in all the SnTe NWs inspected by us). The EDS elemental composition scans of this region prove that similarly to the NWs shown in Fig. 4–6, in these regions the NW sidewalls are rich in gold and consist of a very thin (2–3 ML) film of gold or a gold–tin intermetallic alloy. As mentioned before, this very thin Au-rich shell prevents the SnTe NW sidewall from oxidation, which appears as light NW body narrowing visible in the lower part of the image shown in Fig. 5a.

Complementary information about the composition of the top part of SnTe NWs has been obtained by detection of plasmon resonances with the use of electron energy loss spectroscopy (EELS) in STEM. EELS is a powerful technique enabling the detection of electron energy losses due to excitation in a broad spectral range from optical energies up to vacuum ultraviolet. Fig. 8 shows the 2D maps of EELS intensity obtained by the integration of signals for different energy window spectra of the SnTe NW tip and solidified Au–Sn droplet registered for the NWs shown in Fig. 7 (lower energy part of the EELS spectrum), and Sn and Te element distribution mapping due to the signal corresponding to the N$_{4,5}$ edges of both elements. The plasmon-loss spectra are detected...
for energies corresponding to Au localized surface plasmons (2.4 eV),\textsuperscript{25} SnTe volume plasmons (14.8 eV) and SnTe surface plasmons (10.8 eV).\textsuperscript{36} The width of the EELS signal integration window was set at 1 eV, 3.4 eV and 3 eV, respectively.

In accordance with EDS and structural information obtained previously, the plasmon resonances observed in EELS confirm the presence of the thin Au-rich film at the top parts of the NW sidewalls. The higher energy part of the EELS spectrum (in the 20 eV–50 eV range) enables direct elemental mapping of the NWs and the metal catalyst at the top. The 2D mapping of the EELS signal corresponding to absorption at Sn N\textsubscript{4,5} (24 eV) and Te N\textsubscript{4,5} (40 eV) edges (see Fig. 8d and e) confirms the inhomogeneous composition of the catalyzing Au–Sn nanoparticles (solid at the TEM measurement temperature), and the negligible amount of Te in it.

4. Conclusions

In summary, we have investigated the Au-assisted MBE growth of SnTe topological crystalline insulator nanowires on graphene/SiC(6H) substrates. The NWs have a cubic rock-salt structure and grow along the [001] axes which implies their square cross-sections and occurrence of four {100} sidewalls recognized as topologically protected surfaces of the SnTe topological crystalline insulator. Analysis of the metal catalyst nanocrystals at the NW tips reveals that they contain a substantial amount of Sn. Two distinct phases of the Au–Sn intermetallic alloy: Au\textsubscript{0.5}Sn\textsubscript{0.5} and Au\textsubscript{0.85}Sn\textsubscript{0.15} have been identified in the solidified catalyst droplet. Both phases are liquid at the NW growth temperature (400 °C–440 °C) which indicates that the NWs grow in the VLS mode. In the majority of NWs, thoroughly investigated by TEM, no structural defects have been found along the entire NW lengths. In most of the NWs, nanocrystals at the NW tips reveals that they contain a substantial amount of Sn. Two distinct phases of the Au–Sn intermetallic alloy: Au\textsubscript{0.5}Sn\textsubscript{0.5} and Au\textsubscript{0.85}Sn\textsubscript{0.15} have been identified in the solidified catalyst droplet. Both phases are liquid at the NW growth temperature (400 °C–440 °C) which indicates that the NWs grow in the VLS mode. In the majority of NWs, thoroughly investigated by TEM, no structural defects have been found along the entire NW lengths. In most of the NWs, we observed the oxidation of the NW sidewalls. Only the very top parts of the NWs 40 nm–80 nm below the catalyzing metal nanoparticles are not oxidized due to protection by the very thin Au-rich film. This points out the necessity of depositing protective shells on the SnTe NW sidewalls in view of studying the transport phenomena involving charge carriers occupying topologically protected states residing at four SnTe(100) NW sidewalls.

Conflicts of interest

There are no conflicts to declare.

Acknowledgements

This work has been supported by the research projects no. 2014/13/B/ST3/04489, 2014/15/B/ST3/03833, 2016/21/B/ST3/03411, and 2017/25/N/ST5/02942 financed by the National Science Centre (Poland). The authors thank the Institute of Electronic Materials Technology and Dr W. Strupiński for graphene/SiC substrates.

References

27 W. Norimatsu and M. Kusunoki, *Transitional structures of the interface between graphene and 6H-SiC (0 0 0 1)*, 2009, vol. 468, pp. 52–56.