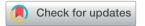
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# Enhancement of organic field-effect transistor performance by incorporating functionalized double-walled carbon nanotubes†

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Excellent electrical properties and large-scale fabrication are essential for extending the application of flexible organic electronics in practice. Organic semiconductor materials usually suffer from low charge carrier mobility, while carbon-based materials, such as graphene and carbon nanotubes (CNTs), often exhibit a rather low on/off ratio. Incorporating carbon-based materials into organic field-effect transistors (OFETs) is expected to combine their advantages. However, the dispersity of CNTs in organic semiconductors is rather poor, leading to a limited development of the hybrid devices. In this work, we overcame the challenge by subtly utilizing the advantages of covalently functionalized double-walled carbon nanotubes (f-DWCNTs). The f-DWCNTs can be well dispersed in solutions of organic semiconductor with a wide concentration range, which also makes the hybrid devices solution processable. OFETs based on f-DWCNT and an organic semiconductor hybrid exhibited higher sourcedrain currents as compared with that based on an organic semiconductor only. In addition, the threshold voltage of the OFETs decreased obviously due to charge injection enhancement by the f-DWCNTs. The comprehensive electric performance of the hybrid OFETs were further optimized by adjusting the mixing ratio of the two materials. Therefore, we demonstrated that incorporating f-DWCNTs into organic semiconductors is a simple and effective route to enhance the electric performance of solutionprocessed OFETs, and this strategy is expected to further advance flexible organic electronics for practical applications.

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# Introduction

Organic field-effect transistors (OFETs) have attracted numerous research attention due to their great flexibility, lowcost and large-scale fabrication, and thus possess potential applications in flexible displays, wearable electronics and portable chemical sensors. 1-13 Nowadays, the practical application of OFETs is limited by the rather low charge carrier mobilities of organic semiconductors. On the other hand, carbon-based materials, such as graphene and carbon nanotubes (CNTs), are promising for flexible electronics because of their intrinsic high mobility and mechanical flexibility. 14-18 However, transistors based on such materials often suffer from high off-current. Many efforts have been made to solve this problem.19-21 For example, soft lithography and reactive-ion etching were adopted to cut fine lines into the carbon nanotube networks, and the obtained devices presented sufficiently high current on/off ratio, although the fabrication process is rather complicated.22 To simply realize OFETs with both high

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mobility and current on/off ratio, incorporating organic semiconductor with carbon-based material in active layer of the device can be a promising strategy.

Hybrid materials are allowed to utilize the advantages of each constituents and make up for weak points of each other. For instance, by adding small amounts of single-walled carbon nanotubes, charge injection in bottom contact/top gate polymer field-effect transistors was generally enhanced, and thus lead to low onset voltages for the hybrid device.23 However, the performances of hybrid devices are strongly affected by the microphrase separation between different components and transport barriers and traps of charge carrier at the constituents interface. Interfacial modification is thus needed to improve the compatibility of the components by altering their hydrophilicity, surface charge, surface energy and reactivity and so on. As for CNTs, covalent functionalization on the wall allow improvement of their dispersity in solution and their compatibility with organic semiconductors, but the charge carrier density and effective mobility of CNTs could be significantly reduced at the same time. 17,24,25 A special case of CNTs is the double-walled carbon nanotube (DWCNT), which consists of two coaxially aligned single-walled carbon nanotube (SWCNT). Heavy covalent functionalization on SWCNT would significantly decrease or completely eliminate its conductance by

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introducing defects onto the wall, while the unvarnished inner

wall of DWCNT allows to retain the electric performance of the material after functionalization.<sup>25-28</sup> Functionalized DWCNTs (f-DWCNT) possess combined advantages of decent electric performance and compatibility with organic semiconductors, and thus can be adopted to incorporate with organic semiconductors.

In this work, we demonstrate a solution-processed OFET with significantly enhanced electric performance by incorporating f-DWCNTs into organic semiconductors. Due to the functional groups, f-DWCNTs can be well dispersed in the solution of organic semiconductor. Homogeneous suspension is thus obtained and solution-processed fabrication of the hybrid device is allowed. Compared with pure organic semiconductor FETs, the organic semiconductor/f-DWCNTs hybrid FETs exhibited 4 times higher mobility while keeping sufficient on/off ratio. These improved results can be attributed to that the unvarnished inner wall acted as conducting bridges between the crystalline regions of the semiconductor film while the functionalized outer wall took responsibility for well-dispersed in semiconductor film. By varying the ratio of f-DWCNTs to organic semiconductors, the optimized electric performance of the hybrid device were further obtained. Therefore, by incorporating f-DWCNTs into organic semiconductors, this work provide a broad applicable and simple strategy to fabricate solution-processed and flexible electronics with combined advantages of high mobility and high current on/off ratio.

# **Experimental**

#### Materials

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DWCNTs (below 5 nm (diameter), 5-15 micrometer (length)) were purchased from Tokyo Chemical Industry CO., LTD. and a purification process was applied. The mixed solution of hydrochloric acid (1 M) and hydrogen peroxide (30%) treatment (volume ratio 1:1, 70 °C, 2 h) was carried out to remove metal catalysts, followed by air oxidation at 420 °C for 1 h to remove carbonaceous impurities. Fig. S1a and b† show the transmission electron microscope (TEM) images of DWCNTs before and after purification, respectively. Fig. S1c and d† show highresolution TEM images of a single DWCNT. C8-BTBT was purchased from Suna Tech Inc and used as received. Octadecyltrichlorosilane (OTS) were purchased from Sigma-Aldrich. Diazonium salts and F8T2 were synthesized according to previous literatures reported, respectively.25,29

#### **Functionalization of DWCNTs**

50 mg purified DWCNTs were dispersed in a 50 ml of aqueous 1 wt% sodium dodecylbenzenesulfonate (SDBS) by tip-sonication (JY92-IIDN, Scientz) for 1 h. Then 4-(tert-butyl)benzenediazonium was added into the DWCNTs dispersion and stirred in dark at room temperature for 6 hours. The functionalized DWCNTs were collected by vacuum filter and washed with ethanol and ultrapure water.

#### Fabrication of two-electrode devices of DWCNTs

f-DWCNTs and DWCNTs were dispersed respectively in aqueous 1 wt% sodium dodecyl benzene sulfonate (SDBS) by tip-sonication for 1 h. 20 ml of diluted dispersions (DWCNTs: 4 × 10<sup>-4</sup> mg ml<sup>-1</sup>) were filtered through mixed cellulose ester microporous filter membranes (VMWP04700, Merck Millipore Ltd., pore size of 200 nm) and rinsed with ultrapure water to remove surfactant. The resulting percolated DWCNT films along with the filter membranes were transferred onto silicon oxide surfaces by thermal pressing. After cooling, the MCE filter membranes was dissolved sequentially using an acetone bath for 30 min, a dimethyl sulfoxide (DMSO) bath for 30 min, a methanol bath for 30 min, then rinsed with acetone and ethanol. The two electrodes were thermally evaporated onto the DWCNTs film with the spacing of 0.2 mm.

#### **OFET devices fabrication**

All the OFET devices were fabricated on heavily doped Si wafers with 300 nm of thermally grown SiO<sub>2</sub> layers. Si wafers with OTS treatment and oxygen plasma (Plasma Cleaner PDC-32G) treatment were used to fabricate poly-9,9'-dioctyl-fluorene-cobithiophene(F8T2)-based OFETs and 2,7-dioctyl[1]benzothieno [3,2-b]benzothiophene(C8-BTBT)-based OEFTs, respectively. (i) F8T2/DWCNTs films: (1) the dispersions were tip-sonicated for 1 h and then added to solutions of the F8T2 with high concentration (10 mg ml<sup>-1</sup> F8T2 in 1,2-dichlorobenzene), followed by ultrasonication for 15 min. F8T2/DWCNTs dispersions were spin-coated onto the substrates at 1000 rpm for 1 min to form semiconductor layers. (ii) C8-BTBT/DWCNTs films: the f-DWCNTs and DWCNTs were added respectively into chlorobenzene at a concentration of 0.5 mg ml<sup>-1</sup>. The mixture were tip-sonicated for 1 h and then more chlorobenzene was added to form various dispersion concentrations. C8-BTBT was added into the dispersions at 5 mg ml<sup>-1</sup>, followed by ultrasonication for 5 min. Semiconductor layers were formed by spin coating at 3000 rpm for 40 s. Finally, 50 nm gold source-drain electrodes were thermally evaporated through a shadow mask to form topcontact OFET devices. The channel length and width are 0.2 mm and 6.0 mm, respectively.

#### Device characterization

Raman spectra were collected on a DXR Raman Microscope (Thermo Fisher Scientific) with excitation wave length of 633 nm. The surface morphologies of semiconductor/DWCNTs hybrid films are investigated by atomic force microscope (AFM, SEIKO SPA-300HV) operated in tapping mode. The morphologies of DWCNTs were observed by employing TEM (Hitachi H-600). All electrical characterization of OFETs were carried out in air with a Keithley 4200-SCS.

## Results and discussion

#### Device fabrication and DWCNTs functionalization

The devices were fabricated in a typical bottom gate/top contact OFET structure (schematic geometry in Fig. 1a). The gate electrode was a heavily doped Si wafer with 300 nm of thermally **RSC Advances** 

Au
Au
Au
H<sub>17</sub>C<sub>8</sub> C<sub>8</sub>H<sub>17</sub> F8T2
H<sub>17</sub>C<sub>8</sub> C<sub>8</sub>H<sub>17</sub> C8-BTBT

C8-BTBT

**Fig. 1** (a) Schematic diagram of the OFET device structure (left) and the chemical structures of the organic semiconductors, F8T2 and C8-BTBT, respectively (right). AFM images of (b) F8T2/f-DWCNTs film and (c) C8-BTBT/f-DWCNTs film.

grown SiO<sub>2</sub> layer as the gate dielectric. In this study we adopted two different organic semiconductors as their molecular structures shown in Fig. 1a (right), namely F8T2, a polymeric semiconductor, and C8-BTBT, a small molecular semiconductor. The substrates were pretreated by using either octadecyltrichlorosilane (OTS) or oxygen plasma treatment before the deposition of the organic semiconductors/DWCNTs hybrid layers. The OTS-treated wafers were used for F8T2-based OEFTs fabrication because SiO<sub>2</sub>/polymer interface with OTS selfassembled monolayers (SAMs) is allowed to enhance the charge carrier mobility in spin-coated F8T2 film.30 On the other hand, the oxygen plasma treated wafers were used for fabrication of C8-BTBT-based OEFTs due to the treatment can improve the adhesion between C8-BTBT/chlorobenzene solution and the substrate. After removing solvents and moisture by annealing and vacuuming, gold source and drain electrodes were deposited by thermally evaporation through a shadow mask.

As shown in Fig. 1b and c, f-DWCNTs were well dispersed in F8T2 film and C8-BTBT film, respectively, thanks to the good compatibility between the two materials. The functionalization of DWCNTs were realized by using 4-(tert-butyl)benzenediazonium as shown in Fig. 2a. Comparative Raman spectroscopy was carried out to study the pristine DWCNTs and f-DWCNTs (Fig. 2b). The integrated area of the disorder-induced mode (D band) around 1325 cm<sup>-1</sup> increased obviously for f-DWCNTs and the D/G ratio (peak area ratio between the D band and the G band) increased from 0.36 to 0.56, which indicate the outer wall has been successfully functionalized. The electrical conductivities of DWCNTs and f-DWCNTs were also investigated by fabricating and testing two-electrode configuration device. Compared to DWCNTs, f-DWCNTs had a substantial decline in current, indicating a significant decrease in electrical

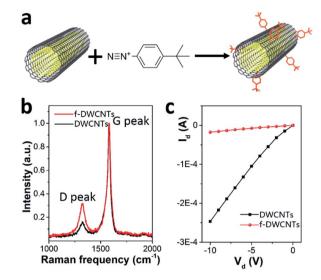


Fig. 2 (a) Schematic of DWCNT functionalization. (b) Normalized Raman spectra of DWCNTs before (black line) and after (red line) functionalization. (c) I-V curves of DWCNTs before (black line) and after (red line) functionalization.

conductivity (Fig. 2c). This reduced conductivity was due to degradation of the sp<sup>2</sup> hybridized framework of the f-DWCNTs outer wall. With long alkyl chain, the chemical groups could help DWCNTs disperse well in organic solvent. The dispersions of f-DWCNTs with three different chemical groups in chlorobenzene were also studied. 1 mg f-DWCNTs with carboxyphenyl group, 4-tert-butylphenyl group and phenyl group were added to 2 ml of chlorobenzene, respectively, followed by ultrasonication for 10 min. The dispersion of f-DWCNTs with 4-tert-butylphenyl group was better than that of f-DWCNTs with the other two chemical groups, as shown in Fig. S2b.† The f-DWCNTs with 4-tert-butylphenyl group were thus used to fabricate organic semiconductor/f-DWCNTs hybrid FETs.

#### Electrical performance of the hybrid OFETs

Fig. 3a shows transfer curves of F8T2 based pure OFET and hybrid OFETs, including F8T2/DWCNTs and F8T2/f-DWCNTs OFETs, with the drain voltage  $(V_d)$  fixed at -60 V. Both the hybrid OFETs exhibited higher source-drain current  $(I_d)$  and threshold voltage  $(V_{th})$  than those of pure OFET. The transportation of charge carriers in DWCNTs presents much longer mean free path as compared with that in organic semiconductors, which leads to a high  $I_d$  for the hybrid device. Meanwhile, hole injection in the P-type hybrid OFETs was also improved by incorporation of DWCNTs, and  $V_{th}$  of the devices was thus increased. On the other hand, the two kinds of hybrid OFETs exhibited obviously different current on/off ratios. The on/off ratio of F8T2/DWCNTs OFETs dropped a lot, while that of F8T2/f-DWCNTs device retained almost the same as that of F8T2 pure OFET. We suggest that the better performance of the F8T2/f-DWCNTs hybrid devices can be assigned to following reasons: (a) f-DWCNTs are allowed to be well dispersed in the solution of organic semiconductor because of their much lower surface energy as compared to DWCNTs, leading to a better

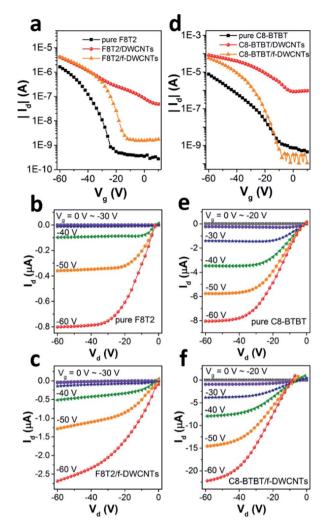


Fig. 3 (a) Transfer characteristics of pure F8T2 device, F8T2/DWCNTs device and F8T2/f-DWCNTs device. Source-drain I-V curves of (b) pure F8T2 device and (c) F8T2/f-DWCNTs device. (d) Transfer characteristics of pure C8-BTBT devices, C8-BTBT/DWCNTs device and C8-BTBT/f-DWCNTs device. Source-drain I-V curves of (e) pure C8-BTBT device and (f) C8-BTBT/f-DWCNTs device.

hybrid semiconducting film; (b) the benzene-based functional groups are allowed to form  $\pi$  bond conjugate system with organic semiconductor, and thus reduced the energy barrier for charge injection between f-DWCNTs and organic semiconductor. Fig. 3b and c shows the output characteristics of F8T2 based pure OFET and F8T2/f-DWCNTs OFET, respectively. The F8T2 based OFETs exhibited the electric performance of typical P-type devices, with both linear and saturation regime. The F8T2/f-DWCNTs OFET shown a much increased sourcedrain current compared with the pure F8T2 OFET in general, which gave further support to above conclusions. Fig. 3d shows the comparative transfer characteristics of C8-BTBT based pure OFETs and hybrid OFETs at a fixed  $V_{\rm d}$  of -60 V. The sourcedrain current obtained a significant increase after the organic semiconductor incorporating with DWCNTs, which was in the similar situation as F8T2 based devices. The C8-BTBT/f-DWCNTs OFET exhibited even a higher on/off ratio than C8-BTBT based pure OFET, with a comparable off-current but a much higher on-current. The source-drain off-current of C8-BTBT/f-DWCNTs OFET retained as low as that of C8-BTBT based pure OFET in the low gate voltage range, because small amount of well-dispersed f-DWCNTs might disturb the morphology of the crystalline C8-BTBT film, and at the same time they provided "fast lanes" for charge carriers. The "fast lane" could just neutralize the adverse effects of the disturbed morphology when the C8-BTBT/f-DWCNTs OFET was on the offstate. As a small molecule organic semiconductor, C8-BTBT film often exhibits high crystallinity, while polymer organic semiconductor F8T2 is semicrystalline.29-31 When small amount of f-DWCNTs were incorporated into C8-BTBT film, the crystallinity of C8-BTBT was more affected than that of F8T2, leading a low off-current of C8-BTBT/f-DWCNTs OFET. For the on-state, the C8-BTBT/f-DWCNTs OFET exhibited source-drain current about four times higher than that of C8-BTBT based pure OFET, as transfer curves shown in Fig. 3d and output I-V curves shown in Fig. 3e and f. Therefore, incorporating f-DWCNTs with organic semiconductor is an effective route to enhance the performance for both F8T2 based OFETs and C8-BTBT based OFETs.

To study the dependence of device performance on f-DWCNTs dispersion concentration, organic semiconductor/f-DWCNTs hybrid films containing various densities of f-DWCNTs were also investigated. The transfer curves of F8T2/f-DWCNTs OFETs with different F8T2/f-DWCNTs mass ratio were shown in Fig. 4a (linear scale) and Fig. 4b (log scale). When

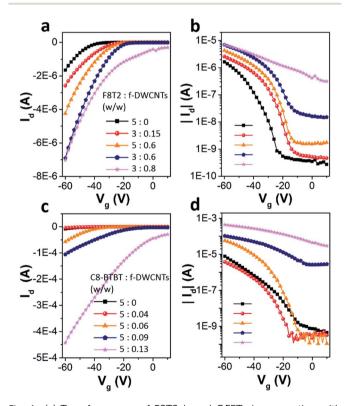


Fig. 4 (a) Transfer curves of F8T2-based OFETs incorporating with various amount of f-DWCNTs in (a) linear scale and (b) log scale. Transfer curves of C8-BTBT-based OFETs incorporating with various amount of f-DWCNTs in (c) linear scale and (d) log scale.

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a small amount of f-DWCNTs were added to F8T2, the device shown a slightly increased source–drain current ( $I_{\rm d}$ ). As more f-DWCNTs were added,  $I_{\rm d}$  continued to increase while the on/off ratio began to decrease. The on/off ratio even dropped to a low of 20 since too much f-DWCNTs were added, which exceeded the percolation threshold of f-DWCNTs and an unwanted f-DWCNTs network formed. It seemed like that the unwanted f-DWCNTs network was formed more easily as F8T2 reduced (comparison of orange line and blue line in Fig. 4b), leading to the decrease of on/off ratio. Fig. 4c and d show transfer characteristics of C8-BTBT based OFETs incorporating with different amount of f-DWCNTs. A small amount of f-DWCNTs added to C8-BTBT could barely enhance device performance while too much f-DWCNTs leaded to a drastic drop in on/off ratio. Therefore, the electric performance of the hybrid OFETs

can be optimized by adjusting the incorporating ratio of f-

DWCNTs to organic semiconductor, and f-DWCNTs should be

added below percolation threshold.

The device parameters of F8T2 based OFETs and C8-BTBT based OFETs are summarized in Table 1. Due to the morphology of organic semiconductor films was disturbed, the mobilities dropped slightly when a minimal amount of f-DWCNTs were added, as we discussed before. Then the mobilities increased as the f-DWCNTs were added more because f-DWCNTs were beneficial for charge carrier transport. However, the maximal values of mobility were not obtained while the maximal amount of f-DWCNTs were added. When the densities of added f-DWCNTs in organic semiconductor film were higher than the percolation threshold of f-DWCNTs, the on/off ratio dropped so low that the devices were not typical OFETs. Since the mobilities were extracted from OFET transfer curves, values of mobility might become less reliable when too

Table 1 Summary of parameters extracted from (a) F8T2, (b) C8-BTBT based OFETs incorporating with various amount of f-DWCNTs. Average mobilities and threshold voltages ( $V_{\rm th}$ ) were obtained from 10 OFETs

F8T2/DCB (mg ml <sup>-1</sup> )	f-DWCNTs/CB (mg ml <sup>-1</sup> )	Mobility cm <sup>2</sup> $(V^{-1} s^{-1})$	$V_{\mathrm{th}}\left(\mathrm{V}\right)$	On/off ratio
5	0	$0.0124 \pm 0.0014$	$-32\pm2$	5 × 10
3	0.15	$0.0116 \pm 0.0012$	$-23\pm2$	$4 \times 10^{3}$
5	0.6	$0.0146 \pm 0.0006$	$-17\pm1$	$3 \times 10^3$
3	0.6	$0.0213 \pm 0.0008$	$-14\pm1$	$3 \times 10^{2}$
3	0.8	$0.0103 \pm 0.0014$	$1\pm 2$	20

C8-BTBT/CB (mg ml <sup>-1</sup> )	f-DWCNTs/CB (mg ml <sup>-1</sup> )	Mobility cm $^2$ (V $^{-1}$ s $^{-1}$ )	$V_{\rm th}$ (V)	On/off ratio
-	0	0.005   0.003	24   4	$1 \times 10^{4}$
5	0	$0.065 \pm 0.002$	$-34 \pm 1$	
5	0.04	$0.022 \pm 0.006$	$-31\pm3$	$6 \times 10^3$
5	0.06	$0.270 \pm 0.059$	$-28\pm1$	$6 \times 10^5$
5	0.09	$0.132\pm0.035$	$1\pm13$	13
5	0.13	$0.262\pm0.055$	$21\pm 8$	5

much f-DWCNTs were added to the organic semiconductor. The obvious change of  $V_{\rm th}$  showed a shift from negative to positive, which implies that charge injection was enhanced continuously as the amount of f-DWCNTs increased.<sup>32</sup> Incorporating f-DWCNTs into organic semiconductor showed a significant impact on device behavior, and the degree of the impact is directly related to the amount of added f-DWCNTs.

## Conclusions

In conclusion, we have demonstrated that by incorporating a suitable amount of f-DWCNTs into organic semiconductors, including both a polymeric semiconductor F8T2 and a small molecular semiconductor C8-BTBT, the electric performances of OFETs were significantly improved. As compared to unfunctionalized DWCNTs, the lower surface energy of f-DWCNTs allows them to be well-dispersed in solutions of organic semiconductor. Sufficient amount of f-DWCNT are thus allowed to be embedded into the semiconducting film to significantly increase the mobility, without obvious aggregation and decrease in the current on/off ratio of the device. Compared to conventional OFETs, the output source-drain currents of F8T2/f-DWCNTs and C8-BTBT/f-DWCNTs hybrid FETs could be increased, while the current on/off ratios of the hybrid devices retained comparable or exhibited even better than that of the pure OFETs. Meanwhile, the threshold voltage of the OFETs decreased due to charge injection enhancement by the f-DWCNTs. Hybrid OFET with optimized electric performance was then obtained through adjusting the incorporation ratio of the two materials. Therefore, incorporating organic semiconductors and f-DWCNTs into OFETs as active layers could be a broad applicable strategy to enhance the OFETs electric performances, which is expected to further reduce the distance from practical applications of flexible electronics.

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