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Realization of uniaxially strained, rolled-up monolayer CVD graphene on a Si platform via heteroepitaxial InGaAs/GaAs bilayers

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III–V semiconductor/graphene tubular structures with diameters of 4.5–5.4 μm have been fabricated on a silicon platform by rolling up monolayer CVD graphene together with heteroepitaxial InGaAs/GaAs bilayers. Scanning electron microscopy (SEM) reveals that transferred graphene adheres to the wall of the Si-based InGaAs/GaAs microtube. Micro-Raman spectroscopy measurements show remarkable redshifts of the G and 2D bands of graphene after planar graphene totally rolls up, reflecting that rolled-up graphene is under uniaxial tensile strain and the strain originates from the rolled-up InGaAs/GaAs microtube. We also fabricated GaAs-based III–V semiconductor/graphene tubular structures with diameters of 3.7 and 4.7 μm , respectively, thus finding an approach to graphene strain engineering (*i.e.*, the Raman redshift and tensile strain of rolled-up graphene increase with the decrement of microtube diameter). Obviously, assembling strained graphene with III–V semiconductors in rolled-up form on a Si platform will bring about a variety of Si-based electronic and optical applications in the future.

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Introduction

Graphene, a two-dimensional (2D) form of sp^2 -bonded carbon atoms, has sparked great attention and tremendous research interest since it was discovered in 2004.¹ Due to the fact that graphene possesses unique mechanical,² electrical,³ optical,⁴ magnetic⁵ and thermal⁶ properties, a variety of exciting applications of graphene are being vigorously explored by academia and industry. Particularly, the intrinsic properties of graphene can be tuned by heteroatom doping,⁷ zigzag nanoribbons,⁸ applied strain,⁹ heterojunctions,¹⁰ and nanocomposites,¹¹ which is very important for exploiting new phenomena for advanced graphene applications.

In recent years, the integration and interaction of graphene with conventional semiconductors, especially GaAs, has aroused great interest. 18.5% efficient graphene/GaAs van der Waals heterostructure solar cells,¹² a transmission filter based on stacked graphene/GaAs/graphene layers operating at the 1.55 μm wavelength¹³ and other applications have already investigated. Meanwhile, different kinds of interactions between graphene and GaAs have been studied, including electron–hole pairing in graphene/GaAs heterostructures,¹⁴ the phonon assisted cyclotron resonance effect in single-layer graphene on GaAs,¹⁵ piezoelectric surface acoustical phonon amplification in graphene on GaAs,¹⁶ magneto-transport properties of exfoliated graphene on GaAs.¹⁷ Related potential devices utilizing these interactions, such as

analog to digital converters, topologically protected quantum bits, acoustical phonon amplifier and frequency tunable acoustical phonon device, were also imagined. All these research studies show us the tremendous advantages of integrating strained graphene with conventional semiconductors.

On the other hand, as a type of building block for 3D micro-/nano-architectures, self-rolled-up tubes also stimulate significant interest of researchers. Generally, two or more stacked strained layers are grown on the top of a sacrificial layer (SCL), followed by releasing from substrate and forming tubular structure through the selective etching of SCL. Self-rolled-up tubes have naturally hollow channel as well as good controllability of their diameter, length and wall. A variety of applications including electrically-injected tube laser,¹⁸ field effect transistors,¹⁹ fluidic sensing²⁰ and lithium ion batteries (LIBs),²¹ have been envisioned and realized by self-rolled-up tubes.

Recently, physically combining graphene with rolled-up tubes, *i.e.*, producing homogeneously curved graphene structures through a nonlocal bending method, has been actively pursued. Several kinds of tubular hybrid graphene heterostructures have been reported. For example, Chen *et al.* reported strain-driven formation of multilayer rGO/GeO₂ microtubes, which can serve as durable anodes with satisfactory cycling ability and high capacity for lithium ion batteries.²² Liu *et al.* designed the sandwich nanoarchitecture of rolled-up Si/rGO bilayer nanomembranes, improving the cycling performance of silicon anodes of LIBs through rGO conductive protecting layers.²¹ In particular, Barcelos *et al.* produced rolled-up InGaAs/Cr/Graphene microtube on GaAs substrates and indicated that rolled-up graphene is free of strain, which implies interaction of

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graphene with additional materials on the heterostructure is very weak.²³ Obviously, such combining approach has the advantage of keeping or tuning the tubular graphene heterojunctions in a well-controlled fashion. However, there still remain some underlying drawbacks in terms of future applications. First, use of rGO in rolled-up nanomembranes can hardly avoid introducing defects, which is unfavorable for some rolled-up graphene devices. Secondly, opening and tuning bandgap of as-obtained strain-free CVD-grown graphene through applying uniaxial stress is almost impossible.

In this study, III–V semiconductor/CVD-grown-graphene tubular structure arrays have been integrated on silicon platform. Scanning electron microscope (SEM) clearly shows the attachment between III–V semiconductor and graphene. More inspiringly, significant Raman redshifts of G and 2D bands are observed from rolled-up graphene. Through analysis, we conclude that rolled-up graphene is under uniaxial tensile strain, which originates from the strain relaxation during self-rolling process. After that, we fabricated GaAs based III–V semiconductor/graphene tubular structures with typical diameters of 3.7 μm and 4.7 μm and a regulation of the strain state of graphene by the tube diameter has been realized (uniaxially tensile strain of rolled-up graphene increase with the decrement of microtube diameter). Above results also prove the formation of III–V semiconductor/graphene heterojunctions. As far as we know, it is the first time to regulate strained graphene through the rolled-up forms. As-fabricated III–V semiconductor/strained-graphene tubular structures have great potential to be used in sensors, capacitors, microfluidics devices and *etc.* Most importantly, this study opens a smart path to combine III–V semiconductor with graphene on traditional Si platform leveraging the mature Si CMOS technology and low-cost large-area substrates, and undoubtedly bringing impact to Si-based electronics and photonics.

Experimental details

Si-based metamorphic InGaAs/GaAs heterostructures for rolling up microtubes were grown by low-pressure metal–organic chemical vapor deposition (LP-MOCVD). The layer stacks on Si (100) substrate consist of a 2 μm -thick GaAs buffer and a 50 nm-thick AlAs SCL, followed by a 15 nm-thick $\text{In}_{0.2}\text{Ga}_{0.8}\text{As}$ compressive-strained layer and a 35 nm-thick GaAs layer as top bilayer. The growth details can be found in our previous work.²⁴ Periodic rectangular patterns (lengths ranging from 50 μm to 250 μm , widths ranging from 30 μm to 70 μm), separated by 50 μm , were defined on the wafer along the [001] direction by first photolithography. After that, a shallow etch to AlAs sacrificial layer by $\text{H}_2\text{SO}_4\text{--H}_2\text{O}_2\text{--H}_2\text{O}$ (1 : 1 : 60) solution transferred the patterns into InGaAs/GaAs bilayers and formed the mesas (Fig. 1a). As shown in Fig. 1b, CVD-grown monolayer graphene flake with size of 1 cm \times 1 cm was then transferred to the top of wafer, the transfer details are described below. Subsequently, a secondary photolithography was performed to produce stripes with 80 μm in width, followed by oxygen (O_2) plasma etching of graphene, which exposes part of the mesas and provides the etching front (Fig. 1c and e). Finally, highly selective $\text{HF}\text{--H}_2\text{O}$

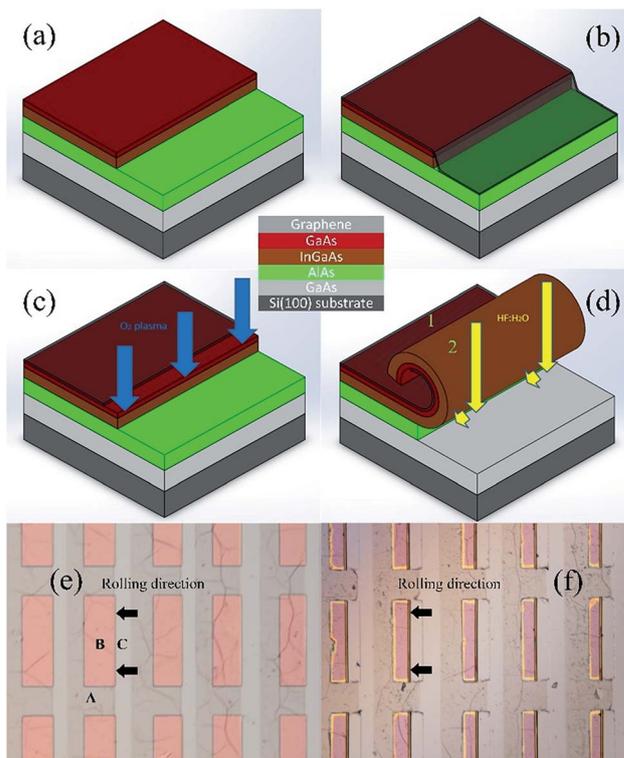


Fig. 1 Process flows for InGaAs/GaAs/graphene tubular structures. (a–d) Schematic illustrations of production steps: (a) shallow wet etching into AlAs forms the rectangular mesas. (b) Transferring CVD graphene onto the mesas realizes total coverage. (c) Dry etching of graphene by O_2 plasma exposes the sidewall of the mesa. (d) Lateral selective etching of AlAs SCL releases the layer stacks into tubular forms. Inset shows schematic diagram of the layer stacks. Number 1 and 2 in (d) represent the chosen measurement points for micro-Raman. (e and f) Optical microscopy images of unrolled planar and rolled-up InGaAs/GaAs/graphene tubes. Dark arrows indicate the rolling direction. Region A (translucent, wrinkled region), B (pink color) and C (grey color) represent graphene, graphene-capped InGaAs/GaAs bilayers and exposed AlAs, respectively.

(1 : 40) dilute solution started to laterally remove AlAs SCL, which rolled up the graphene with InGaAs/GaAs bilayer from the [010] direction due to strain relaxation (Fig. 1d and f). 15/35 nm and 10/20 nm $\text{In}_{0.2}\text{Ga}_{0.8}\text{As}$ /GaAs bilayers grown on GaAs (100) substrate were also used to roll up graphene in this study.

The monolayer graphene used in this study was synthesized on copper foils by chemical vapor deposition (CVD). A supporting layer of polymethyl methacrylate (PMMA) was then spin-coated onto the surface of graphene. Copper substrate was finally etched away from Cu/graphene/PMMA foils with Cu etchant, obtaining delicate graphene/PMMA stacks, followed by thorough rinsing in deionized water. After graphene/PMMA was transferred to GaAs/Si virtual substrate with minimized folding or tearing of graphene layer in DI water, pre-coated PMMA supporting layer was dissolved in acetone.^{25,26}

Optical microscope observation was used to evaluate photolithography quality and monitor the transfer process of CVD graphene. Scanning electron microscopy (SEM) measurements were carried out to characterize the morphological and



structural properties of the III–V semiconductor/graphene tubular structures. In order to investigate the optical properties of graphene and III–V semiconductors, room temperature (RT) micro-Raman spectra were collected using a Renishaw inVia confocal Raman spectrometer in back scattering geometry. The excitation wavelength was fixed to 532 nm and the laser power was 2 mW. Laser beam was focused on the sample by a 100 \times objective lens (numerical aperture = 0.85), resulting in a spot size of \sim 3 μ m in diameter.

Results and discussion

SEM visualization of highly-ordered InGaAs/GaAs/graphene tube arrays on Si platform is displayed in Fig. 2a. We note that as-fabricated tubes are quite homogeneous along the entire length, proving the perfect self-rolling process. Fig. 2b shows that a single InGaAs/GaAs/graphene tube with total length of 140 μ m rolls over the distance of \sim 20 μ m, which therefore determines the number of rotations according to the measured tube diameter. Simultaneously, planar areas are divided into different parts and then labelled in Fig. 2b according to their background colours. In addition, very smooth and crackless tube wall can be clearly observed in Fig. 2b. As shown in Fig. 2c, direct SEM view of rolled-up monolayer CVD graphene inside microtube was realized from the deliberately-broken position, clearly showing that monolayer graphene adheres to the inner wall of Si-based InGaAs/GaAs microtube, thus forming III–V semiconductor/graphene tubular heterojunction directly on silicon substrate. Fig. 2c also shows that the outer diameter of

InGaAs/GaAs/graphene microtube is \sim 5 μ m and the tube performs more than 1 rotation (two InGaAs/GaAs bilayer stacks can be seen). In addition, the windings of InGaAs/GaAs/graphene microtube are quite tight (*i.e.*, neighbouring bilayers are perfectly joined together), also demonstrating high quality of the rolling process. To further assess the quality of microtube arrays, we measured more than 40 tubes from the array by SEM and summarized the statistical results of the diameter into a histogram. As shown in Fig. 2e, diameter distribution of the tubes concentrating at about 4.9 μ m can be observed, indicating as-prepared microtube array is quite uniform. Diameter deviation can be interpreted by a slightly disturbed roll-up process.

Subsequently, we measured micro-Raman spectra from non-rolled-up and rolled-up parts (*i.e.*, point 1 and 2 in Fig. 1d) of a single Si-based InGaAs/GaAs/graphene microtube with diameter of \sim 5 μ m. As appearing very far away from that of III–V semiconductors (*e.g.*, GaAs, AlAs and InGaAs), Raman peaks of graphene can be easily distinguished from the full spectra. In order to clearly show their discrepancy, normalized Raman spectra of III–V semiconductors and graphene are individually shown in Fig. 3 and 4. Fig. 3 highlights the Raman spectra of Si-based rolled-up InGaAs/GaAs bilayer and its corresponding planar (with graphene), in which the longitudinal-optical (LO) phonon modes of top GaAs layer are identified as the strongest peaks around 287–292 cm^{-1} . LO phonon mode of AlAs SCL contributes to the small peak ranging at \sim 399 cm^{-1} in the spectrum of planar InGaAs/GaAs bilayer, which vanishes in the spectrum of rolled-up microtubes due to the complete etching off of AlAs after tube formation. Moreover, the broad and weak spectral features in the range of 260–270 cm^{-1} originate from the signals of transverse optical (TO) phonon modes of top GaAs layer. When the InGaAs/GaAs strained bilayer rolls up into microtube together with CVD graphene, -2 cm^{-1} redshift ($\Delta\omega \sim -2 \text{ cm}^{-1}$, from 289.3 to 287.3 cm^{-1}) and the increment of peak full width at half maximum (FWHM) of LO GaAs peak are simultaneously observed. The spectral redshift and broadening of LO GaAs Raman peak should be induced by the strain state transition of the top GaAs layer during rolling process.

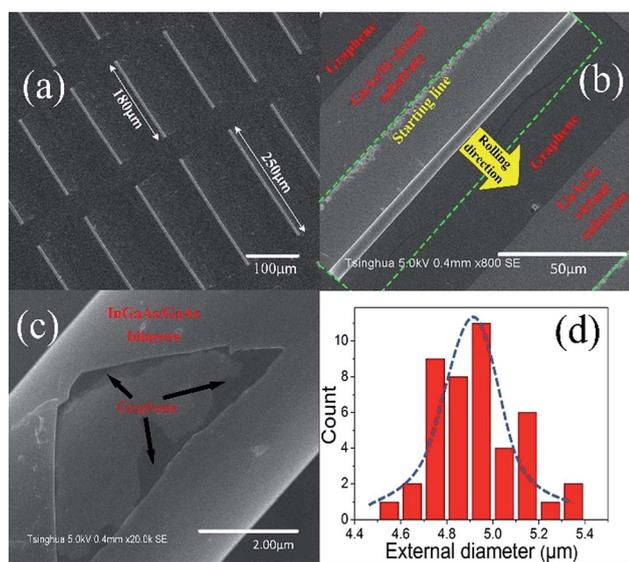


Fig. 2 (a) SEM image of highly-ordered arrays of rolled-up InGaAs/GaAs/graphene microtubes with two typical lengths of 180 and 250 μ m. (b) SEM image of a single InGaAs/GaAs/graphene tube with length of 140 μ m. The broken green lines show the border of rectangular mesas. (c) Magnified SEM image of deliberately-broken part of a single microtube with the outer diameter of 5 μ m. Transferred graphene was exposed and III–V semiconductor/graphene tubular heterojunctions can be clearly distinguished. (d) Histogram of the outer diameter of as-fabricated InGaAs/GaAs/graphene tubes measured by SEM.

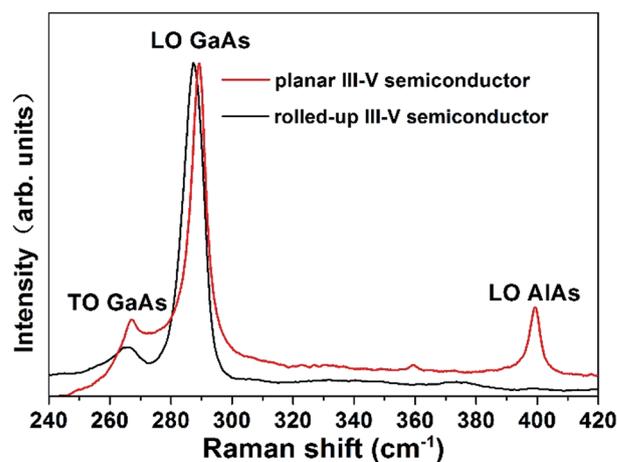


Fig. 3 Raman spectra of Si-based rolled-up InGaAs/GaAs bilayer and corresponding planar (with graphene on surface). TO GaAs, LO GaAs, and LO AlAs peaks are marked, respectively.



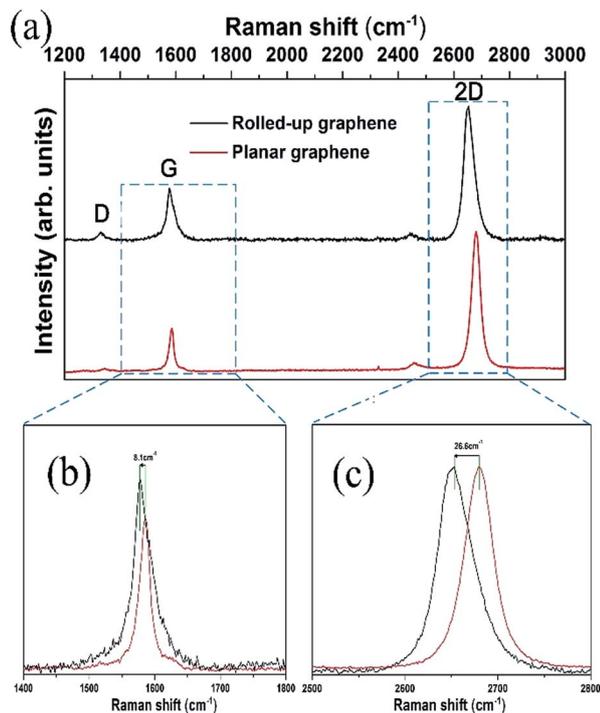


Fig. 4 (a) Raman spectra of planar (red curve) and rolled-up graphene (black curve). G, 2D and D band peaks are labeled. (b and c) Zoom-ins of Raman spectra around G and 2D band, showing the redshift after planar graphene rolls up into microtube with a diameter of 5 μm .

Raman spectra of planar (non-rolled-up) and rolled-up graphene are simultaneously illustrated in Fig. 4a, from which sharp G and 2D bands as well as very weak D bands (defect-related) originating from monolayer CVD graphene flake are clearly visible. After the planar graphene rolls up into microtube, Raman intensity of graphene D band is slightly increased and meanwhile 2D/G intensity ratio (I_{2D}/I_G) is slightly decreased from 3.2 to 2.5, from which we can identify that high-quality self-rolling process only introduces very few defects into graphene. Fig. 4b and c are the magnified parts of G and 2D bands of Fig. 4a, which clearly reveal the Raman shift of G and 2D bands after planar graphene totally rolled up. Surprisingly, average redshifts of G and 2D bands of graphene were up to -8.1 cm^{-1} (Δ_{G^-} , from 1585.2 to 1577.1 cm^{-1}) and -26.6 cm^{-1} (Δ_{2D} , from 2679.8 to 2653.2 cm^{-1}), respectively. In addition, peak FWHM of G band increases from 18.5 to 29.2 cm^{-1} , that of 2D band from 36.8 to 44.9 cm^{-1} . Mohiuddin *et al.* have reported that G band of graphene was splitted into two subbands, (*i.e.*, G⁺ and G⁻ bands) when they applied uniaxial strain to graphene monolayer, and the splitting between G⁺ and G⁻ bands increases with the ramp up in strain.²⁷ In our study, asymmetric G band of rolled-up graphene rather than obvious splitting of G band is observed (see black curve in Fig. 4b). In particular, the wavenumber redshift ratio (Δ_{2D}/Δ_{G^-}) is calculated to be ~ 3.3 after planar graphene rolled-up, which just falls in the range between Δ_{2D}/Δ_{G^+} of ~ 5.9 and Δ_{2D}/Δ_{G^-} of ~ 2.0 derived by Mohiuddin's results. Moreover, the broadening of FWHM along with strain was also observed by Mohiuddin. Considering that

rolled-up and planar graphene are formed in the same process flow and corresponding Raman spectroscopy for graphene are probed under the identical conditions, we thus believe that rolled-up graphene is also under tensile strain which accounts for the redshift and the broadening of G and 2D bands of graphene in this study. Finally, the experimental phenomena that there is no observation of Raman G-band splitting may be attributed to insufficient tensile strain loaded to graphene by 15/35 nm InGaAs/GaAs bilayer.

As the Raman shift of 2D band dominates over that of G band after planar graphene is rolled-up, we have further measured 2D bands of graphene from more than 40 Si-based InGaAs/GaAs/graphene microtubes and summarized the peak wavenumber of 2D bands into a histogram (Fig. 5c), by which we have analyzed the strain state of the rolled-up graphene in detail. As shown in Fig. 5c, a bimodal Gaussian distribution of 2D band peaks of rolled-up graphene can be distinctly observed. Due to the fact that rolled-up graphene contacts with GaAs *via* van der Waals force and has monomodal diameter distribution (refer to Fig. 2d), we think this bimodal Gaussian distribution can be well explained by two types of typical contact intensity between rolled-up graphene and top GaAs layer. We used two Gaussian curves to fit the envelope of the histogram in Fig. 5c and got two peaks. Left peak around 2652 cm^{-1} , named as GP₁, manifests that rolled-up graphene perfectly contacts with InGaAs/GaAs bilayer and thus sustains all the tensile strain

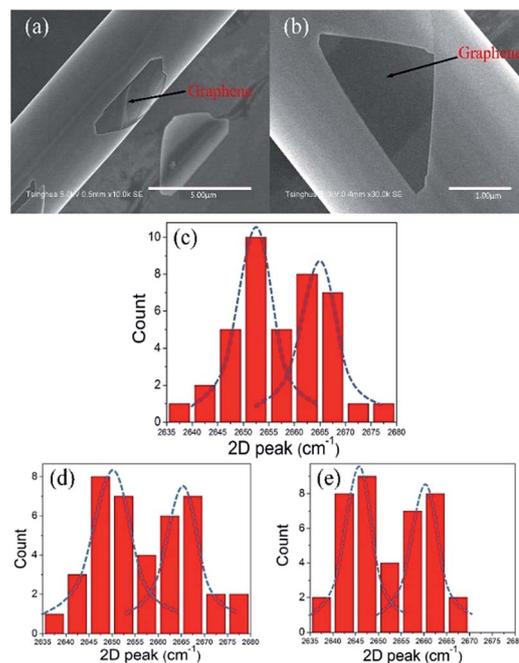


Fig. 5 (a and b) SEM images of GaAs-based InGaAs/GaAs/graphene microtubes fabricated by 15/35 nm (a) and 10/20 nm (b) strained bilayers, respectively. The outer diameter of microtube is 4.7 μm (a) and 3.7 μm (b), respectively. Transferred CVD graphene was exposed by deliberately breaking InGaAs/GaAs bilayers of the microtubes and graphene wrinkle can be observed. (c–e) Histogram of peak wavenumber of graphene 2D bands which originate from Si-based (15/35 nm) and GaAs-based InGaAs/GaAs/graphene microtubes (15/35 nm, 10/20 nm), respectively.



loaded by rolled-up GaAs. While, the other co-existing Gaussian peak (GP₂), situating around 2665 cm⁻¹, suggests that rolled-up graphene with GaAs is not very tight due to the surface wrinkles of graphene.

We also fabricated GaAs-based InGaAs/GaAs/graphene microtubes using 15/35 nm and 10/20 nm In_{0.2}Ga_{0.8}As/GaAs strained bilayers, respectively. Typical diameters of 4.7 μm and 3.7 μm can be calculated from SEM images shown in Fig. 5a and b. Moreover, we deliberately broke the outer wall of the GaAs-based microtube, enabling rolled-up CVD graphene flake exposed and easily distinguished from InGaAs/GaAs bilayer. We summarized Raman peak distribution of graphene 2D band measured from several tens of GaAs-based tubes. As shown in Fig. 5d and e, bimodal Gaussian distribution of 2D band peaks of rolled-up graphene has been observed again. Both GP₁ and GP₂ peaks of the rolled-up graphene in microtube with diameter of 3.7 μm redshifted ~5 cm⁻¹ compared with the corresponding peaks in microtube with diameter of 4.7 μm, reflecting that uniaxial strain of roll-up graphene is increased with the increase of microtube curvature. Obviously, the dependence of the band shift of rolled-up graphene on the diameter should be valid in Si-based InGaAs/GaAs/graphene microtubes. Subsequently, we carefully examine Fig. 5c and d and then find that GP₁ of Si-based tubes fabricated by 15/35 nm InGaAs/GaAs metamorphic bilayer was slightly blueshifted when compared with that of GaAs-based tubes using the same bilayer, while GP₂ almost unchanged. This phenomenon can be interpreted from the point view of surface roughness of inner wall of microtube. Due to the intrinsic limitations in GaAs/Si metamorphic growth, GaAs-based InGaAs/GaAs bilayer has much smoother top GaAs surface than its Si-based counterpart. As GP₁ peak originates from the tight attachment of rolled-up graphene onto the top GaAs, higher surface roughness of GaAs has significant effect on the GP₁ peak, *i.e.*, accommodating parts of strain energy and then making the strain decrease of rolled-up graphene. For GP₂, the influence of surface roughness of top GaAs will be highly overridden by surface wrinkle of rolled-up graphene.

Conclusions

In summary, we have realized 3D III-V semiconductor/strained-graphene tubular nanostructures on Si platform through simply rolling CVD graphene with InGaAs/GaAs bilayers. Tubular heterojunctions which are formed by the attachment of graphene and III-V semiconductor have been demonstrated by the direct SEM visualization. By analyzing micro-Raman spectra of graphene and GaAs, we can prove that uniaxial tensile strain is applied onto rolled-up monolayer CVD graphene and the strain value of rolled-up graphene can be regulated by the diameter of InGaAs/GaAs/graphene microtube. Importantly, as graphene is easily functionalized and microtube can provide a native microfluidic channel, obtained III-V semiconductor/graphene tubular structure has great potential of applications in biosensors, gas sensors, MEMS/NEMS, lab-in-a-tube system and *etc.* Meanwhile, graphene is located inside of the microtube rather than directly exposed to the atmosphere, which can improve the long-term stability of some applications based on tubular heterojunctions.

Finally, integration of strained graphene with traditional semiconductors on silicon platform in such an easy way will make some envisaged electronics and photonics applications true.

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