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Dually crosslinkable SiO₂@polysiloxane core–shell nanoparticles for flexible gate dielectric insulators†

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A hybrid gate dielectric material for flexible OTFT is developed by using core–shell nanoparticles (SiO₂@PSR_{XL}) where the core and the shell consist of silica nanoparticles and polysiloxane resin, respectively. Since polysiloxane resin contains both thermal- and photo-crosslinkable functional groups, densely-crosslinked thin gate dielectric films can be easily prepared on various substrates by conventional solution casting followed by dual crosslinking. SiO₂@PSR_{XL} films exhibit high thermal stability (weight loss at 300 °C is smaller than 3 wt%). The dielectric films made of SiO₂@PSR_{XL} show an exceptionally low leakage current and no breakdown voltage up to 4.3 MV cm⁻¹, which are comparable to those of silica dielectrics prepared by CVD. OTFT devices based on dibenzothiopheno[6,5-b:6',5'-f]thieno[3,2-b]thiophene (DTBTT) as a semiconductor and SiO₂@PSR_{XL} as a gate dielectric exhibit good hole mobility (2.5 cm² V⁻¹ s⁻¹) and I_{on}/I_{off} ratio (10⁶).

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1. Introduction

Organic thin film transistors (OTFTs) represent a promising alternative to silicon-based transistors, applicable to low-cost, flexible and large-area electronic devices including radio-frequency identification (RFID) tags, electronic papers, and backplane circuits for active-matrix displays. Remarkable research interest has been directed towards the development of solution-processable organic gate dielectrics as well as organic semiconducting materials.¹ Since many electrical properties of OTFTs such as carrier trapping, charge doping, molecular reorientation, dipole formation, and chemical reactions occur at the interface between the semiconductor and gate dielectric,^{2–6} engineering of gate dielectrics is pivotal for improving the performance of OTFTs. While the SiO₂ gate dielectric meets the strict demands for use in inorganic devices, its use in OTFTs is hindered by the need of vacuum deposition techniques. To this end, a few solution-processable polymers including poly(4-vinylphenol) (PVP),⁷ polyimides (PI),^{8,9} poly(methyl methacrylate) (PMMA),¹⁰ poly(vinyl alcohol) (PVA)¹¹ and others^{3,12–18} have been demonstrated for organic gate dielectrics. Organosiloxane compounds also have been of

interest as a candidate of the solution-processable gate dielectric material because of their structural similarity to SiO₂ when they are condensed to polysiloxane.^{19–23} Various polymer/nanoparticle hybrid gate dielectrics also have been investigated.^{24–32} Although the performance of organic thin-film transistors (OTFTs) using some of the polymer dielectrics is comparable to those using SiO₂ dielectrics in terms of charge mobility,^{7,14,15,33} many problems including large hysteresis of threshold voltage, high leakage current, and high operating voltage are not fully addressed yet.^{11,34,35}

In this study, we report a solution processable gate insulating material based on dually crosslinkable core–shell nanoparticles (SiO₂@PSR_{XL}). The SiO₂@PSR_{XL} nanoparticles consist of silica nanoparticle core and polysiloxane shell which contains thermal- and photo-crosslinkable functional groups. Because polysiloxane shells are chemically bounded on the surface of SiO₂ nanoparticles, SiO₂@PSR_{XL} nanoparticles can be well dispersed in common organic solvents and form transparent and uniform gate insulating films without nanoparticle aggregation. The core–shell geometry makes minimize the formation of interstitial voids and enhance the performance of the dielectric films comparable to the conventional SiO₂ dielectric layers formed by vacuum deposition. SiO₂@PSR_{XL} gate dielectric films were prepared by two-step reaction. As shown in Fig. 1, the chemical linkages between the surface of SiO₂ and polysiloxane were first created by hydrolysis/condensation, and then subsequently the residual epoxy and acrylate groups on the polysiloxane chains were crosslinked at the second stage. This two-step reaction leads the uniform and highly dense SiO₂@PSR_{XL} gate dielectric films. In this

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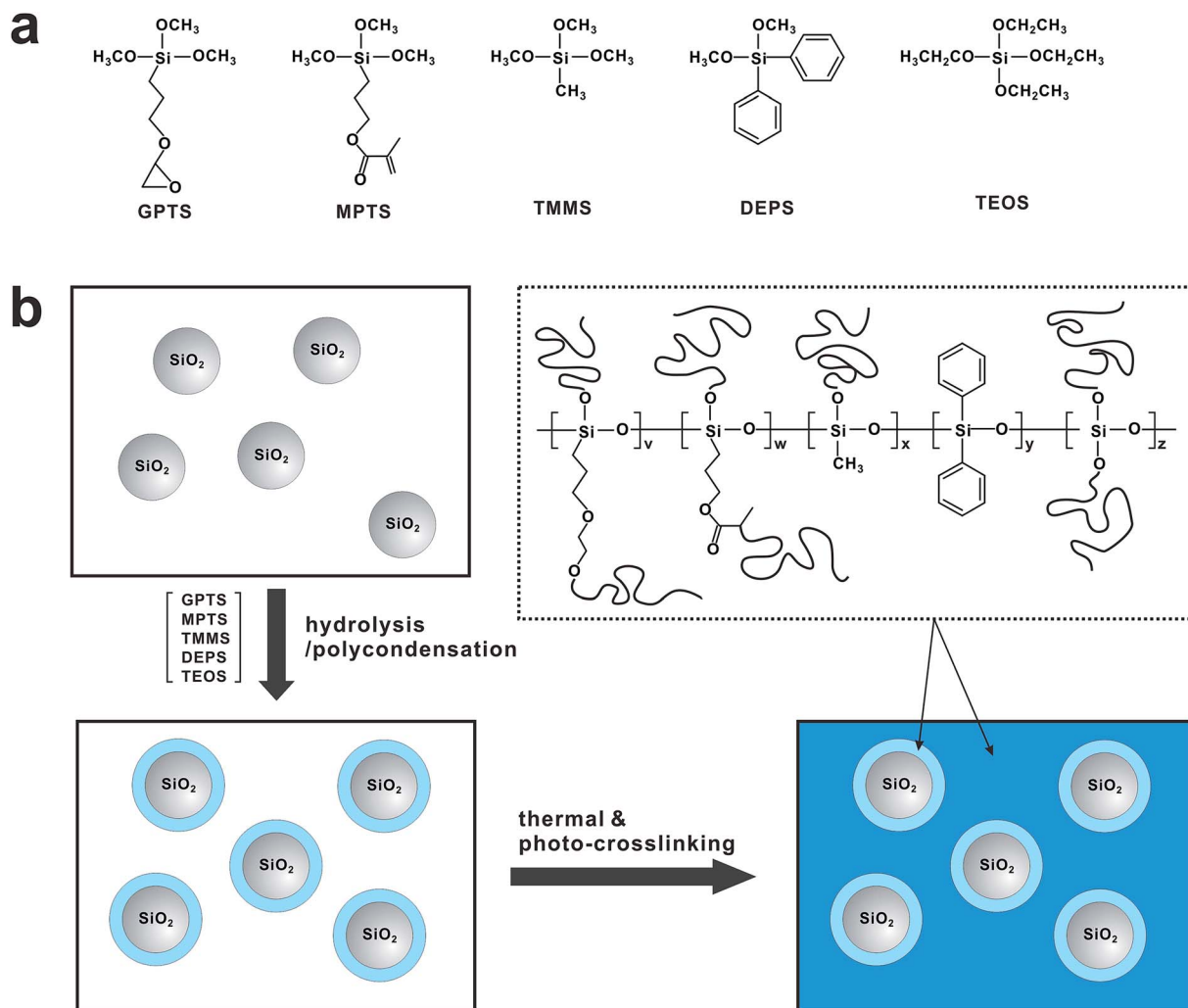


Fig. 1 (a) Chemical structure of siloxane monomers. (b) Synthetic scheme of $\text{SiO}_2@PSR_{XL}$.

sense, our work is different from other examples based on the polymer/nanoparticles composites.^{24–32}

2. Result and discussion

Homogeneous dispersion of silica nanoparticles in polysiloxane resin is pivotal for high performance of the gate dielectric films. To incorporate silica nanoparticles into the polysiloxane matrix, we have utilized the hydrolysis/condensation reaction between the hydroxyl groups on the surface of nanoparticle and siloxane monomers. Briefly, silica nanoparticles ($d_{\text{average}} = 10 \pm 5$ nm) was first treated with strong acid to make abundant hydroxyl groups on the silica surface. Those hydroxyl groups were then used to react with the monomer mixture including (3-glycidyloxypropyl)trimethoxysilane (GPTS), [3-(methacryloyloxy)propyl]trimethoxysilane (MPTS), trimethoxymethylsilane (TMMS), diethoxydiphenylsilane (DEPS) and tetraethoxysilane (TEOS). In this case, GPTS and MPTS were introduced to conduct thermal- and photo-curing at the second stage. Furthermore, the propyl side chains increase the flexibility. TMMS and DEPS were added to increase the mechanical flexibility and electrical properties of

the gate dielectric films, respectively.^{36–38} Finally TEOS was introduced to compensate the deficient reaction sites occupied by the phenyl groups of DEPS.³⁹ The composition of the monomer mixture was optimized to 15 mol%, 20 mol%, 35 mol%, 15 mol% and 15 mol% for GPTS, MPTS, TMMS, DEPS and TEOS respectively. The SiO_2 nanoparticle content was controlled to 20 wt% of the whole monomer mixture. Under the acidic condition, polysiloxane networks formed around the silica nanoparticles. In this case, the hydrolysis/condensation reaction condition was carefully controlled not to form gels by overshoot reaction. Viscosity of the resulting $\text{SiO}_2@PSR$ core-shell nanoparticle solution was 9.2 cps at 20 °C, and well dispersed in polar solvents such as propylene glycol monomethyl ether acetate (PGMEA). To check the extent of polymerization, a pure polysiloxane resin without nanosilica (PSR) was synthesized at the same condition, and the molecular weight was measured on a GPC ($M_w = 2200$ g mol⁻¹, PDI = 1.39).

The structure of $\text{SiO}_2@PSR$ was characterized by SEM and SAXS. As shown in Fig. 2a, SEM micrograph of the dried $\text{SiO}_2@PSR$ shows many isolated particulates with an average



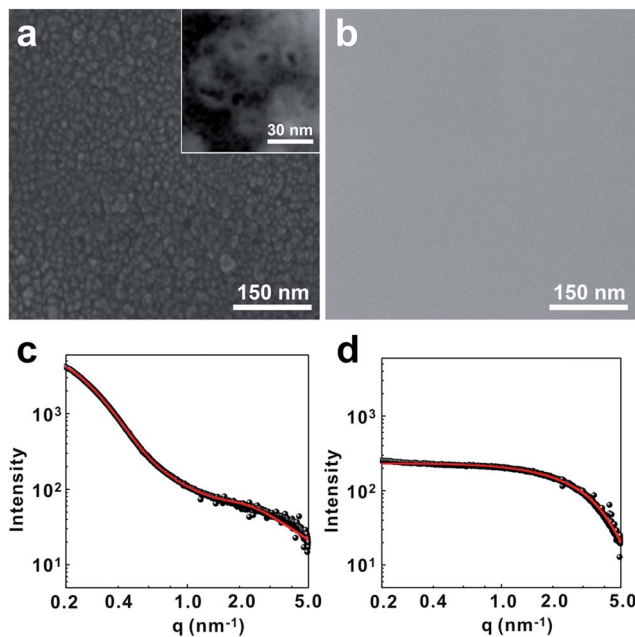


Fig. 2 SEM micrographs of (a) SiO_2 @PSR and (b) PSR. SAXS data of (c) SiO_2 @PSR and (d) PSR.

diameter of 28 ± 2.5 nm which is significantly larger than that of the original silica nanoparticles ($d_{\text{average}} = 10 \pm 5$ nm). Furthermore, nanoparticles show the core-shell structure with ~ 10 nm size of core. These results imply that polysiloxane networks dominantly form around SiO_2 nanoparticles by surface initiated polycondensation reaction. Unlike SiO_2 @PSR, pure PSR sample does not show particulates (Fig. 2b). To understand the structure of SiO_2 @PSR nanoparticles in solution, SAXS analysis has been done in the liquid phase. Fig. 2c and d show SAXS data of SiO_2 @PSR and PSR respectively. The shape and size of two samples were obtained from their scattering profiles. Pair-distance distribution function for each sample was obtained by using the indirect Fourier transform.²⁸ The calculated scattering curves from the distribution functions (red solid lines) show good agreement with the experimental data (black dots). SAXS analysis confirms that SiO_2 @PSR nanoparticles are circular particles with a diameter of 28 nm which is consistent with the previous SEM analysis. SAXS analysis also shows that the size of PSR is 2 nm.

Thermal properties of SiO_2 @PSR and PSR were characterized on a TGA. As shown in Fig. 3, SiO_2 @PSR showed the better thermal stability than PSR. The weight loss of SiO_2 @PSR (5.7 wt%) was smaller than that of PSR (8.8 wt%) at 350 °C. As increasing the temperature, the both weight curves of SiO_2 @PSR and PSR gradually decreased from the relatively low temperature (~ 150 °C), which indicates the presence of the unreacted residual monomers. Utilizing those many remnant reactive groups, we made dense dielectric films by using thermal- and photo-crosslinking reaction at the second-stage.

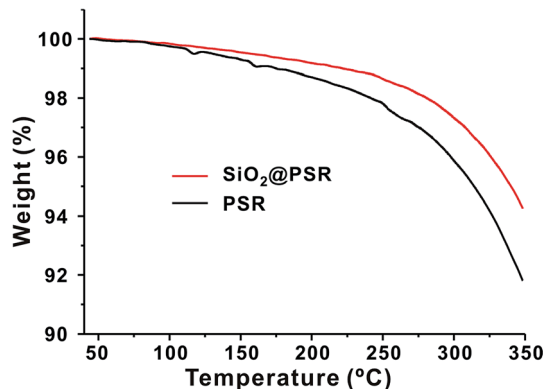


Fig. 3 TGA graphs of SiO_2 @PSR and PSR.

Since the SiO_2 @PSR can be well dispersed in polar organic solvents, uniform dielectric films can be easily prepared by spin casting. For preparing metal-insulator-metal (MIM) devices, dielectric films were first prepared on the molybdenum-coated glass slides by spin coating from a 35 wt% of SiO_2 @PSR solution in PGMEA. The film thickness was controlled to 500 ± 50 nm. The films were then crosslinked by thermal and photo annealing (the crosslinked SiO_2 @PSR film will be notated as SiO_2 @PSR_{XL}). Briefly, the SiO_2 @PSR films were first soft-baked at 70 °C for 15 min to activate the epoxy/hydroxyl groups, and simultaneously exposed to UV ($\lambda_{\text{band}} = 240\text{--}400$ nm) for 75 s to induce photo-crosslinking. To complete the crosslinking reaction, the films were annealed at 200 °C for 1 h under the nitrogen atmosphere. FT-IR analysis confirms that MPTS units polymerize during the photo-crosslinking process (Fig. S1†). The characteristic ethylene peaks of MPTS at 1637 cm^{-1} and 810 cm^{-1} disappeared, and the carbonyl peak was shifted from 1727 cm^{-1} to 1733 cm^{-1} after photo-crosslinking process. The disappearance of ethylene peak and the shift of carbonyl peak in FT-IR was attributed to the polymerization of acrylate groups of MPTS. After the crosslinking process, gold was evaporated on the films to make the top electrode. For comparison, MIM devices using pure PSR were also prepared following the same procedure.

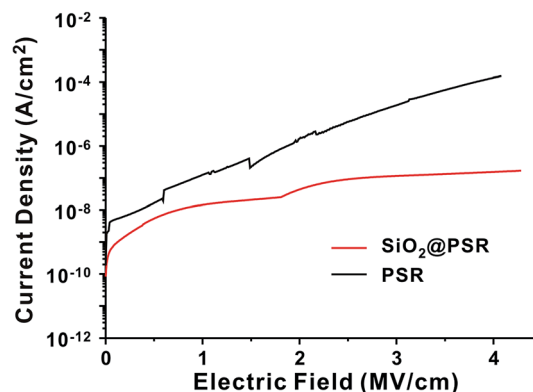


Fig. 4 The changes of leakage current density as a function of electric field for SiO_2 @PSR and PSR films.



The leakage current density of the MIM devices was measured as a function of electric field (Fig. 4). The MIM devices based on $\text{SiO}_2@\text{PSR}_{\text{XL}}$ showed very low leakage current, $\sim 10^{-8}$ A cm^{-2} at 2 MV cm^{-1} of the applied voltage, which is 2 orders lower than that of the MIM based on PSR_{XL} . No breakdown behavior was observed at least up to 4.3 MV cm^{-1} , which is significantly higher than that of other polymer or polysiloxane gate insulators typically showing breakdown in the range of 1.5–2.5 MV cm^{-1} .⁴⁰ In terms of the breakdown strength, our dielectric films based on $\text{SiO}_2@\text{PSR}_{\text{XL}}$ are comparable to the silica films prepared by CVD method.^{41,42} We attribute such high breakdown strength and low leakage current to the dense network structure made of core-shell-type $\text{SiO}_2@\text{PSR}_{\text{XL}}$ nanoparticles. The dielectric constant of $\text{SiO}_2@\text{PSR}_{\text{XL}}$ ($\epsilon = 4.26$ at 1 kHz) measured on the MIM device was comparable to that of PSR_{XL} ($\epsilon = 4.30$ at 1 kHz) (Fig. S2†).

$\text{SiO}_2@\text{PSR}_{\text{XL}}$ gate dielectric films were tested for the organic thin film transistor (OTFT). In our experiments, a thiophene-rich heteroacene derivative, dibenzothiopheno[6,5-*b*:6',5'-*f*]thieno[3,2-*b*]thiophene (DTBTT)⁴³ was used as an active material (Fig. 5a). DTBTT was synthesized following the previously reported procedures.⁴³ For the fabrication of OFET transistors, $\text{SiO}_2@\text{PSR}_{\text{XL}}$ films were first prepared on the glass substrates patterned with molybdenum gate electrodes with a thickness of 550 nm following the previously described procedures.⁴⁴ DTBTT was then vacuum-deposited on the $\text{SiO}_2@\text{PSR}_{\text{XL}}$ films. Finally, the gold source and drain electrodes were formed on the DTBTT

by using the conventional vacuum deposition technique. The channel width and length of the devices were 1000 and 100 μm , respectively. Fig. 5b represents a SEM micrograph of the cross-section of the device prepared by using focused ion beam (FIB) technique. It is notable that the gate insulator layer is very uniform and flat with considering that it was prepared by the wet coating process. For comparison, OFET devices based on PSR_{XL} were also prepared following the same procedures. As shown in Fig. 5c and d, the devices based on $\text{SiO}_2@\text{PSR}_{\text{XL}}$ showed much better performance than that of PSR_{XL} . The hole mobility and the $I_{\text{on}}/I_{\text{off}}$ ratio was 2.5 ± 0.13 $\text{cm}^2 \text{V}^{-1} \text{s}^{-1}$ and 10^6 respectively for the OFET device based on $\text{SiO}_2@\text{PSR}_{\text{XL}}$, while they were 0.9 ± 0.07 $\text{cm}^2 \text{V}^{-1} \text{s}^{-1}$ and 10^4 for the device based on PSR_{XL} . Furthermore, it was noticed that the off-current of the device based on $\text{SiO}_2@\text{PSR}_{\text{XL}}$ was lowered than the PSR_{XL} sample.

The fabrication process of $\text{SiO}_2@\text{PSR}_{\text{XL}}$ films is applicable for the plastic substrates (Fig. 6a). Similar to the above-mentioned procedures, $\text{SiO}_2@\text{PSR}$ solution was spun on the Al patterned polyimide substrate, and cured by heating and UV treatment. In this case, the annealing temperature was lowered to 60 °C while it was 200 °C for the fabrication on glass substrates. The thickness was same as 550 nm. AFM image showed that the RMS roughness increased slightly from 4.5 Å to 6 Å comparing with the sample prepared on glass (Fig. 6b). DTBTT was then deposited by thermal evaporation, and finally gold source/drain electrodes were evaporated thereto. The hole

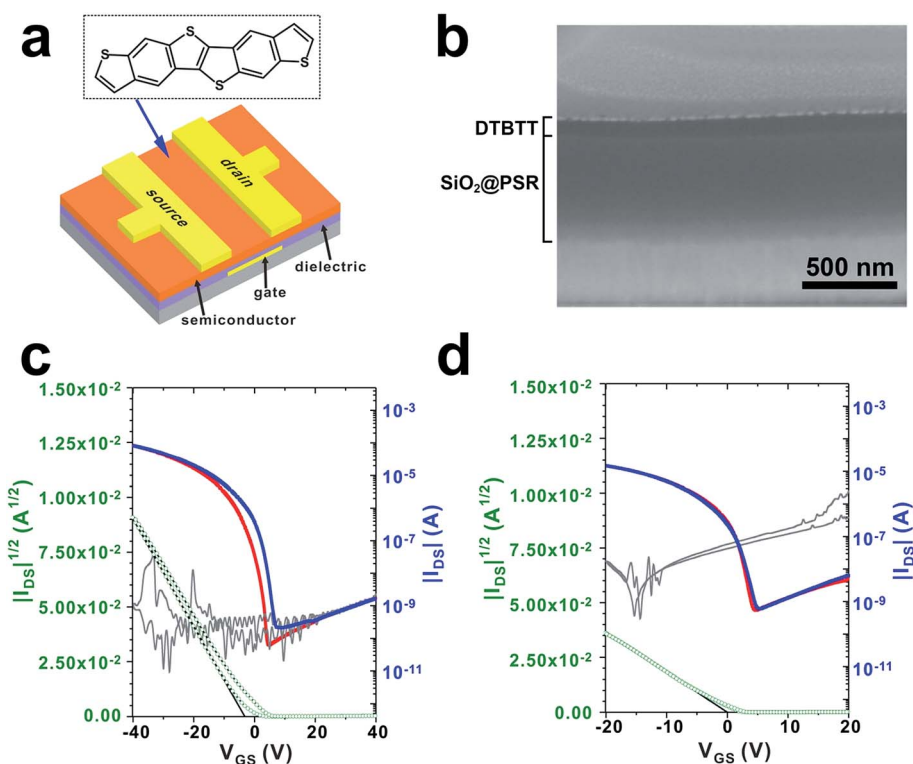


Fig. 5 (a) The chemical structure of DTBTT and the schematic structure of DTBTT OFET. (b) SEM micrograph of a cross-section of the fabricated OFET. The representative transfer characteristics of DTBTT OFET fabricated on (c) $\text{SiO}_2@\text{PSR}_{\text{XL}}$ and (d) PSR_{XL} gate dielectric. The average hole mobility obtained from 16 devices was $\mu_{\text{FET}} = 2.5 \pm 0.13$ $\text{cm}^2 \text{V}^{-1} \text{s}^{-1}$ for $\text{SiO}_2@\text{PSR}_{\text{XL}}$ and $\mu_{\text{FET}} = 0.9 \pm 0.07$ $\text{cm}^2 \text{V}^{-1} \text{s}^{-1}$ for PSR_{XL} , respectively.



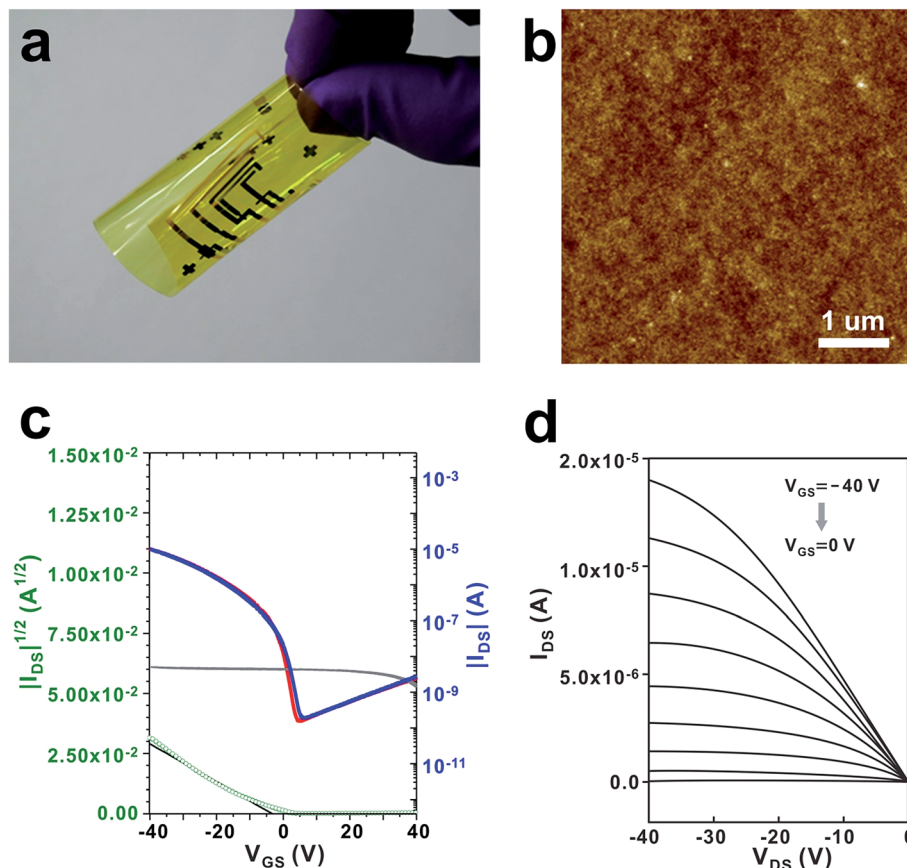


Fig. 6 (a) Photograph of DTBTT OFET devices fabricated on a PI substrate. (b) AFM micrograph of $\text{SiO}_2\text{@PSR}_{\text{XL}}$ film coated on PI substrate. (c) Transfer and (d) output characteristics of DTBTT OFET devices fabricated on a PI substrate. The average hole mobility obtained from 16 devices was $\mu_{\text{FET}} = 0.88 \pm 0.04 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$.

mobility and the $I_{\text{on}}/I_{\text{off}}$ ratio for the sample was $0.88 \pm 0.04 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ and 10^5 respectively (Fig. 6c and d).

3. Conclusion

In summary, we have successfully demonstrated a novel gate dielectric material for OTFT by incorporating SiO_2 nanoparticles into polysiloxane matrix. The two-step process, formation of colloidal core-shell nanoparticles and subsequent double-crosslinking, leads to form uniform and dense organic/inorganic composite dielectric films by using the conventional solution process. $\text{SiO}_2\text{@PSR}_{\text{XL}}$ films showed not only good thermal and mechanical properties but also outstanding electrical properties. The dielectric films made of $\text{SiO}_2\text{@PSR}_{\text{XL}}$ showed exceptionally low leakage current and no breakdown voltage up to 4.3 MV cm^{-1} , which are comparable to those of silica dielectrics prepared by CVD. Their excellent dielectric properties were also reflected in the performance of OTFT. The OFET device based on $\text{SiO}_2\text{@PSR}_{\text{XL}}$ exhibited the much better mobility and $I_{\text{on}}/I_{\text{off}}$ ratio than its counterpart made of PSR_{XL} . Our approach is versatile since it can be easily expanded to other nanoparticles including TiO_2 and ZnO , and the physical properties can be easily tuned by monomer compositions.

4. Experimental

(1) Synthesis of $\text{SiO}_2\text{@PSR}$

Aqueous alkaline silica sol (500 g) was passed through a column charged with acidic cationic exchange resin, obtaining acidic silica sol having $\text{pH} = 2$. The collected acidic silica sol (500 g) and methanol (1000 mL) were then charged in a reactor equipped with a reflux condenser and a mechanical stirrer. Subsequently, (3-glycidyloxypropyl)trimethoxysilane (GPTS, 11 g, 0.05 mol) was added to the reactor. The mixture was well mixed for 1 h, and then the temperature increased to 95°C , and sit for 6 h. To remove water from the silica sol, vacuum distillation was performed. After removing water, methanol (1000 mL) was recharged to disperse the silica sol. Trimethoxymethylsilane (TMMS, 43 g, 0.32 mol), (3-glycidyloxypropyl)trimethoxysilane (GPTS, 89 g, 0.38 mol), 3-(methacryloxypropyl)trimethoxysilane (MPTS, 183 g, 0.73 mol), diethoxydiphenylsilane (DEPS, 86 g, 0.32 mol) and tetraethoxysilane (TEOS, 66 g, 0.32 mol) were added to the silica sol, and the mixture was mixed well at room temperature for 1 h. Following this, hydrochloric acid aqueous solution (56 g, 0.1 N) was added in dropwise for 2 h, and the mixture was additionally stirred at room temperature for 2 h to perform hydrolysis reaction. The reaction temperature was then increased to 80°C and sit for



36 h to induce polycondensation reaction. The resulting SiO₂@PSR showed the core-shell structure having SiO₂ core and polysiloxane shell. Viscosity of SiO₂@PSR was 9.2 cPs at 20 °C, and pH was 5. SiO₂@PSR was redispersible in polar organic solvents. In our experiments, SiO₂@PSR was dispersed in propylene glycol monomethyl ether acetate (PGMEA) with a concentration of 35 wt%.

(2) Synthesis of PSR

TMMS (43 g, 0.32 mol), GPTS (89 g, 0.38 mol), MPTS (183 g, 0.73 mol), DEPS (86 g, 0.32 mol) and TEOS (66 g, 0.32 mol) were charged in a flask and stirred at room temperature. Hydrochloric acid aqueous solution (56 g, 1 N) was then added thereto in dropwise for 2 h, and the mixture was stirred at room temperature for 2 h. The mixture was heated to 80 °C and maintained for 36 h to perform polycondensation. The resulting PSR showed a viscosity of 8.6 cP at 20 °C, pH = 5, and the molecular weight measured on a GPC of 2200 g mol⁻¹ (PDI = 1.39). PSR was dispersed in PGMEA with a concentration of 35 wt%.

(3) Fabrication of MIM devices

SiO₂@PSR was first spin-casted on molybdenum-coated glass substrates from 35 wt% of solutions in PGMEA. The thickness was controlled to 500 ± 10 nm. SiO₂@PSR films were first soft-baked at 70 °C for 15 min to activate the epoxy/hydroxyl groups, and simultaneously exposed to UV ($\lambda_{\text{band}} = 240\text{--}400$ nm) for 75 s to induce photo-crosslinking. To complete the crosslinking reaction, the films were annealed at 200 °C for 1 h under the nitrogen atmosphere. After the crosslinking process, gold was evaporated on the SiO₂@PSR_{XL} films to make the top electrode.

(4) Fabrication of OTFT devices

Molybdenum was sputtered on glass substrates to make gate electrodes. SiO₂@PSR was spin-coated thereto from 35 wt% of solutions in PGMEA. The films were first soft-baked at 70 °C for 15 min to activate the epoxy/hydroxyl groups, and simultaneously exposed to UV for 75 s to induce photo-crosslinking. The films were then annealed at 200 °C for 1 h under the nitrogen atmosphere for crosslinking. Dibenzothiopheno[6,5-*b*:6',5'-*f'*]thieno[3,2-*b*]thiophene (DTBTT) was vacuum-deposited on to the films. Finally, gold was sputtered on the DTBTT layer to make source/drain electrodes.

Acknowledgements

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