PAPER

Patterning of amorphous-InGaZnO thin-film transistors by stamping of surface-modified polydimethylsiloxane†

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An indium gallium zinc oxide (IGZO) layer was patterned and thin-film transistors (TFTs) were fabricated by surface modified polydimethylsiloxane (PDMS) stamping and IGZO solution. The PDMS stamp was prepared by immersion in piranha solution and treatment with UV–ozone to make a hydrophilic surface. Patterned PDMS was inked by contact with the IGZO layer, and then stamped on the desired substrate. The process did not cause etching damage, so the stamped amorphous-IGZO TFTs showed low leakage current of $\sim 10^{-11}$ A, high on/off current ratio of $\sim 10^8$, carrier mobility of 6 cm^2 V^{-1} s^{-1}, and narrow hysteresis of 0.2 V. UV irradiation on the IGZO layer caused a photochemical annealing effect that improved the electrical properties of IGZO TFTs. This method provides a simple and versatile process to fabricate transparent metal-oxide TFTs based on patterning the devices by reusable stamping methods.

Introduction

Metal-oxide-based thin-film transistors (TFTs), especially amorphous indium gallium zinc oxide (a-IGZO)-based TFTs, have good characteristics including high electron mobility and transparency, and are therefore regarded as essential elements for displays, organic light-emitting diodes (OLEDs) and opto-electronic devices.[1–5] Recent advances in this field have achieved carrier mobility of higher than 10 cm^2 V^{-1} s^{-1}.[6–8] Metal-oxide-based TFTs can be fabricated using vacuum processes, such as pulsed laser deposition[1] and radio frequency (RF) sputtering.[9,10] However, vacuum deposition has disadvantages, such as slow fabrication and high cost. As an alternative, solution processes including inkjet printing,[11] spin-coating,[12] and dip-coating,[13] can be used to fabricate devices under ambient conditions.

The patterning process is very important in fabrication and integration of semiconductor devices. Precise fabrication can reduce the leakage current between adjacent devices. Among various patterning technologies, photolithography has been most widely used, but it has disadvantages, such as high production cost and complicated process. In addition, chemicals used to etch thin films can affect the electrical properties, such as carrier mobility, leakage current, threshold voltage, and hysteresis. Printing methods, such as ink-jet printing and electro-hydrodynamic-jet printing, are emerging alternatives to

photolithography,[11] but these methods also have limitations, such as slow fabrication and nozzle clogging.

Recently, patterning techniques on polydimethylsiloxane (PDMS) elastomer such as nano-imprinting and direct transfer of materials have been introduced. Direct transfer of a-IGZO onto PDMS is reported using RF magnetron sputtering.[14] ZnO has been patterned by chemical imprinting with a PDMS stamp that had been soaked in ammonia solution and used to selectively dissolve ZnO.[15] Zinc tin oxide (ZTO) stripe patterns have been fabricated by exploiting capillary force to draw ZTO solution into stripe patterns of PDMS.[16] However, to fabricate devices these methods still require several steps.

In this research, we suggest PDMS stamping to transfer the IGZO layer for a low-cost and high-speed fabrication method. To give appropriate properties to the PDMS surface, PDMS elastomer was immersed in piranha solution to remove hydrophobic methyl groups. Then the PDMS surface was UV–ozone treated to make it hydrophilic. IGZO solution was transferred by PDMS stamping and subjected to a combination of photochemical and thermal annealing. The IGZO was not damaged by chemical etchant, so patterning decreased leakage current and narrowed the hysteresis loop. The patterned PDMS stamps can be reused many times. We show the possibility of using IGZO solution and PDMS to pattern the active layer of the TFTs.

Experimental

IGZO solution preparation

All chemicals were purchased from Sigma-Aldrich and used without further purification. Metal precursors, that is, indium nitrate hydrate (In(NO$_3$)$_3$·xH$_2$O, 99.9%), gallium nitrate hydrate (Ga(NO$_3$)$_3$·xH$_2$O, 99.9%) and zinc acetate dihydrate

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$(Zn(CH_3CO_2)_2 \cdot 2H_2O$, 98%), were 0.125 M and were mixed at a mole ratio of In : Ga : Zn = 7 : 1 : 2. Metal precursors were dissolved in 2-methoxyethanol (2-ME, $CH_3OCH_2CH_2OH$, anhydrous 99.8%) and stirred thoroughly for 6 h at 75 °C to make the transparent and homogeneous solution.[17] Ethylene glycol (EG) was added to synthesize the EG/IGZO solution. The volume ratio was 2-ME : EG = 20 : 1. Solution viscosity was controlled by adjusting the amount of EG. IGZO solutions were filtered using 0.2 μm syringe filters (PTFE, Whatman) before being spin-coated.

Fabrication of patterned PDMS stamps and surface modification

PDMS elastomer (Sylgard 184) was purchased from Dow Corning. Sylgard 184 base and curing agent in 10 : 1 (w : w) ratio were mixed thoroughly and poured onto the master substrate in a Petri dish. Air bubbles generated during mixing were removed completely in a vacuum chamber. The PDMS was cured at 75 °C for 6 h, and then peeled off smoothly from the master substrate.[18–20] PDMS for IGZO transfer was thermally annealed at 100 °C for 1 d in an oven, and then immersed in piranha solution $(H_2SO_4 : H_2O_2 = 1 : 1$ mol : mol). The PDMS was removed from the solution, followed by a thorough rinse with deionized (DI) water to remove chemical residue. For ozone treatment, the sample was illuminated using a low-pressure mercury lamp (wavelength $\lambda = 254$ nm (90%) and 185 nm (10%)) for 30 min.

Fabrication of stamped a-IGZO TFTs

Highly doped n-type Si wafer with 100 nm SiO_2 was used as the substrate. Before device fabrication the wafers were cleaned in a piranha solution $(H_2SO_4 : H_2O_2 = 4 : 1$ mol : mol), sonicated sequentially in acetone, ethanol, and DI water for 15 min each, and then dried under N_2 gas. Finally, the wafers were treated with UV–ozone for 10 min to remove organics and clean the surface.

IGZO solution was spin-coated on the wafer at 500 rpm for 5 s, and then at 3000 rpm for 30 s. The PDMS stamp was attached to a substrate coated with IGZO solution (Fig. 1a). Hydrophilic modified PDMS was put on the IGZO spin-coated layer for several seconds under pressure, and then peeled off and immediately stamped onto the new substrate (Fig. 1b). After peeling off, patterned IGZO on the substrate was annealed photochemically for 1 h, and then annealed thermally at 350 °C for 1 h (Fig. 1c). After annealing, Al (100 nm) was deposited through a shadow mask by electron-beam evaporation (Fig. 1d). To compare the effect of PDMS stamping, IGZO TFTs without patterns were also fabricated by spin coating.

Characterization

The contact angle of IGZO solution on PDMS was measured using the contact angle measurement system (SmartDrop). Images of solution droplets were taken by a high-resolution CCD camera. The electrical properties of TFTs were analyzed in the dark using semiconductor parameter analyzer (KEITHLEY 4200-SCS). Surface roughness and IGZO layer thickness were measured using atomic force microscopy (AFM, VEECO Dimension 3100) and Alpha-Step (KLA-Tencor), respectively.

Results and discussion

Surface treatment of PDMS

The PDMS stamp surface is intrinsically hydrophobic with low surface energy because it is coated with nonpolar methyl groups. 2-ME-based IGZO solution is polar so it does not wet well onto PDMS. Dipping PDMS in piranha solution and UV–ozone treatment made the PDMS surface hydrophilic. Before wet chemical treatment, PDMS was thermally aged at 100 °C for 1 d in an oven. Low molecular weight (LMW) species such as uncrosslinked PDMS chains or residual crosslinking chains may be volatilized during thermal aging.[21] The decrease in the amount of LMW species helps to stabilize the hydrophilic surface after wet chemical treatment. After thermal aging, PDMS surface was first modified by dipping it in piranha solution. Oxygen generated in piranha solution reacted with PDMS to form polar hydrophilic silanol groups. Contact angles decreased with as UV–ozone treatment time increased after dipping the PDMS stamp in piranha solution (Fig. 2). The 185 nm irradiation induces formation of ozone from oxygen in the air. The organic contamination can also be removed by reaction with ozone driven by irradiation at $\lambda = 254$ nm. The PDMS surface becomes oxidized and hydrophilized.[22,23] 2-ME-based IGZO solution was wetted on the PDMS surface. Excessive UV–ozone treatment (>30 min) caused the PDMS to crack.
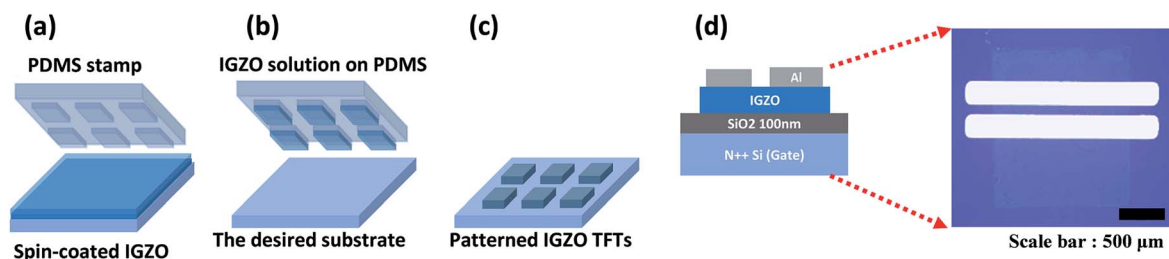

Fig. 1 Schematic of fabrication procedures. (a) PDMS stamp and spin-coated IGZO layer. (b) PDMS stamp with IGZO ink and desired substrate. (c) IGZO transfer to the desired substrate. (d) Cross-sectional view of final device structure. (Right image: optical microscope image of the fabricated device.)
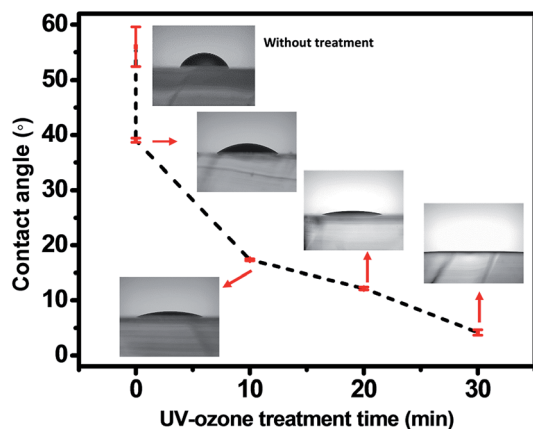
Fig. 2 Contact angles *vs.* UV–ozone treatment time. Insets: optical images of IGZO solution droplet on PDMS.

The volume of solution transferred from PDMS to substrate has an effect on the thickness of active layers. Although the volume of solution transferred cannot be measured precisely, it can be controlled by adjusting stamping pressure. When the PDMS stamp was not surface treated, the transfer failed (Fig. S1†).

Characteristics of stamped IGZO TFTs

Output and transfer characteristics (Fig. 3) were obtained from the measurement of spin-coated and single-stamped IGZO TFTs. To obtain output curves, drain voltage (V_d) was swept from 0 V to 30 V at fixed gate voltage (V_g) in increments of 5 V. To obtain transfer curves, V_g was swept from −20 V to 30 V to −20 V at $V_d = 30$ V. Spin-coated IGZO showed threshold voltage (V_{th}) of ∼3 V and narrow hysteresis, but high leakage current (I_{LEAK}) (Fig. 3a). In contrast, stamped IGZO showed I_{LEAK} as low as 10^{-11} A with still narrow hysteresis and low V_{th} (Fig. 3b).

The root mean-square roughness (R_{RMS}) of spin-coated and single-stamped IGZO surface was measured using AFM (Fig. 4). The average R_{RMS} was 0.196 nm and 0.352 nm on the spin-
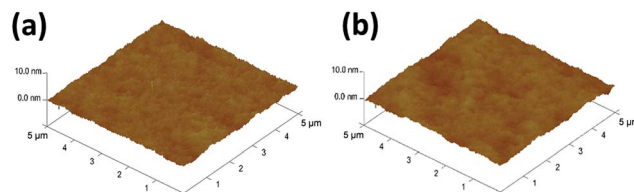

Fig. 4   Atomic force microscopy images of IGZO TFTs. (a) Spin-coated IGZO. (b) Single-stamped IGZO.

coated IGZO surface and stamped IGZO surface, respectively. The thickness of IGZO layer was measured using a stylus profiler. The thickness was ∼15 nm in both the single-stamped and spin-coated layers. The small R_{RMS} and uniform thickness demonstrate that uniform layers can be synthesized by PDMS stamping. The stamping method transfers IGZO solution directly to the desired substrate and does not require chemicals to etch thin films, whereas conventional device fabrication does. As a result, uniform layers with good electrical properties can be achieved by this stamping method.

To improve electrical properties, photochemical annealing was done.[17] UV absorbance of IGZO solution increased near $\lambda = 254$ nm (Fig. S2†). When UV irradiates IGZO solution, alkoxy groups can be broken by photochemical energies, and metal and oxygen atoms are activated to form metal–oxygen–metal (M–O–M) networks. Compared to thermal annealing alone, photochemical annealing improves the electrical properties (Fig. 5).[22,24,25]

Carrier mobility μ is calculated using the following equation:[26]

Fig. 3   Electrical properties of IGZO TFTs. (a) Output and (b) transfer characteristics of spin-coated IGZO TFTs. (c) Output and (d) transfer characteristics of stamped IGZO TFTs (single-stamped IGZO).
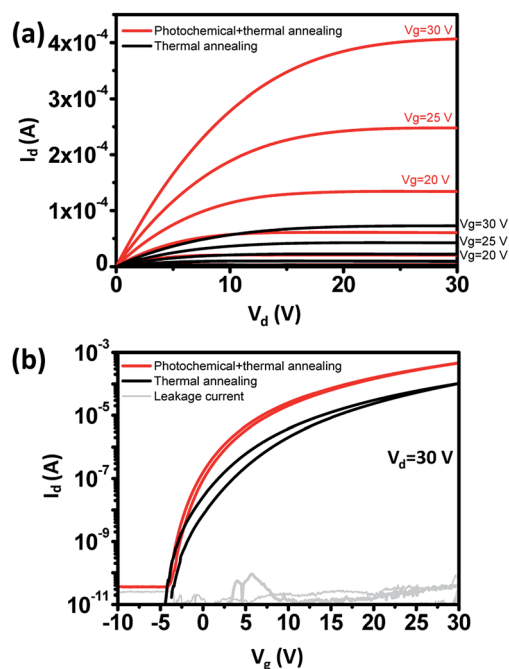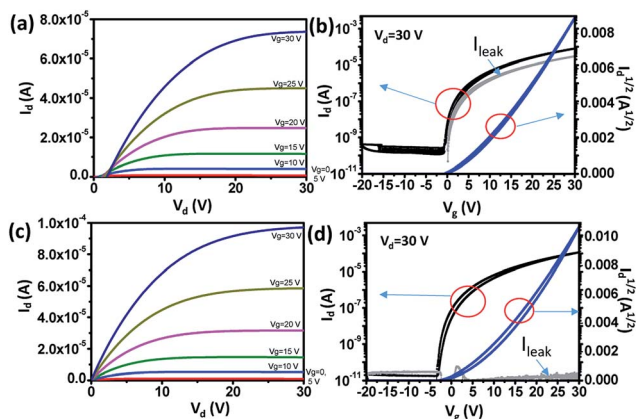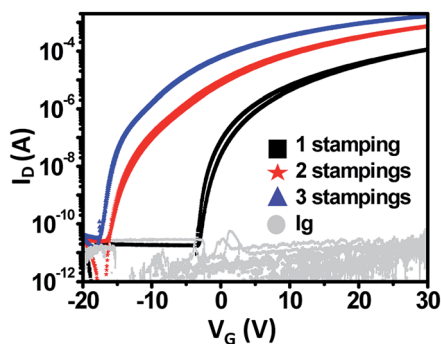

Fig. 5   Electrical properties with and without photochemical annealing of single-stamped IGZO TFTs. (a) Output curves. (b) Transfer curves. Red line, thermal annealing after photochemical annealing. Black line, thermal annealing only.

Fig. 6 Electrical characteristics of IGZO TFTs as a function of stamping numbers.

$$I_d = (W/(2L))\mu C_{ox}(V_g - V_{th})^2, \ V_d > (V_g - V_{th}), \text{ (saturation)}$$

where W is channel width, L is channel length, and C_{ox} is the capacitance of the gate oxide. V_{th} is calculated from the square root of I_d from the transfer curve. Films that had only been thermally annealed had $\mu = 1.6 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, on/off current ratio $= 10^7$, and $V_{th} = 6.2$ V. In contrast, when the IGZO layer had been treated using UV and ozone, thin films can be condensed and densified as a result of the elimination of residual organics. UV–ozone treatment can also decrease the density of defects in IGZO related to oxygen vacancies.[25,27] As a result, the combination of photochemical and thermal annealing improved the electrical characteristics of IGZO TFTs to $\mu = 3.73 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ and $V_{th} \approx 0.5$ V with narrow hysteresis (Fig. 5).

As the number n of stampings increased, μ increased and V_{th} shifted negatively from 6.2 V to -6.7 V (Fig. 6). As n rose, the thickness of IGZO layers increased. When IGZO solution is stamped on the IGZO layer, the pores or pin-holes may be filled by subsequent stampings (Fig. S3†). Accumulation of free carriers and the decreasing number of defects lead to increased μ and a negative shift in V_{th}.[28] These results demonstrate that the electrical properties of the TFT can be controlled by adjusting the number of stacked layers (Fig. S4†).

Conclusions

We developed a-IGZO TFTs by using surface-modified, patterned PDMS to stamp an IGZO layer. The patterned IGZO obtained by PDMS stamping had high μ, low V_{th} and high on/off ratio. In particular, lower leakage current and hysteresis were obtained with the PDMS stamping method than by conventional spin-coating. PDMS stamping is a simple, inexpensive, and fast fabrication method. In addition, the stamp can be reused. The proposed method may reduce the time and cost of fabricating a-IGZO TFTs, and may also have other applications.

Acknowledgements

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