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Tin perovskite/fullerene planar layer photovoltaics: improving the efficiency and stability of lead-free devices†

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We report the first demonstration of orthorhombic CsSnI_3 films prepared from solution at room temperature that have defect densities low enough for use as the light harvesting semiconductor in photovoltaic devices even without using excess Sn in the preparative method, and demonstrate their utility in a model p–i–n photovoltaic device based on a $\text{CuI} | \text{CsSnI}_3 | \text{fullerene}$ planar layer architecture. We also report an effective strategy for simultaneously improving both the efficiency and stability of these devices towards air exposure based on the use of excess of SnI_2 during CsSnI_3 synthesis from CsI and SnI_2 . A combination of photoelectron spectroscopy, contact potential measurements and device based studies are used to elucidate the basis for this improvement and role of the excess SnI_2 . The open-circuit voltage in these lead-free photovoltaic devices is shown to be strongly dependent on the degree of alignment between the perovskite conduction band edge and the lowest occupied molecular orbital (LUMO) in the fullerene electron transport layer. Furthermore, the energetics at the perovskite–fullerene interface are shown to be a function both of the LUMO energy of the fullerene and the nature of the interaction at the heterojunction which can give rise to a large abrupt vacuum level shift across the interface. A champion open-circuit voltage of ~ 0.55 V is achieved using indene- C_{60} bis-adduct as the electron extraction layer, which is twice that previously reported for a CsSnI_3 based PPV.

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Introduction

Over the past 4 years there has been dramatic progress in the field of perovskite photovoltaics (PPVs), most of which has focused on using methylammonium (MA) lead iodide (MAPbI_3)^{1–4} and chloride substituted analogues, $\text{MAPbI}_{3-x}\text{Cl}_x$,^{5–7} as the light harvesting semiconductor. This is due to the high absorption coefficient, high charge carrier diffusion length ($L_D > 100$ nm)⁸ and low band gap (E_g) of 1.5–1.6 eV^{5,9} in these materials. The highest power conversion efficiency (η) achieved to date in this field is over 19%.¹⁰

The majority of PPV devices reported to date use the perovskite in a distributed heterojunction device architecture, in which n-type mesoporous TiO_2 serves as the electron-transport layer (ETL) and a p-type organic semiconductor is used as the hole-transport layer (HTL). There are also a smaller number of reports of planar-heterojunction PPVs based on a p–i–n structure, including those in which TiO_2 is replaced with organic semiconductors such as fullerenes, which have achieved $\eta > 17\%$.^{11–13} Unlike TiO_2 , fullerene ETLs do not require annealing which removes the need for the substrate electrode to be able to

withstand high temperatures. Planar device architectures using organic charge transport layers may also offer the best prospects for accommodating the very large thermal expansion exhibited by some metal halide perovskites.¹⁴ There is also interest in identifying low cost organic and inorganic HTLs, most recently the Cu(I) compounds CuI and CuSCN .^{15–18}

The primary drawback of Pb perovskites for photovoltaic applications is their susceptibility to degradation in the presence of water to produce PbI_2 ,^{19–21} which is a partially water soluble compound.^{21,22} Soluble Pb compounds are well known to be toxic to both humans and the natural environment²³ and so there is growing interest in the development of alternative, narrow E_g perovskites that use Sn instead of Pb,²⁴ such as CsSnI_3 ^{25–28} and MASnI_3 .^{29,30}

At room temperature and in the absence of air CsSnI_3 adopts a 3D orthorhombic perovskite structure which is known as the *black phase*. The E_g of this semiconductor is near optimal for PV applications (~ 1.3 eV¹⁴),³¹ which combined with its high optical absorption coefficient ($\sim 10^4$ cm^{−1} in the visible region)³² and low exciton binding energy (~ 18 meV),³³ makes CsSnI_3 attractive as a light harvesting semiconductor in PPVs because photoexcitation results in free carrier generation for light with wavelengths as long as ~ 950 nm.¹⁴ However, the material is prone to the formation of a high density of Sn cation vacancy defects, which serve as p-type dopants and recombination centres.^{14,34}

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Sn perovskites also have the drawback of air sensitivity, with the black phase converting to yellow phase CsSnI_3 , which in turn oxidises to Cs_2SnI_6 in which Sn is in its more stable 4+ oxidation state.^{14,35} Unlike Sn, Pb is most stable in the 2+ oxidation state due to the relativistic lowering of the Bohr radius of s-orbitals more than that of other atomic orbitals, which stabilises them towards oxidation, an effect that is most pronounced in heavy p-block elements like Pb.^{36,37} In practice the ingress of oxygen and moisture into PV devices is inevitable and so a central challenge in Sn-PPV research is to develop strategies that can be used in conjunction with device encapsulation to reduce the rate of oxidation of Sn perovskite enough for practical applications to be possible. To date there have been six literature reports relating to the use of Sn perovskites in PPVs which collectively demonstrate the potential to achieve high η .^{25–30}

CsSnI_3 was first used by Chung *et al.*²⁵ as a HTL in PPVs in which a ruthenium dye was the primary light harvesting element and the CsSnI_3 was synthesised using a vacuum melt process at 450 °C. That pioneering work was quickly followed by Chen *et al.*,²⁶ who demonstrated a Schottky contact type PPV based on a vacuum evaporated layer of CsSnI_3 formed by sequential deposition of alternating layers of CsI and SnCl_2 followed by annealing at 175 °C to form the perovskite, although the device fill factor (FF) was extremely low (~ 0.22) due to a very low shunt resistance and high series resistance. Concurrently to this Zhou *et al.*³⁸ reported that CsSnI_3 could be synthesised at room temperature from a mixed solution of CsI and SnI_2 , although films formed using that method were not tested for optoelectronic applications. In 2014 Noel *et al.* reported the highest η to date for a Sn PPV of $\sim 6\%$, achieved using MASnI_3 as the light harvesting semiconductor.²⁹ However those devices were also shown to exhibit a very large variation in performance and poor stability with deeply coloured devices becoming completely transparent minutes after measuring.²⁹ That work was published only days before a report by Hao *et al.*³⁰ who showed that η close to 6% could be achieved using $\text{MASnI}_3\text{Br}_2$ and the open-circuit voltage (V_{oc}) could be tuned by varying the ratio of iodide to bromide as a result of a change in E_g . Shortly afterwards Kumar *et al.*²⁷ reported that CsSnI_3 processed at 70 °C and with 20 mol% SnF_2 additive could be used as the light absorber in a PPV device based on a distributed heterojunction with TiO_2 , achieving photocurrents $>22 \text{ mA cm}^{-2}$. Unfortunately the V_{oc} and fill factor (FF) were limited to $\sim 240 \text{ mV}$ and ~ 0.37 respectively which constrained η to 2.02%. Notably, devices fabricated without SnF_2 did not function and whilst it was shown that fluoride was not incorporated into the CsSnI_3 lattice, the possible optoelectronic functionality of the relatively large quantity of SnF_2 present in the perovskite film was not discussed.²⁷ Kumar *et al.* concluded that the V_{oc} in CsSnI_3 PVs is limited by the high background carrier density due to Sn-vacancy defects even when prepared under Sn-rich conditions, and is not set by the difference between the conduction band minimum of the ETL and the valence band maximum (or highest occupied molecular orbital (HOMO)) of the HTL. The same group went on to show that substitution of iodide by bromide in CsSnI_3 is an effective means of increasing the V_{oc} in Sn-PPV achieving a V_{oc} of 0.41 V. However, the

improvement in V_{oc} was at the expense of photocurrent due to the increase in E_g .²⁸

Herein we report: (i) the first demonstration of orthorhombic CsSnI_3 films prepared from solution at room temperature with defect densities low enough for use as the light harvesting semiconductor in PPV devices, even in the absence of excess Sn; (ii) a new strategy for improving device stability towards oxidation in air, which does not complicate the process of device fabrication; (iii) the results of a study which unambiguously show that the energetics at the interface between CsSnI_3 and ETL are a key determinant of device V_{oc} .

Results

Model p–i–n PPVs were fabricated with the structure: indium tin oxide (ITO)|CuI| CsSnI_3 |fullerene|bathocuproine (BCP)|Al, as schematically illustrated in Fig. 1a. The CsSnI_3 layer was prepared from 8 wt% CsSnI_3 in *N,N*-dimethylformamide (DMF) using SnI_2 and CsI. CuI was chosen as the p-type material due to its proven effectiveness as a HTL in PPVs.¹⁵ CuI is also amenable to deposition by both thermal evaporation and by solution processing. Since thermal evaporation enables a very high degree of control over film thickness and morphology it is the preferred method in this study. Likewise C_{60} was selected as the n-type material because it can also be deposited by thermal evaporation. There are also a number of simple derivatives of C_{60} commercially available with modified frontier orbital energies. For these reasons C_{60} serves as a model n-type charge extraction layer in this work. The CsSnI_3 layer was deposited by spin casting directly onto freshly evaporated CuI at room temperature to give films with a thickness of $\sim 50 \text{ nm}$, as measured using cross-sectional AFM histogram analysis (Fig. 1b(ii)). In contrast to all other reports relating to the use of Sn-perovskites in PPVs these films were used without post-deposition annealing.

Fig. 1b(i) shows a representative SEM image of a CsSnI_3 film deposited from 8 wt% CsSnI_3 in DMF onto a CuI coated ITO substrate. The film is made up of small crystals approximately 50–100 nm in diameter, which is comparable to the film thickness determined from a step edge analysis using atomic force microscopy (Fig. 1b(ii) and (ESI) Fig. S1a–c†). It is therefore likely that these crystallites extend from the top to the bottom of the film as schematically depicted in Fig. 1a. Increasing the loading of CsSnI_3 to 15 wt% resulted in an increase in film thickness to $\sim 80 \text{ nm}$ with a larger mean crystallite size and very few pin holes (Fig. 1c and ESI Fig. S1d–f†). Whilst there are still some gaps in films of this thickness the coverage is exceptionally high for such a thin layer of metal halide perovskite.¹¹ Furthermore, films of comparable quality could also be prepared on glass, ITO and Au coated substrates which proved useful for fundamental measurements of film properties.

A representative X-ray diffraction (XRD) pattern of a CsSnI_3 film spin cast onto a glass substrate pre-coated with 10 nm of evaporated CuI is shown in Fig. 2b. The very thin CuI layer is included to ensure the structure of the perovskite film probed is identical to that used in PPV devices. Since there is no evidence



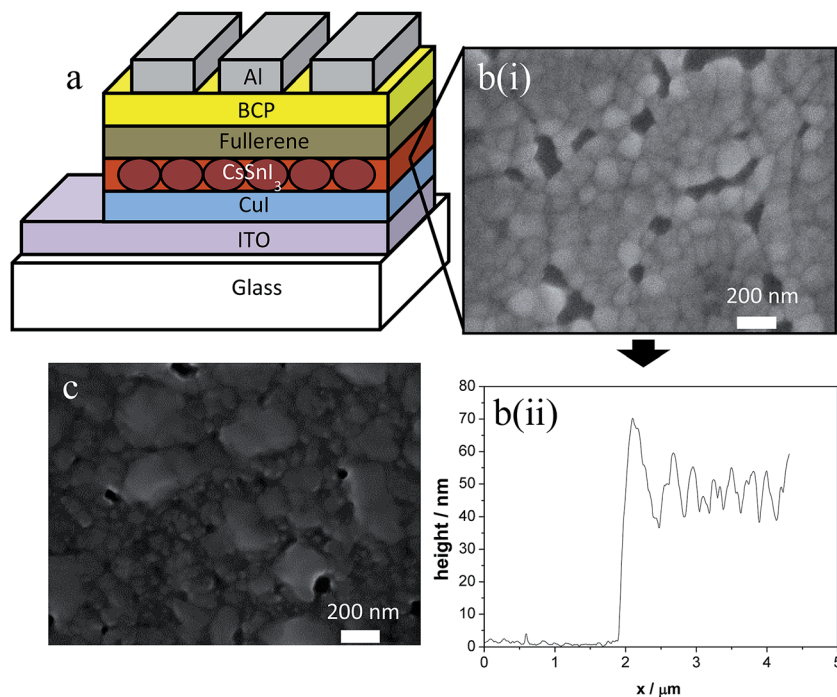


Fig. 1 (a) Schematic of the device architecture used in this work; (b(i)) SEM image of a CsSnI_3 film prepared with a 10 mol% excess SnI_2 and spin cast at 4000 rpm from 8 wt% solution onto an ITO glass substrate coated with a 100 nm layer of CuI; (b(ii)) AFM step height of a scored CsSnI_3 film spin cast from 8 wt% solution onto glass; (c) SEM image of a CsSnI_3 film spun from 15 wt% CsSnI_3 solution with 10 mol% SnI_2 excess using the same deposition process and substrate as for (b(i)).

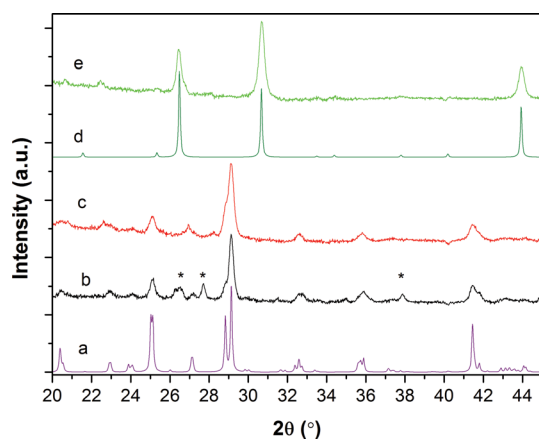


Fig. 2 X-ray diffraction (XRD) patterns of films of: (a) a simulated powder diffraction pattern of CsSnI_3 ; (b) a thin film of CsSnI_3 ; (c) a thin film of CsSnI_3 prepared with a 10 mol% excess SnI_2 ; (d) a simulated powder diffraction pattern of Cs_2SnI_6 , and (e) a CsSnI_3 film oxidised in air for 16 hours. All films probed using XRD were supported on glass substrates coated with 10 nm CuI and deposited by spin casting at 2000 rpm from 16 mol% DMF solutions.

of CuI in the XRD pattern it can be concluded that the buried CuI layer is essentially amorphous and/or too thin to be observed using this technique. The XRD pattern in Fig. 2b is consistent with the orthorhombic perovskite structure expected for the *black phase* of CsSnI_3 , as reported in the literature¹⁴ and simulated in Fig. 2a. The prominence of the peaks at $\sim 29^\circ$ in the XRD pattern of the film as compared to the simulated

powder pattern is indicative of preferential crystal orientation in the spin cast film on CuI. The small reflections at $\sim 26.3^\circ$, $\sim 27.7^\circ$ and $\sim 37.8^\circ$ (starred) are associated with the supporting substrate and sample holder. After 16 hours air exposure the CsSnI_3 film gives the XRD pattern Fig. 2e, which is consistent with the formation of the Cs_2SnI_6 , in which Sn is in the more stable 4+ oxidation state, as is evident from the good agreement with the simulated reflections for Cs_2SnI_6 shown in Fig. 2d.³⁹ X-ray photoelectron spectroscopy (XPS) measurements of the oxygen 1s binding energy shows that those Sn atoms that do not form part of Cs_2SnI_6 structure are most likely present as SnO_2 (ESI Fig. S2†). The absence of reflections in the XRD pattern indicative of crystalline SnO_2 indicates that it is present as an amorphous or nano-crystalline phase.

It has recently been proposed that due to the ease with which Sn-cation vacancies form in CsSnI_3 it must be prepared in a Sn-rich environment or at high temperature to ensure the density of these defects is low enough for its use as a light harvesting material in PPV devices.^{27,34,35} However we find that devices based on CsSnI_3 films prepared using stoichiometric mixtures of CsI and SnI_2 with no excess of Sn and processed entirely at room temperature generate a short circuit current density (J_{sc}) of $\sim 10 \text{ mA cm}^{-2}$ under 1 sun simulated solar illumination, which is a very high current for such a thin semiconductor layer ($\sim 50 \text{ nm}$) (Fig. 3). The incident-photon-to-converted-electron (IPCE) efficiency (ESI Fig. 3†) confirms that CsSnI_3 is the primary light absorbing component in these devices, since wavelengths as long as $\sim 930 \text{ nm}$ are converted into electrons in the external circuit, which corresponds to sub-band gap photon



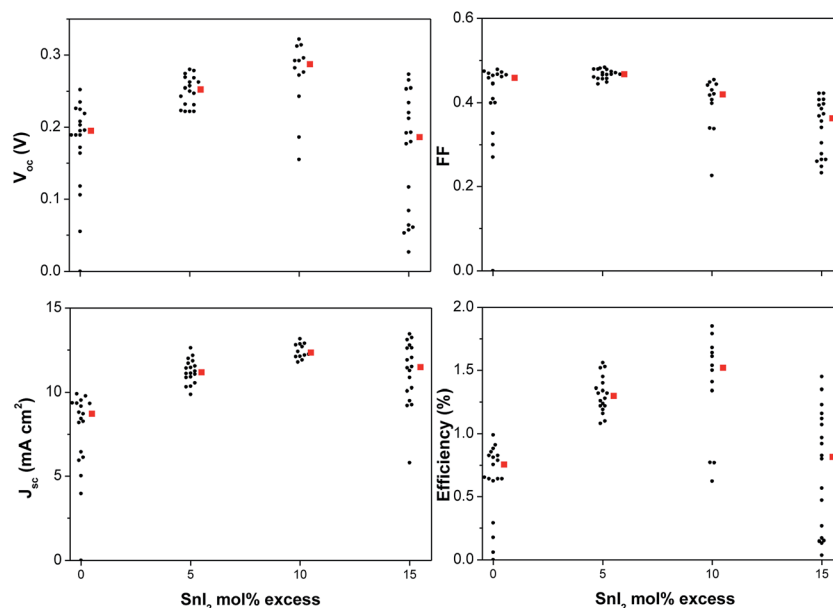


Fig. 3 Scatter graphs of PPV performance characteristics for devices with the structure: ITO|CuI|CsSnI₃|C₆₀|BCP|Al using CsSnI₃ prepared from solutions containing either 0, 5 or 10 mol% excess SnI₂. The mean values are shown as red squares.

energies for C₆₀. This spectral response is also consistent with E_g for CsSnI₃ reported in the literature of ~ 1.3 eV.^{14,35}

As shown in Fig. 3, devices based on CsSnI₃ films prepared using an excess of SnI₂ and C₆₀ ETL exhibit up to $\sim 30\%$ increase in J_{sc} and V_{oc} as compared to those without. The optimal SnI₂ excess is ~ 10 mol% which results in a doubling of η from $\sim 0.75\%$ to $\sim 1.5\%$. All reflections in the XRD pattern for films prepared using 10 mol% excess SnI₂ (Fig. 2c) can be attributed to CsSnI₃, except the weak reflection at $\sim 28.5^\circ$. The latter cannot be assigned to SnI₂ or its oxidised form; SnI₄, which indicates that the excess SnI₂ is nano-crystalline or amorphous. High-resolution XPS measurements of the surface chemical composition of freshly prepared films with a 10 mol% SnI₂ excess show that there is very little SnI₂ and no evidence for SnI₄ at the CsSnI₃ surface (Fig. S4†), since $\sim 95\%$ of the photoelectrons originate from the top ~ 7 nm of the sample. The XPS data therefore provides evidence that the 10 mol% excess SnI₂ phase separates to the buried interface upon solvent evaporation. Notably, since these films were unavoidably exposed to the laboratory for ~ 1 minute when transferring from the nitrogen filled glove box to the spectrometer vacuum system there is also evidence for significant oxidation of the surface of the CsSnI₃ to Cs₂SnI₆ in the XPS spectra.

If the excess SnI₂ is primarily located at the buried CsSnI₃|CuI interface it must be capable of transporting holes from CsSnI₃ into CuI since PPV devices incorporating up to 15 mol% SnI₂ perform comparably to those with 5% SnI₂ excess (Fig. 3). To our knowledge there are no previous reports of SnI₂ being used as a semiconductor in an organic or hybrid electronic device and so its charge transport properties are not known. To investigate the transport properties of SnI₂ we have estimated the hole-mobility in SnI₂ from the space charge limited current in unipolar diodes to be $2.2\text{--}5.1 \times 10^{-6} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ (ESI, Fig. S5†). Whilst this value

is low as compared to that of Sn and Pb halide perovskites it is comparable to that of many amorphous organic semiconductors⁴⁰ and offers an explanation as to why device FF begins to deteriorate with increasing SnI₂ excess beyond 10 mol%. To determine the accessibility of the SnI₂ valence band for hole transport we have measured the ionisation potential (I_p) of SnI₂ ($I_p \sim 5.64 \text{ eV} \pm 0.05 \text{ eV}$) and CuI films ($I_p \sim 5.84 \text{ eV} \pm 0.05 \text{ eV}$) using ultraviolet photoelectron spectroscopy (UPS). Full details of the UPS study are given in the (ESI Fig. S6 and S7†). These energy levels, along with the E_g for CuI determined from electronic absorption measurements (ESI, Fig. S8†) and E_g for SnI₂ taken from the literature,⁴¹ are given in Fig. 4a. The susceptibility of CsSnI₃ films to oxidation in air prevented us from making a reliable measurement of the CsSnI₃ I_p using UPS, and the values reported in the literature for argon sputtered CsSnI₃ – also measured using UPS – vary from 4.9 eV²⁵ to 5.7 eV.⁴¹ In order to estimate the position of the valence and conduction band edges in CsSnI₃ with confidence we have measured the work function of CsSnI₃ films without air exposure, or argon sputtering, using a Kelvin probe located in the same glove box as the spin coater used to deposit the perovskite film. On the basis of this measurement, combined with the known E_g (~ 1.3 eV) and knowledge of the tendency of this material to form p-type defects,^{27,34,35} we have estimated the possible range of energies for the valence and conduction band edges in CsSnI₃. These energy levels along with the work function of each material measured using the Kelvin probe technique are given in Fig. 4a. It is clear from the location of the Fermi level in CuI that it is heavy p-doped which is consistent with literature reports⁴³ and can be attributed to Cu atom array vacancies. In the absence of such intrinsic defects CuI would block the extraction of holes from the CsSnI₃ valence band due to its very deep lying valence band. However, the high density of electron acceptor states near to the valence band edge should ensure that there is no barrier to hole-



extraction from CsSnI_3 when thermodynamic equilibrium is established across the interface. Similarly, spontaneous electron transfer from the SnI_2 into CuI due to the difference in Fermi level would be expected to remove the relatively small barrier to hole-extraction from SnI_2 into CuI . On the basis of the energy levels given in Fig. 4a there would however be expected to be a significant barrier to hole-transport from the CsSnI_3 valence band into the SnI_2 valence band. However, since the current-voltage characteristics of PPV devices fabricated with an excess of SnI_2 in the CsSnI_3 layer (up to 15 mol%) are not consistent with a high device series resistance it is likely that there is a favourable vacuum level shift at the interface due to the formation of an interfacial dipole.

The improvement in J_{sc} and V_{oc} in PPV devices using a CsSnI_3 layer prepared with excess SnI_2 can be rationalised in terms of a reduction in the background carrier density which gives rise to recombination losses, since it is known that the density of Sn-vacancy defects (*i.e.* the primary source of the background carrier density) is suppressed when the perovskite is synthesised in a Sn-rich environment.^{27,34,35} However, to investigate the possibility that the energetics at the perovskite/ C_{60} interface might also play a role in controlling V_{oc} we have measured the change in energy between the perovskite Fermi level and the surface vacuum level (*vac*) as a function of the C_{60} layer thickness using the Kelvin probe method. This energy difference, $\varepsilon_{\text{f}}^{\text{vac}}$, corresponds to the work function of C_{60} when the film thickness is sufficient for the establishment of Fermi level alignment across the interface. If thermodynamic equilibrium is not achieved then $\varepsilon_{\text{f}}^{\text{vac}}$ corresponds to the work function of the perovskite with a modified surface potential.^{42,43} Changes in $\varepsilon_{\text{f}}^{\text{vac}}$ can therefore be interpreted in terms of a *vacuum level shift* which give rises to a comparable shift in the energy of the HOMO and LUMO in the C_{60} with respect the valance and conduction band edges in CsSnI_3 . The results of this experiment are summarised in Fig. 4b. At the interface between CsSnI_3 prepared with 0% excess SnI_2 there is no significant change in the energy of the vacuum level. Conversely for films prepared with 10 mol% excess SnI_2 there is a large abrupt

positive vacuum level shift of ~ 0.3 eV meV. Abrupt positive vacuum level shifts at the interface between inorganic materials and organic semiconductors are relatively rare^{42,43} and are indicative of ground state charge transfer from the substrate into the first layer of molecule forming an interfacial dipole. The reason for the stronger interaction between C_{60} and CsSnI_3 with fewer Sn-vacancy sites is not yet clear, although the existence of this abrupt positive vacuum level shift offers an alternative explanation for the increase in V_{oc} , since it would operate to reduce the loss in electron potential energy when moving from the perovskite conduction band into the fullerene LUMO.

To further investigate the extent to which V_{oc} in these devices is controlled by the degree of alignment between the LUMO of the ETL and the conduction band edge in CsSnI_3 , we have systematically reduced the LUMO energy of the ETL from ~ 4.00 eV^{45,46} to ~ 3.91 eV⁴⁶ and finally to ~ 3.74 eV⁴⁶ below the vacuum level by substituting C_{60} with the C_{60} derivatives [6,6]-phenyl- C_{61} -butyric acid methyl ester (PC_{60}BM) and indene- C_{60} bis-adduct (IC_{60}BA) (Fig. 4a). Whilst the exact magnitude of these values remain the subject of debate⁴⁵ the differences in LUMO energy between these derivatives is firmly established.^{45,46} Champion J/V characteristics in the dark and under 1 sun simulated illumination are given in Fig. 5 and the full data set is given in the ESI (Table S1†). It is clear from Fig. 5 and the full data set in (Table S1†) that the increase in device V_{oc} correlates with an increase in the LUMO energy of the fullerene ETL which is consistent with a reduction in the potential energy lost by an electron moving from the CsSnI_3 into the ETL as seen in organic PVs.⁴⁶ The highest η is achieved for devices using ICBA as the ETL at 2.76%, which is significantly higher than the best previously reported for a CsSnI_3 based PPV (2.02%).²⁷ Collectively these data show that the performance of PPVs based on CsSnI_3 depends strongly on the energetics at the interface between the CsSnI_3 and the ETL, much like that seen at donor/acceptor heterojunctions in OPVs.⁴⁶

For practical applications the stability of the light harvesting semiconductor towards oxidation in air is critically important.

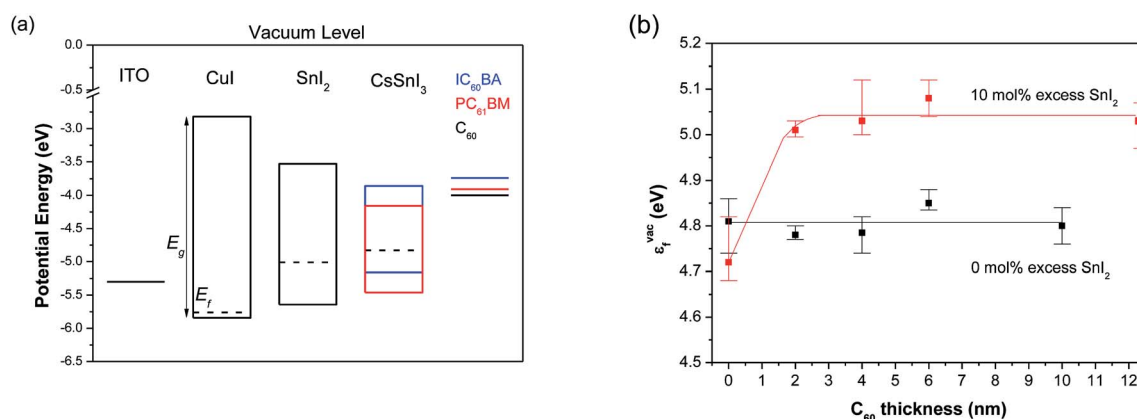


Fig. 4 (a) Schematic energy level diagram depicting the valence band edge for CuI and SnI_2 films measured using UPS. The conduction band edge for CuI has been estimated from the electronic absorption spectrum (ESI, S8†). The energy of the conduction band edge for SnI_2 is estimated based on the reported E_{g} for SnI_2 .⁴⁴ The work function of freshly UV/ O_3 treated ITO glass, CuI , SnI_2 and CsSnI_3 measured using the Kelvin probe technique are also shown; (b) the evolution of $\varepsilon_{\text{f}}^{\text{vac}}$ at the surface of a freshly deposited CsSnI_3 film with increasing thicknesses of thermally evaporated C_{60} measured using the Kelvin probe technique. The error bars represent maximum and minimum values.

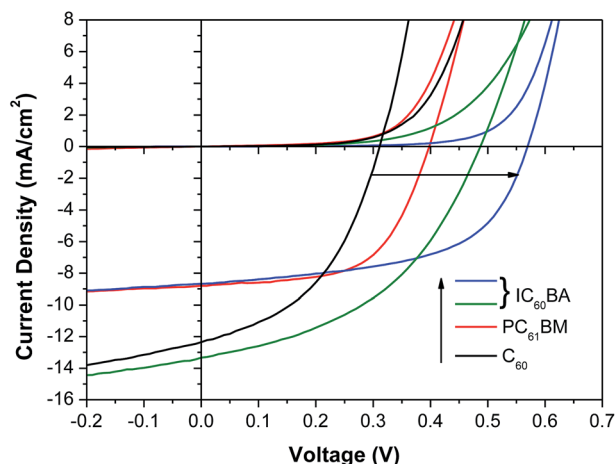


Fig. 5 Dark and light JV curves of best performing PPV devices with the structure ITO|CuI|CsSnI₃ + 10 mol% SnI₂|fullerene|BCP|Al, using different fullerene ETLs. For the case of ICBA the device V_{oc} was found to be strongly dependent on the concentration of the ICBA solution used: 3 mg ml⁻¹ ICBA (green), 5 mg ml⁻¹ ICBA (blue), although was always higher than that achieved in devices using PCBM when all other parts of the device were identical.

Encouragingly PV devices based on CsSnI₃ prepared in a Sn-rich environment have been reported to be stable for more than 250 hours when stored in glove box,²⁷ although – to our knowledge – the stability of Sn perovskite PVs in air has not been quantitatively reported. We find that the use of excess SnI₂ in the preparation of CsSnI₃ dramatically improves device

stability. Fig. 6 shows the key performance parameters of *unencapsulated* PPV devices (using C₆₀ as the ETL) as a function of SnI₂ excess in the CsSnI₃ layer. The data points in the hashed region correspond to device performance immediately after fabrication and after storage for 10 days in a nitrogen filled glove box (<5 ppm O₂ and <0.1 ppm H₂O). For devices based on CsSnI₃ films prepared using stoichiometric amounts of CsI and SnI₂ the η of is reduced by $\sim 70\%$ after this ten day period of storage due to the combined effect of deteriorated J_{sc} , V_{oc} and FF. Conversely devices based on CsSnI₃ with 10 mol% excess SnI₂ exhibit a relatively small reduction in η of $\sim 10\%$ due primarily to a reduction in FF. These devices were then brought into the laboratory and the performance tested as a function of time. Devices with 0 mol% excess SnI₂ stopped working altogether after ~ 30 minutes air exposure due to a complete collapse in J_{sc} . Conversely devices with 10 mol% excess SnI₂ exhibit dramatically improved stability such that even after 14 hours in air without encapsulation they still function, albeit with a J_{sc} of one fifth the starting value. Both the FF and V_{oc} are essentially stable during this period.

It is likely that the deterioration in device performance upon exposure to the laboratory environment results from a number of parallel degradation mechanisms some of which are not related to the perovskite layer, such as oxidation of the Al cathode and/or doping of the C₆₀ layer by oxygen.⁴⁷ However, since it is evident from our XPS studies (ESI, Fig. S4†) that oxidation of CsSnI₃ in air to form Cs₂SnI₆ is a very rapid process and that Cs₂SnI₆ is known to absorb light very weakly as compared to CsSnI₃,⁴¹ it is likely that conversion of CsSnI₃ into Cs₂SnI₆ is major contributor to the

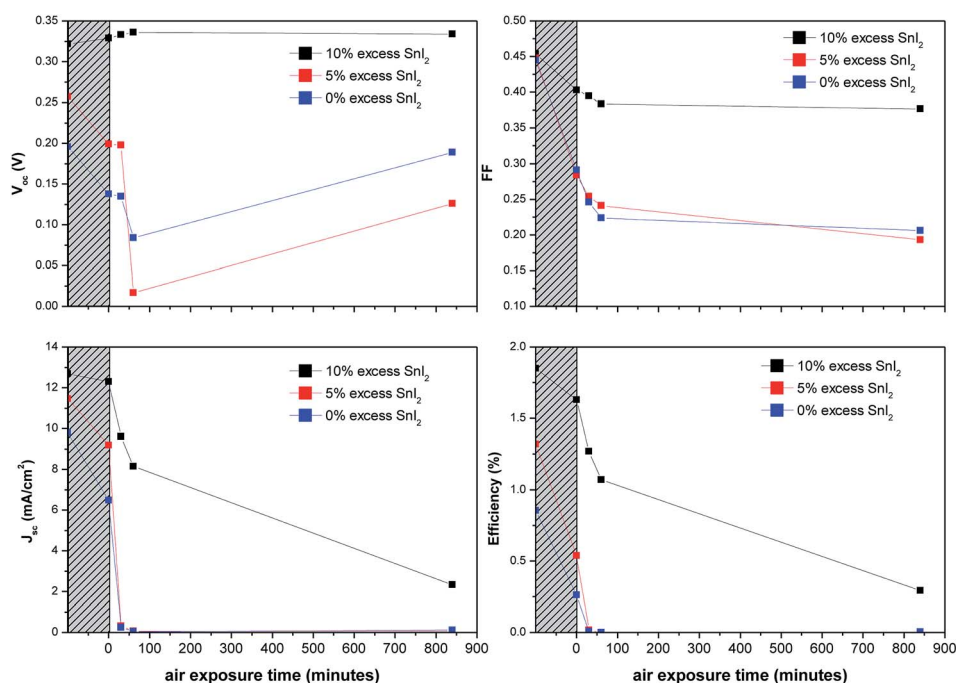


Fig. 6 Key device performance characteristics for unencapsulated PPV devices with the structure ITO|CuI|CsSnI₃/C₆₀|BCP|Al using CsSnI₃ prepared from solutions containing either 0, 5 or 10 mol% excess SnI₂. The data in the hashed region correspond to device performance immediately after fabrication and then again after storage in a nitrogen atmosphere (<5 ppm O₂ and <0.1 ppm H₂O) for 10 days. These devices were then brought into laboratory and the performance was measured as a function of time stored in air.



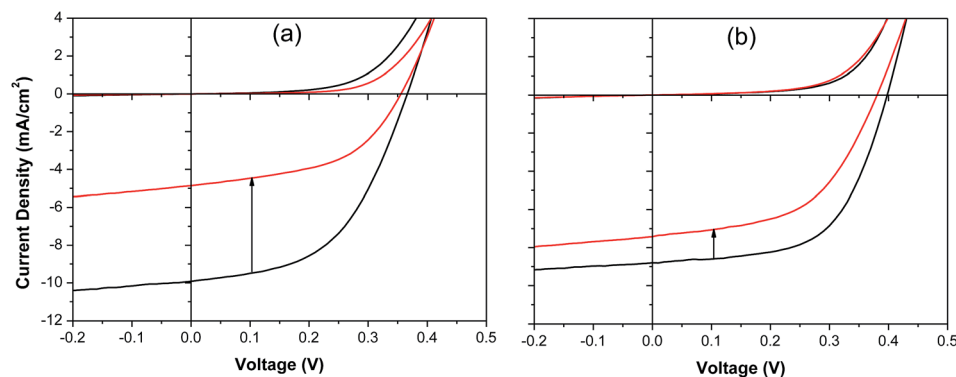


Fig. 7 Representative J - V characteristics of PPV devices with the structure: ITO|CuI 70 nm|CsSnI₃ (with and without 10 mol% excess SnI₂) |PCBM|BCP|Al: (a) CsSnI₃ with no excess SnI₂; (b) CsSnI₃ with 10 mol% excess SnI₂. Graphs show devices made entirely in glove box (black) and those made using a CsSnI₃ layer exposed to the laboratory environment for 1 minute (red) before being returned to the glove box to complete fabrication and testing.

continuous deterioration in J_{sc} . This conclusion is supported by the evolution of the absorption spectra of CsSnI₃ films on glass, with and without 10 mol% SnI₂, as a function of time exposed to air ((ESI, Fig. 9†)), from which it is evident that the addition of 10 mol% SnI₂ greatly reduces the rate of oxidation of the CsSnI₃. Since this process would be expected to begin at the interface with the ETL any significant change in the energetics at this interface for electron extraction as a result of this transformation would manifest as a change in device series resistance and FF, the effect of which would be most pronounced in the early stages of oxidation process when the potential energy step forms. Cs₂SnI₆ has been shown to have a relatively high electron and hole mobility⁴⁸ although its conduction band edge is ~ 200 meV

deeper lying than that of CsSnI₃⁴¹ and so the formation of a Cs₂SnI₆ layer between the remaining CsSnI₃ and C₆₀ would most likely impede the flow of electrons from the conduction band of the CsSnI₃ into the LUMO of the C₆₀ resulting in an increase in device series resistance and reduction in FF, as is observed to be the case. To test the generality of this result for another fullerene ETL and to remove the complexity associated with possible doping of the fullerene layer by oxygen upon exposure to air we have fabricated PPV devices using PCBM as the ETL with and without exposure of the CsSnI₃ layer to air for 1 minute, before transferring back into the glove box to complete device fabrication. Typical J - V characteristics for PPV devices with and without exposure of the perovskite layer to air are given in Fig. 7 from

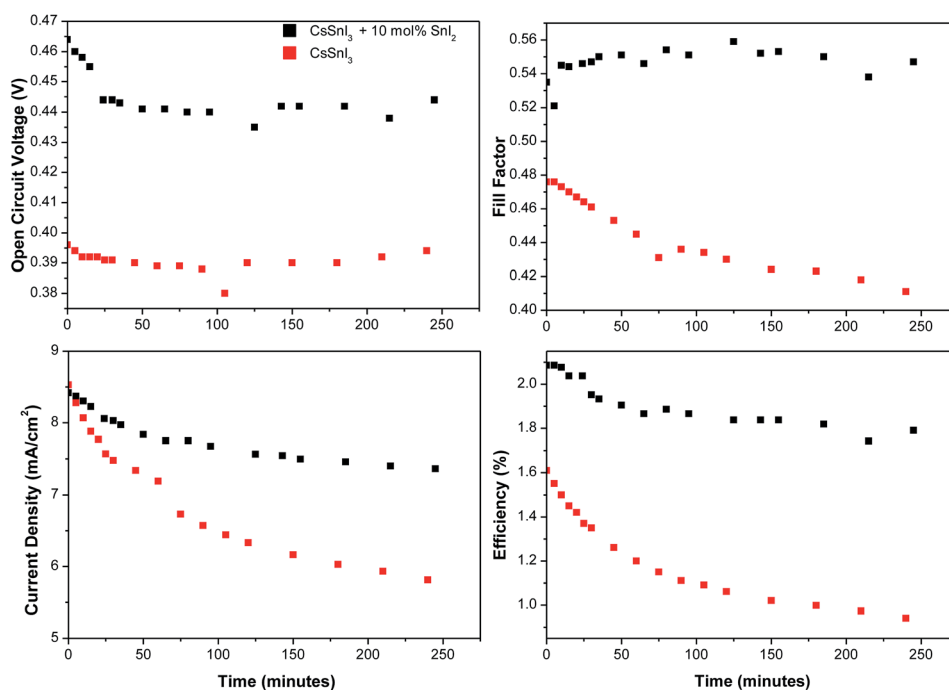


Fig. 8 Graphs showing the effect of constant illumination (100 mW cm^{-2} of AM 1.5) for 245 minutes. Device structure; ITO|CuI (70 nm)|CsSnI₃ + 0% or 10% SnI₂ (8 wt%, 4000 rpm)|PC₆₁BM (15 mg ml⁻¹, 1000 rpm)|BCP (8 nm)|Al (50 nm).



which it is clear that devices incorporating an excess of SnI_2 are much more stable than those without and that the changes in J_{sc} , V_{oc} and FF after 1 minute air exposure are consistent with that in Fig. 6.

Finally, we have tested the stability of devices, with and without 10 mol% excess SnI_2 in the CsSnI_3 layer, over a period of four hours continuous illumination in a nitrogen atmosphere. Representative results are given in Fig. 8, from which it is clear that devices incorporating excess SnI_2 exhibit greatly improved stability including a stable V_{oc} and FF after the first 20 minutes. Whilst J_{sc} deteriorates rapidly by $\sim 10\%$ within the first 50 minutes the rate of reduction in J_{sc} then slows markedly. Conversely devices with no excess of CsSnI_3 exhibit a continuous rapid decline in FF and J_{sc} .

Collectively the aforementioned stability studies show that the use of a 10 mol% excess of SnI_2 in CsSnI_3 preparation imparts dramatically improved device stability, which indicates that the stability of CsSnI_3 towards oxidation is very sensitive to the density of Sn vacancy defects. Since the transformation of CsSnI_3 into Cs_2SnI_6 requires the loss of Sn it seems plausible that at the site of a Sn vacancy in CsSnI_3 the barrier to transformation into Cs_2SnI_6 is reduced. It is also possible that the excess SnI_2 occupies some of the space between adjacent CsSnI_3 crystallites thereby impeding the ingress of the oxygen and water that gives rise to oxidation.

Conclusion

In summary we have demonstrated the preparation of orthorhombic CsSnI_3 films from solution at room temperature with defect densities low enough for use as the light harvesting semiconductor in photovoltaic devices, even without the addition of excess Sn. We have shown how the V_{oc} in a model planar layer device based on a $\text{CuI}|\text{CsSnI}_3|\text{fullerene p-i-n}$ structure can be doubled from ~ 0.28 V to ~ 0.55 V by engineering the energetics at the $\text{CsSnI}_3/\text{fullerene}$ interface. To our knowledge, the champion device V_{oc} , FF and η are the highest that have been reported using CsSnI_3 as the light harvesting semiconductor in a PPV by a considerable margin. We have also demonstrated a simple and effective strategy for improving the stability of PPV devices based on CsSnI_3 and have rationalised the reason for this improvement using a combination of photoelectron spectroscopy, contact potential measurements and device based studies. This work shows that if tin-halide perovskites are prepared in a tin rich environment, in order to minimise the density of Sn vacancy defects, then consideration must be made as to the charge transport properties of the source of the excess tin.

Materials

CuI (Sigma-Aldrich, 99.999%), CsI (Sigma-Aldrich 99.9%), SnI_2 (Alfa Aesar, 99.999%), PC_{61}BM (Nano-C, 99.5%), indene- C_{60} bis-adduct (IC_{60}BA) (Ossila, 99.5%), C_{60} (Nano-C, 99.5%), bathocuproine (Alfa Aesar, 98%), N,N -dimethylformamide (DMF) (VWR, anhydrous, 99.8%), chlorobenzene (Sigma-Aldrich, anhydrous, 99.8%), acetone (Sigma-Aldrich, GPR, $\geq 99\%$), propan-2-ol (Sigma-Aldrich, HPLC, 99.8%), deionised H_2O

(pureit dispenser, >10 M Ω). CsI and non-anhydrous solvents were stored in air. All other chemicals were stored in nitrogen filled glove box (<5 ppm O_2 and <0.1 ppm H_2O).

Synthesis of caesium tin(II) iodide

CsI was added to SnI_2 in a 1 : 1.1 molar ratio under nitrogen. To this DMF was added to make an 8 wt% solution which is bright yellow in colour. The solution was shaken for a few minutes until all CsI and SnI_2 had dissolved and was left overnight before use. The perovskite material precipitated on spin casting to give dark red/brown coloured films.

Device fabrication

Solar cells were fabricated on indium tin oxide (ITO) coated glass slides (Thin Film Devices Inc., $15 \pm 3 \Omega \text{ sq}^{-1}$). The slides were prepared by cleaning the ITO using acetone, water and isopropanol in an ultrasound bath for 10 minutes each followed by UV/O_3 treatment for 15 minutes. The slides were then loaded into a dry nitrogen filled glove box for device fabrication. 70–100 nm thick CuI films were deposited onto the ITO glass substrates by thermal evaporation. CuI can also be deposited by spin casting a 50 mg ml^{-1} in chlorobenzene : di-*n*-propyl sulfide (5.5 : 1) at 1000 rpm. Onto this 8 wt% CsSnI_3 with a 10 mol% excess of SnI_2 was spin cast at 4000 rpm from a DMF solution for 60 s. 40 nm of C_{60} was the deposited by thermal evaporation at a rate of 0.1–0.4 nm s^{-1} . PC_{61}BM was spin cast at 1000 rpm from 15 mg ml^{-1} chlorobenzene solution, and IC_{60}BA from 3, 5 or 7 mg ml^{-1} chlorobenzene solutions for 60 s. The PCBM and ICBA solutions were filtered with a 0.2 μm PTFE syringe filter before use. Finally 8 nm of bathocuproine (BCP) followed by 50 nm of Al were deposited by thermal evaporation at a rate of 0.5–1 \AA s^{-1} . The Al was evaporated through a mask to produce 0.06 cm^2 pixels. For devices without CuI , ITO was cleaned in the same way, but was put under the UV/O_3 lamp for 5 minutes longer to help the perovskite film wet the surface.

Device testing

Device testing was performed in the same glove box as used for device fabrication unless otherwise stated. Current–voltage (J – V) curves were measured using a Keithley 2400 source-meter under AM1.5G solar illumination at 100 mW cm^{-2} (1 sun) by sweeping from -1 V to $+1$ V in 0.01 V increments at a rate of 0.1 V s^{-1} . There was no significant current–voltage hysteresis, as shown in Fig. S10.† IPCE measurements were carried out using a Scientech SF150 xenon arc lamp and a PTI monochromator, with the monochromatic light intensity calibrated using a Si photodiode (Newport 818-UV). The incoming monochromatic light was chopped at 500 Hz. For signal measurement a Stanford Research Systems SR 830 lock-in amplifier was used.

X-ray diffraction

CsSnI_3 films were spin cast from 16 wt% DMF solutions at 2000 rpm onto glass slides coated with 10 nm of CuI . The X-ray



diffraction patterns were collected under a flow of nitrogen using a Cu $K\alpha_{1/2}$ source in θ - θ mode on a Bruker D8 Advance powder diffractometer equipped with an Anton-Paar HTK900 gas chamber. Patterns were corrected for the height of the sample by calibrating the 2θ axis against reference patterns. Simulated diffraction patterns were calculated using the program Mercury 3.1 with CIFs from the Inorganic Crystal Structure Database (ICSD).

UPS and XPS

UPS and XPS were performed on freshly prepared CsSnI_3 films spin cast onto Au substrates. The samples were exposed to the laboratory environment for ~ 1 min prior to loading into the ultra-high vacuum system. UPS spectra were recorded using UV photons with energy 21.22 eV from a He I plasma source. The X-ray photoelectron spectroscopy (XPS) measurements were carried out in a UHV system with a base pressure of 5×10^{-11} mbar. The sample was excited with X-rays from a mono-chromated Al $K\alpha$ source ($h\nu = 1486.6$ eV) with the photoelectrons being detected at a 45° take-off angle using an Omicron Sphera electron analyser.

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