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# Highly reliable switching *via* phase transition using hydrogen peroxide in homogeneous and multi-layered GaZnO<sub>x</sub>-based resistive random access memory devices†

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**Here, we propose an effective method for improving the resistive switching characteristics of solution-processed gallium-doped zinc oxide (GaZnO<sub>x</sub>) resistive random access memory (RRAM) devices using hydrogen peroxide. Our results imply that solution processed GaZnO<sub>x</sub> RRAM devices could be one of the candidates for the development of low cost RRAM.**

Conventional nonvolatile flash memory has been extensively studied in recent years due to its low fabrication cost and density comparable to that of 2X nm generations. However, flash memory still suffers from limitations such as low endurance, low write speed, and high voltage requirements for write operations.<sup>1</sup> In addition, the downscaling of flash memory is expected to reach physical limits in near future due to retention degradation. When the tunnelling oxide thickness is less than 10 nm, stored charges of the floating gate easily escape because of defects in the tunnelling oxide, formed by repeated write/erase operations or direct tunnelling current.<sup>2</sup> Hence, resistive random access memory (RRAM) based on reversible resistive switching (RS) behaviour has been extensively studied for use in next-generation nonvolatile data-storage devices.<sup>2–5</sup> Many candidate materials have been reported for RRAM devices, including transition metal oxides (TMOs), perovskites such as Pr<sub>0.7</sub>Ca<sub>0.3</sub>MnO<sub>3</sub> and SrZrO<sub>3</sub>, amorphous silicon, organic and inorganic materials.<sup>3–9</sup> Among these materials, TMOs are promising candidates as they exhibit reliable and stable switching, improved high resistance state to low resistance state (HRS/LRS) ratios, high endurance, and complementary metal oxide semiconductor (CMOS) process compatibility.<sup>10,11</sup> In particular, zinc oxide (ZnO) based TMOs have been actively investigated since they are considered to be environmentally friendly,<sup>12</sup> exhibit a large HRS/LRS ratio, are transparent and flexible, and their raw materials are available in natural abundance.<sup>13,14</sup> Gallium-doped zinc oxide

(GaZnO<sub>x</sub>) has shown particular promise recently for its low cost and thermal stability.<sup>15</sup> GaZnO<sub>x</sub> can be fabricated using various techniques including atomic layer deposition,<sup>16</sup> pulse laser deposition,<sup>17</sup> sputtering,<sup>18</sup> and a solution process.<sup>19</sup> The solution process has a number of advantages, including the simplicity of the process, easy control of substances, low cost, and selective deposition.<sup>20</sup> Among the solution process technologies, however, the spin coating method requires repeated processing to achieve optimized active layer thickness, which may result in interface barriers between active layers that affect the RS characteristics of the RRAM device. Also, solution processed TMOs contain intrinsic oxygen vacancies, denoted V<sub>O</sub>, which may be shown through a filament model to form a conduction path in storage media.<sup>21</sup> Among switching mechanism models, the widely used nanoscale conductive V<sub>O</sub> filament model shows applicability to an extensive range of materials and compares well to experimental measurements;<sup>21</sup> use of this model has demonstrated that control of oxygen species (oxygen and V<sub>O</sub>) is necessary for improving RS characteristics. Also, reducing the number of V<sub>O</sub> filaments assists in lowering the RESET current and raising the resistance. Many studies have been undertaken to ameliorate these problems *via* doping, post-treatment, and development of multi-layer structures.<sup>2,22,23</sup> In this communication, we propose an effective method for controlling V<sub>O</sub> concentrations, and investigate the phenomenon of phase transition using hydrogen peroxide (H<sub>2</sub>O<sub>2</sub>) in solution processed GaZnO<sub>x</sub> RRAM devices with a homogeneous multi-layered (HML) structure. Aluminum and heavily doped p-type silicon (p<sup>+</sup>Si) were used for the top and bottom electrodes, respectively; these are conventional materials in microelectronics systems, and serve as a good integration platform. Lastly, we studied the correlation between switching behavior and the material phase of GaZnO<sub>x</sub> by varying the deposition location of H<sub>2</sub>O<sub>2</sub> treated GaZnO<sub>x</sub>: pristine, top (top treatment), middle (middle treatment), and bottom (bottom treatment). Detailed experimental conditions and steps are provided in the ESI.† Fig. 1 shows the atomic percentage with depth for each H<sub>2</sub>O<sub>2</sub>-treated GaZnO<sub>x</sub> thin film according to deposition location: pristine 1(a), top 1(b), middle 1(c), and bottom deposition 1(d), analyzed using X-ray photoelectron spectroscopy (XPS). XPS spectra were

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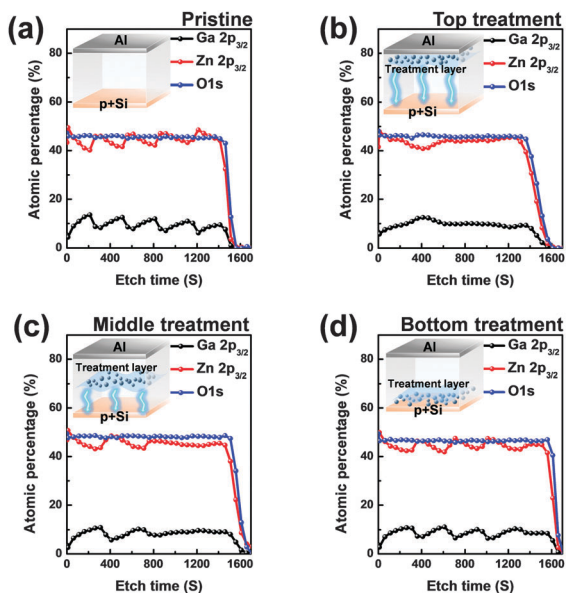


Fig. 1 XPS depth profiles to observe composition variations according to deposition location of  $\text{H}_2\text{O}_2$ -treated  $\text{GaZnO}_x$  thin films for (a) pristine, (b) top treatment, (c) middle treatment, and (d) bottom treatment (the inset image shows the schematic of the deposition location of the  $\text{H}_2\text{O}_2$ -treated  $\text{GaZnO}_x$  thin film).

obtained using an Al monochromated X-ray source (Al  $K\alpha$  line: 1486.6 eV). The etch rate of the XPS depth analysis was  $0.15 \text{ nm s}^{-1}$  using ionic Ar at 1 keV. We observed that atomic percentages of Ga and Zn fluctuate periodically in the pristine  $\text{GaZnO}_x$  film with increasing etch time, as shown in Fig. 1(a). These results suggest that the Ga and Zn composition in the multi-layered pristine  $\text{GaZnO}_x$  thin film is segregated at the interfaces during the pre-annealing process, since the film was formed using five repeated processes to optimize the thickness (four times for each pristine  $\text{GaZnO}_x$  sub-layer and once for  $\text{H}_2\text{O}_2$ -embedded  $\text{GaZnO}_x$ ). This segregation may derive from doping inefficiencies or from the maximum solubility of Ga.<sup>24</sup> The segregated phase at the interface can affect the formation and rupture of  $\text{V}_\text{O}$  filaments, which degrades the electrical characteristics of a RRAM device. The top-treated  $\text{GaZnO}_x$  thin film, however, showed smooth atomic Ga and Zn percentages, as shown in Fig. 1(b). These results indicate the effect of  $\text{H}_2\text{O}_2$  on HML  $\text{GaZnO}_x$  thin films. XPS depth profiles were also measured for the other deposition locations (middle and bottom). Fig. 1(c) and (d) clearly shows smooth Ga and Zn atomic percentages, blended by the  $\text{H}_2\text{O}_2$  treatment. This implies that atomic arrangement varies for  $\text{GaZnO}_x$  treated with  $\text{H}_2\text{O}_2$  when compared with the pristine  $\text{GaZnO}_x$  thin film. Fig. 2(a) and (b) show the XPS depth spectra for Ga  $2p_{3/2}$  and Zn  $2p_{3/2}$  peaks, for both  $\text{H}_2\text{O}_2$ -treated  $\text{GaZnO}_x$  and the non-treated  $\text{GaZnO}_x$  region in the middle-treated  $\text{GaZnO}_x$  thin film. Each spectrum was deconvoluted using a Gaussian distribution after correcting for background. The Ga  $2p_{3/2}$  and Zn  $2p_{3/2}$  spectra were centered at 1118.77 eV and 1022.21 eV, respectively, corresponding to GaO and ZnO.<sup>25,26</sup> As shown in Fig. 2(a) and (b), the Ga  $2p_{3/2}$  and Zn  $2p_{3/2}$  peaks shift to higher binding energy in the  $\text{H}_2\text{O}_2$ -treated  $\text{GaZnO}_x$  region, suggesting that the GaO–ZnO film formed not just as a

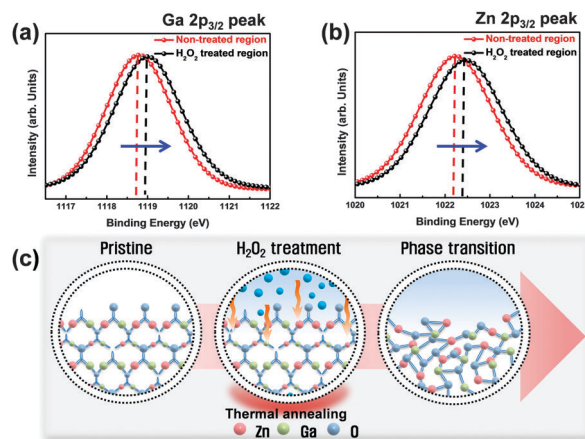


Fig. 2 XPS results for (a) Ga  $2p_{3/2}$  and (b) Zn  $2p_{3/2}$  peaks for middle-treated  $\text{GaZnO}_x$  thin film (c) schematic of phase transition.

physical mixture with two distinct GaO and ZnO phases, but that an enhanced oxidation state was found in the  $\text{GaZnO}_x$  system. Based on the above results, the effects of  $\text{H}_2\text{O}_2$  treatment on the  $\text{GaZnO}_x$  film can be clearly seen. Oxygen radical diffusion by  $\text{H}_2\text{O}_2$  dissociation may affect the  $\text{GaZnO}_x$  phase during pre-annealing. These results are consistent with the study by Tsai *et al.*,<sup>27</sup> who assumed that the phase of the ZnO film can be changed from polycrystalline to amorphous-like by penetration of dissociated oxygen radicals. Fig. 2(c) shows the phase transition and blended arrangement of Ga and Zn due to  $\text{H}_2\text{O}_2$  treatment schematically, after  $300^\circ\text{C}$  pre-annealing. To verify the phase transition and crystallinity of  $\text{H}_2\text{O}_2$ -treated  $\text{GaZnO}_x$  films by oxygen radical diffusion, we compared the  $\text{H}_2\text{O}_2$ -treated  $\text{GaZnO}_x$  film with the pristine  $\text{GaZnO}_x$  film using grazing incidence X-ray diffraction (GIXRD), with a 3 kW sealed X-ray tube Cu  $K\alpha$  ( $\lambda = 1.541 \text{ \AA}$ ), as shown in Fig. 3. For the pristine  $\text{GaZnO}_x$  film, XRD analysis revealed no diffraction peaks, implying the amorphous phase. However, the  $\text{H}_2\text{O}_2$ -treated  $\text{GaZnO}_x$  film was found to be in the  $c$ -axis oriented polycrystalline phase, with the highest peak observed at  $34.48^\circ$ ,<sup>28</sup> indicating a  $\text{GaZnO}_x$  phase transition instigated by  $\text{H}_2\text{O}_2$  treatment. Overall, the  $\text{H}_2\text{O}_2$ -induced phase transition correlates well with the results of the previous study in terms of oxygen radical diffusion.<sup>27</sup>

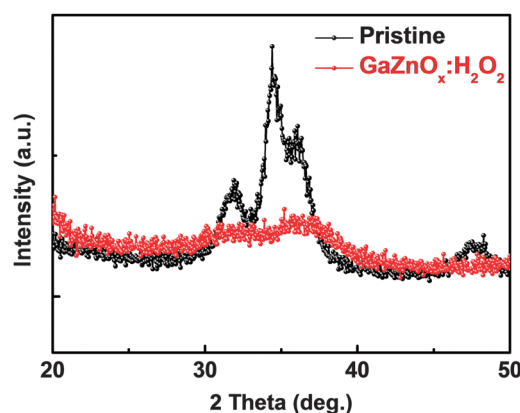


Fig. 3 Grazing incidence X-ray diffraction patterns of  $\text{GaZnO}_x$  thin films with and without  $\text{H}_2\text{O}_2$ .



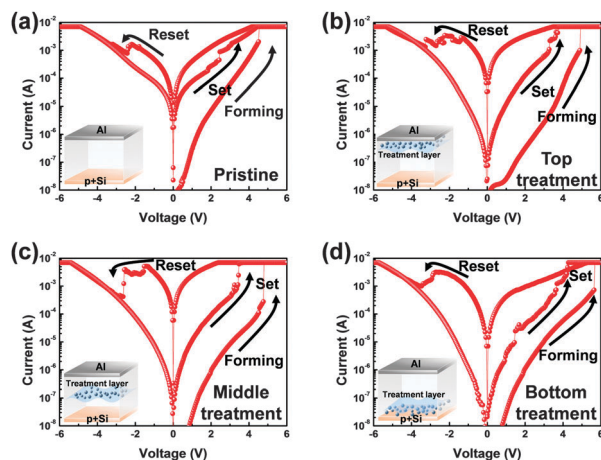


Fig. 4  $I$ - $V$  characteristics of the HML structure RRAM devices for (a) pristine, (b) top treatment, (c) middle treatment, and (d) bottom treatment.

We analyzed the electrical characteristics of all devices to investigate the effect of  $\text{H}_2\text{O}_2$  treatment on HML  $\text{GaZnO}_x$  RRAM devices. Fig. 4 shows the current-voltage ( $I$ - $V$ ) characteristics of the devices according to the deposition location. All devices show clear BRS characteristics. This bi-stable switching is due to the formation and rupture of  $\text{V}_\text{O}$  filaments.<sup>21</sup> The bottom electrode was ground, and a bias voltage (positive and negative) was applied to the top electrode for switching the device. When a sweep voltage ranging from zero to positive was applied to the top electrode of the pristine  $\text{GaZnO}_x$  device, a sudden decrease in resistance from HRS to LRS (defined as the SET process) was observed at about +2.43 V. As the applied voltage swept from positive to zero, LRS was maintained. Sweeping from zero to negative, an increase in resistance from LRS to HRS (defined as the RESET process) was observed at about -2.19 V. As the applied voltage swept from negative to zero, HRS was maintained. The current was limited to 7 mA during the high-to-low and low-to-high resistance switching processes to avoid permanent damage to the devices. The pristine  $\text{GaZnO}_x$ -based device exhibited poor BRS characteristics as shown in Fig. 4(a). There are two possible explanations; the first is the generation of excess intrinsic  $\text{V}_\text{O}$  in the solution processed  $\text{GaZnO}_x$  thin film. Since the formation and rupture of conduction filaments (CFs) are attributed to  $\text{V}_\text{O}$ , appropriate control of  $\text{V}_\text{O}$  in TMO-based RRAM is a necessity. Therefore, excess intrinsic  $\text{V}_\text{O}$  induces poor switching characteristics. The second explanation is that nanocrystalline grain boundaries in pristine  $\text{GaZnO}_x$  cause non-uniform switching characteristics. Grain boundaries are known to be preferential sites for  $\text{V}_\text{O}$  filament formation, it is therefore commonly accepted that excess electrical conduction paths might form due to the high number of grain boundaries in the nanocrystalline  $\text{GaZnO}_x$  thin film.<sup>29</sup> Also, random nucleation and growth of CFs along grain boundaries make their formation difficult to control, which is a significant impediment to device performance improvement. In contrast, top-, middle-, and bottom-treated  $\text{GaZnO}_x$  devices exhibited an improvement in the switching window (of the order of  $\sim 10^2$ ) as shown in Fig. 4(b)-(d), respectively. For bottom-treated  $\text{GaZnO}_x$  devices, however, HRS and LRS were shifted toward the low current level and showed a multi-step SET

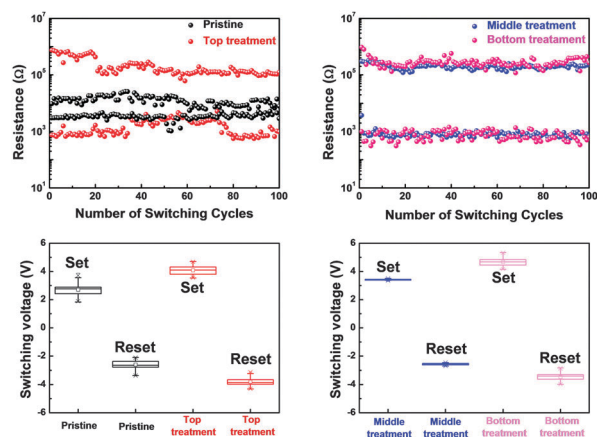


Fig. 5 Comparison of endurance characteristics and SET/RESET voltage distribution of the HML structure RRAM devices.

voltage (Fig. 4(d)). This phenomenon is attributed to the  $\text{GaZnO}_x/\text{Si}$  interface at the bottom electrode, where a native oxide forms because of the strong oxidation potential of  $\text{H}_2\text{O}_2$  and large free energy of formation.<sup>30-32</sup> The formation of native oxide at the  $\text{GaZnO}_x/\text{Si}$  interface interrupts the generation of uniform CFs and impedes current flow. To compare the electrical stability of devices, we carried out endurance tests at a reading voltage of 0.4 V and plotted SET/RESET distribution during 100 switching cycles, as shown in Fig. 5. With the exception of the pristine device, all devices showed enhanced endurance characteristics up to 100 cycles. However, resistance fluctuations were observed in top and bottom treated  $\text{GaZnO}_x$  devices, possibly due to the amorphized  $\text{GaZnO}_x$  phase transition and formation of native oxide at the bottom electrode. Also, the SET and RESET voltages for the top-treated  $\text{GaZnO}_x$  device were higher than for the middle-treated sample, since  $\text{H}_2\text{O}_2$  treatment increases the metal-oxide bond in  $\text{GaZnO}_x$  and causes a material phase change from polycrystalline to amorphous. These results are consistent with middle-treated samples. In contrast, the middle-treated device exhibited a highly uniform SET/RESET distribution compared with other devices. Overall, the middle-treated  $\text{GaZnO}_x$  device showed better BRS characteristics, including SET/RESET distribution, endurance, and switching window, amongst the four comparison groups. Fig. 6 shows a feasible BRS conduction mechanism for the middle-treated  $\text{GaZnO}_x$  device. The schematic is based on characteristics resulting from the rupture and formation of conductive  $\text{V}_\text{O}$  filaments at the interfaces between non-treated  $\text{GaZnO}_x$  and  $\text{H}_2\text{O}_2$ -treated  $\text{GaZnO}_x$ , caused by a redox reaction. When the applied voltage reaches the positive forming voltage to create a conduction path, oxygen ions attracted from the amorphous region move toward the top Al electrode and the  $\text{V}_\text{O}$  simultaneously move downward. Compared with the non-treated  $\text{GaZnO}_x$  layer, higher densities of lattice and interstitial oxygen ions exist in  $\text{H}_2\text{O}_2$ -treated  $\text{GaZnO}_x$ , hence uniform CFs were formed in the  $\text{H}_2\text{O}_2$ -treated region. Thus, asymmetrically shaped CFs with weak CF offshoots were formed at the interface, causing the device to switch to the LRS. When a negative bias is applied to the top Al electrode, the repelled oxygen ions



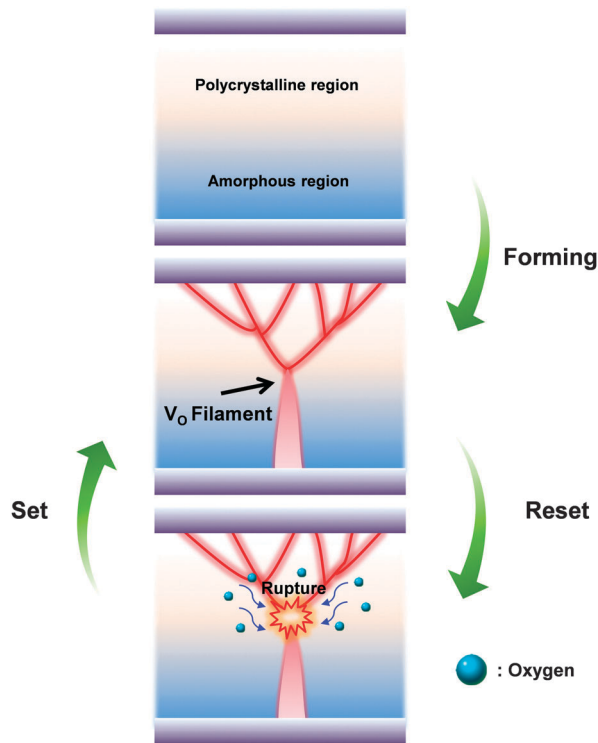


Fig. 6 The conduction mechanism schematic for the BRS in the middle-treated GaZnO<sub>x</sub> RRAM device.

from the non-treated polycrystalline region move to the polycrystalline/amorphous interface, where they induce local Joule heating and oxidation. The narrow size of the asymmetric CFs at the interface induces intense local Joule heating, resulting in switching to HRS. This localized interface RS reduces the problem of escape of oxygen ions from electrodes in the middle-treated device.<sup>33</sup> Hence, the middle-treated device showed superb RS characteristics compared with other devices, with improved switching parameters such as SET/RESET distribution, endurance and on/off ratio.

In this communication, we investigated the phase transition of solution-based HML GaZnO<sub>x</sub> RRAM devices using H<sub>2</sub>O<sub>2</sub> to improve the RS characteristics such as SET/RESET uniformity, endurance, and switching window. Significant variations in *I*-*V* characteristics were observed, depending on the deposition location of H<sub>2</sub>O<sub>2</sub>-treated GaZnO<sub>x</sub>. The pristine GaZnO<sub>x</sub> based device exhibited poor BRS characteristics such as large variation in switching voltage, narrow switching window, and poor reproducibility during repeated switching cycles. In contrast, the H<sub>2</sub>O<sub>2</sub>-treated RRAM devices showed improved BRS characteristics. In particular, the middle-treated device exhibited a high LRS/HRS ratio (of the order of ~10<sup>2</sup>), stable endurance, and narrow dispersion of SET/RESET switching voltage. These results suggest that H<sub>2</sub>O<sub>2</sub>-treated GaZnO<sub>x</sub> solution both assisted physical mixing in the segregated material system, and changed the material phase from polycrystalline to amorphous in pristine GaZnO<sub>x</sub> layers. Furthermore, H<sub>2</sub>O<sub>2</sub> effectively smoothens interfaces *via* diffusion of oxygen radicals dissociated from H<sub>2</sub>O<sub>2</sub> in solution-based HML GaZnO<sub>x</sub> RRAM devices. Solution based HML

GaZnO<sub>x</sub> RRAM devices using H<sub>2</sub>O<sub>2</sub> therefore enable the realization of low-cost portable electronics.

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