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Detrapping of the carriers from shallow states in a highly responsive, fast, broadband (UV-vis-NIR), self-powered SnSe/Si photodetector with asymmetric metal electrodes†

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Tin-selenide (SnSe), as an eco-friendly and low-cost semiconductor material, has exhibited great potential to detect weak signals and has wide applications in imaging and optical communication. Here, self-powered SnSe-based photodetectors (PDs) via asymmetric metal electrodes are fabricated to achieve weak light detection for broad spectral ranges from 385 to 1064 nm with high response and fast response (on/off) times. The results show that the SnSe PDs are highly sensitive in a wide spectral range from UV to NIR, even at 0 V applied bias. They exhibit an excellent responsivity (R) of 54.7 mA W⁻¹ and detectivity (D) of 7.87×10^{10} Jones, corresponding to rise/decay times of 26/47 ms under 1064 nm illumination at 0 V applied bias, respectively. With increasing the bias, detrapping of the carriers can be seen from the shallow trap states arising from the secondary phases in the SnSe. The results suggest that SnSe-based PDs obtained from the evaporation method have great potential in developing low-cost, next-generation, and self-powered optoelectronics.

1. Introduction

Over the past few decades, broad spectral photodetectors (PDs) have been extensively utilized in diverse fields, including optical communications, image processing, bioimaging, process control, chemical analysis, astronomy, security, thermal imaging, defense, night vision, environmental monitoring, process control, *etc.*^{1–5} Several inorganic semiconductor thin film materials, *i.e.*, perovskite,⁶ 2D-materials,⁷ graphene,⁸ CuInGaSe₂,⁹ Cu₂ZnSnS₄,¹⁰ Sb₂Se₃,¹¹ GaN,¹² PbS,¹³ *etc.*, have been explored regarding their capability for broadband photodetection. The above materials can

absorb specific electromagnetic spectra and generate appropriate signals for various applications. Recently, self-powered broadband thin-film PDs have received enormous recognition and hold great promise due to their built-in photoelectric detection capability, excellent responsivity (R), broadband photodetection, high detectivity (D), ultrafast speed (on/off), and high external quantum efficiency (EQE) even at 0 V applied bias.^{14–16} Thin-film PDs with self-powered characteristics can open a new avenue for next-generation flexible optoelectronics.

Graphene,¹⁷ perovskite,¹⁸ and MoS₂¹⁹ materials have displayed excellent performance; however, their large-scale commercial production is hindered due to their low absorption, stability issues, and complex fabrication processes. Furthermore, CuInGaSe₂ has also been explored for broadband photodetection from visible to near-infrared (Vis-NIR), but the presence of rare and expensive elements, *i.e.*, In and Ga, restricts their practical applications.²⁰ Moreover, Cu₂ZnSn(S,Se)₄ has attractive optoelectronic properties like low-cost, eco-friendly nature, formation from earth-abundant elements, but suffers from low minority-carrier lifetime and intrinsic defect formation, resulting in low device performance compared to similar materials.^{21,22} These issues suggest the search for an alternative material with a less complex fabrication process, low-cost, earth-abundance, eco-friendly nature, and optoelectronic properties similar to CuInGaSe₂ and other materials. In recent times, binary element-based semiconductor materials such as SnSe thin films have gained enormous attention

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owing to their suitable thermoelectric^{23–25} and optoelectronic properties,^{26–29} *i.e.*, low-cost, eco-friendliness, earth-abundance, tunable bandgap (from 1.1 to 1.7 eV), high optical absorption coefficient ($\sim 10^5 \text{ cm}^{-1}$), and chemical stability.³⁰ Yan *et al.*³¹ developed flexible, ultra-broadband SnSe PDs that exhibited a spectral response up to $10.6 \mu\text{m}$ enabled by the photo-bolometric effect. Dong *et al.*³² reported 2D layered SnSe films for a UV-visible-NIR PD array with a high R of 277.3 A W^{-1} , EQE of $8.5 \times 10^{4\%}$, and D of 7.6×10^{11} Jones. Moreover, Jianfeng *et al.*³³ fabricated broadband, self-powered SnSe PDs *via* an ultrafast laser. Despite excellent PD device performance, complex and expensive fabrication processes hinder their practical applications. Meanwhile, SnSe thin films grown using thermal evaporation offer a less complicated fabrication process, suggesting a potential route to develop SnSe-based high-quality absorber materials for optoelectronic devices.

Herein, we report self-powered, broadband SnSe-based PDs fabricated on a n-Si substrate using thermal evaporation. This study observed the effect of asymmetric metal electrodes on the SnSe PD performance. The fabricated SnSe device exhibits excellent broadband (Vis-NIR) photoresponse at 0 V applied bias voltage with an R of 54.7 mA W^{-1} and D of 7.87×10^{10} Jones under 1064 nm. Moreover, fast rise/decay (on/off) times of 26/47 ms under 1064 nm at 0 V applied bias were observed. The existence of the shallow states acts as a trapping center for the carriers at low biases. In contrast, the higher bias triggers the detrapping of the carriers, as inferred from the power law and ultrafast pump-probe study. This study suggests insights for developing low-cost and self-powered SnSe-based next-generation optoelectronic devices.

2. Experimental details

SnSe thin films were deposited onto the n-Si substrate from in-house-grown SnSe powder (synthesized using the solid-state reaction method) *via* thermal evaporation. Detailed synthesis and deposition conditions of the SnSe thin films can be found in our previous reports.^{34,35} The oxide layer formed on the n-Si substrate was not removed intentionally, so there was a SiO_2 layer on the n-Si substrate. The amorphous nature of the SiO_2 helps to achieve the neutral and smooth surficial characteristics that help to reduce the lattice mismatch.³⁶ The asymmetric Pt and Ag metal contact on the SnSe/n-Si thin film was deposited using sputtering and thermal evaporation methods *via* shadow masking with a spacing between the two electrodes of about 100 microns. The performance of the fabricated SnSe PDs was measured using different laser light illuminations (*i.e.*, 385, 532, and 1064 nm) *via* a photodetection measurement facility (Cascade Microtech EPS150TRIAx), kept in a shielded enclosure (EPS-ACC-SE750) for low-signal measurements.

3. Results and discussion

3.1. Structural, optical, and morphological properties

The structural properties of the SnSe thin film were characterized using X-ray diffraction (XRD) and Raman spectroscopy. Fig. 1(a) shows that the XRD spectra of the SnSe thin film match well with the JCPDS data 89-0232 and reveal the presence of the pure SnSe phase. The peak at 14.18° showed the presence of the SnSe_2 phase (depicted as (001)SnSe₂ red highlighted in Fig. 1(a)).³⁵ Raman spectroscopy was carried out to confirm the

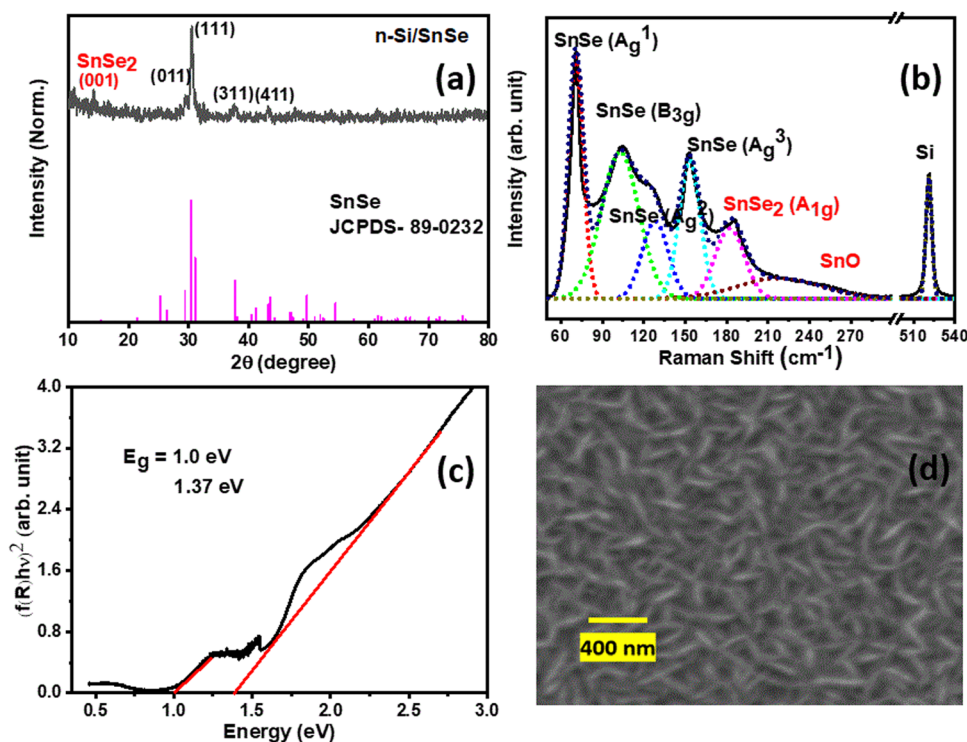


Fig. 1 (a) XRD spectra, (b) Raman analysis, (c) Tauc plot, and (d) SEM image of the SnSe/n-Si thin film.



SnSe phase further and peaks were observed at 70.67, 102.9, 128.16, and 153.24 cm^{-1} corresponding to the SnSe phase of the A_g^1 , B_{3g} , A_g^2 , and A_g^3 modes, respectively (Fig. 1(b)).³⁴ The Raman spectra show small peaks at 182.20 and 217.89 cm^{-1} corresponding to the SnSe_2 (A_{1g} mode) and rutile SnO_2 ,³⁷ respectively, whereas the peak at 521.16 cm^{-1} is assigned to the Si substrate.³⁶ From the Raman spectra, it is evident that the SnSe thin film is oxidized to a small extent. Oxidation of the SnSe thin film led to the presence of SnO_2 and, after rearrangement, the SnSe_2 phase.^{38,39} Fig. 1(c) shows the Tauc plot of SnSe thin film estimated using ultra-visible spectroscopy and an estimated bandgap of about 1.36 eV, and there is some absorption around 1.0 eV. Fig. 1(d) shows the scanning electron microscope (SEM) image of the SnSe thin film deposited using thermal evaporation on the Si substrate. The SEM image shows the formation of petal-type nanostructures of SnSe thin film prepared using the thermal evaporation method. Matthew *et al.*⁴⁰ demonstrated nanosheets of SnSe deposited onto a soda-lime glass (SLG) substrate using thermal evaporation. They observed nanosheets formed throughout the film, and these uniformly distributed nanosheets may develop because of an initial energetic preferential interaction with the glass substrate, which is maintained throughout the growth of the SnSe film. Singh *et al.*⁴¹ reported petal-type nanostructured $\text{Cu}_2\text{ZnSnS}_4$ (CZTS) thin films and studied their optical, structural, and morphological properties. Gour *et al.*¹⁰ demonstrated rice-like nanostructures of CZTS thin films on an SLG substrate deposited using sputtering and exhibited improved broadband (Vis-NIR) self-powered photodetection. They observed an enhanced optical absorption coefficient (10^5 cm^{-1}) owing to the surface modification of CZTS thin film.

3.2. Optoelectronic properties under different laser light illumination conditions

Fig. 2(a) shows the device architecture and Fig. 2(b) the current–voltage (I – V) measurement characteristics of the devices SnSe/n-Si and bare n-Si in the dark and under different laser light illuminations (385, 532, and 1064 nm). The device is shown in Fig. S1(a) (ESI†). SnSe on the n-Si substrate shows Schottky behavior with the electrodes, resulting in self-powered

characteristics.³² The work function of a semiconductor depends on the material's thickness, strain, and stress.^{42,43} The substrate choice induces different strain/stress on the deposited thin film, which may lead to the Schottky behavior of the Ag electrode on SnSe thin films. The Ag electrodes on the n-Si show Schottky behavior as the work function of Ag is greater than the work function of the n-Si semiconductor.⁴⁴ The bare n-Si-based device (Fig. S1(b), ESI†) performance was measured under different wavelengths at 250 mV applied bias under different laser light illuminations of 385, 532, and 1064 nm (Fig. S1(c)–(e), ESI†). The performance of the PDs has been estimated based on figures-of-merit, *i.e.*, photocurrent (I_{ph}), R , D , noise equivalent power (NEP), and response time (on/off) using the following eqn (1) and (2), where P is the optical power density of the incident photons illuminated, A is the active area of the device, I_{ph} is photocurrent and I_{d} is dark current, respectively.^{45,46}

$$I_{\text{ph}} = I_{\text{light}} - I_{\text{dark}} \quad (1)$$

$$R = \frac{I_{\text{ph}}}{P \times A} \quad (2)$$

$$D = \frac{R\sqrt{A}}{\sqrt{2eI_{\text{dark}}}} \quad (3)$$

$$\text{NEP} = \frac{\sqrt{(2 \times e \times I_{\text{dark}})}}{R} \quad (4)$$

Fig. 3 shows the figure of merit of the SnSe PDs under different laser light illuminations, *i.e.*, 385, 532, and 1064 nm at 0 V applied bias. The SnSe device performs under 385 nm mainly due to SnO, SiO_2 , and Si showing a photoresponse even in the UV region.³² In contrast, the photoresponse under 532 and 1064 nm laser illumination is contributed primarily by the SnSe thin film. The photocurrent of the device increases with the applied bias due to the increased drift velocity of the excited carriers. The device also showed performance under 0 V applied bias due to the asymmetric electrode contacts. Asymmetric contacts led to the different barrier heights at the electrodes and, hence, the net in-built electric field that directs the

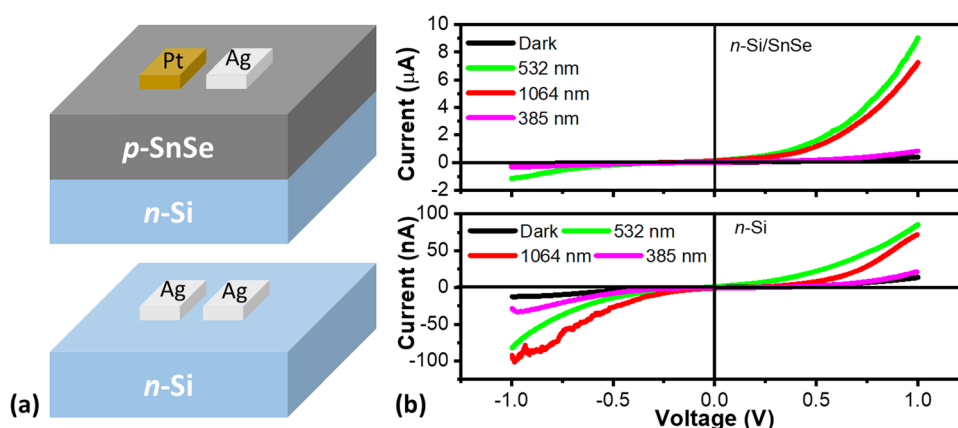


Fig. 2 (a) Schematic diagram of the devices and (b) device current–voltage (I – V) characteristics.



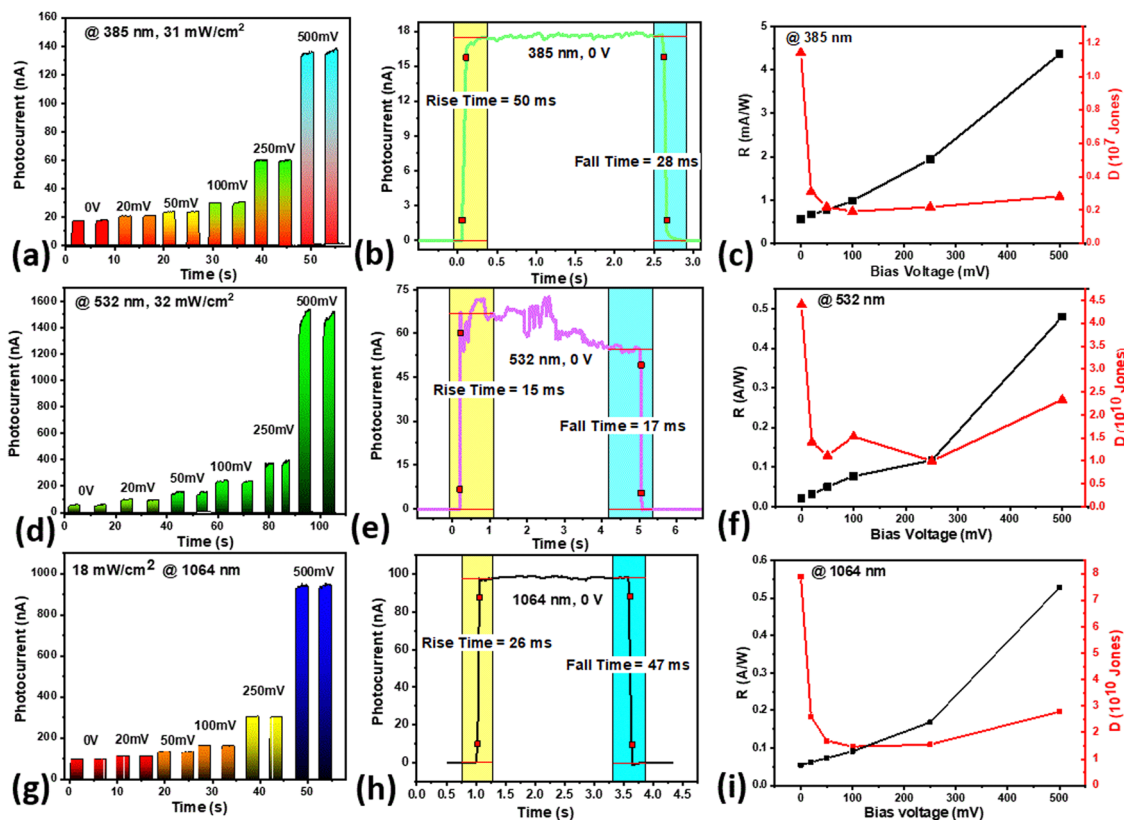


Fig. 3 Cycles of photocurrent at different bias voltages, rise and decay times, and R and D at 0 V applied bias under different laser illuminations (385, 532, and 1064 nm) for SnSe/n-Si thin films.

currents under zero bias (0 V) conditions.⁴⁷ Many pieces of literature use this strategy due to its easy fabrication techniques.^{48–52} The photocurrent under 385 nm is less than that under 1064 nm and highest under 532 nm laser light illumination. The device showed a fast rise/decay (on/off) time of 50/28, 15/17, and 26/47 ms under 385, 532, and 1064 nm laser illumination at 0 V applied bias (Fig. 3). This swift response of the SnSe device has been linked to generating a high density of photons, potentially resulting in faster response times. Furthermore, the higher applied bias voltage attributed to increased drift velocity ($T_{tr} = l^2/[\mu \times V_b]$, where l is the length of the device, μ is the carrier mobility, and V_b is the applied bias voltage) and decreased carrier transit time of charge carriers results to swift rise/decay response times.⁴⁶ The continuous cycling stability characteristics of the Sn-Se device under 385, 532, and 1064 nm laser light illumination under self-bias mode (0 V) are shown in Fig. 4(a)–(c). The photocurrent under a 532 nm laser shows an unstable value after the incident light due to the laser's unstable nature. The R and D under 385, 532, and 1064 nm laser light illumination at 0 V are shown in Fig. 4(d). Dynamic response curves of the dark current and NEP are shown in Fig. S2(a)–(c) (ESI[†]).

The estimated value of R and D is at its maximum under 1064 nm laser illumination. The continuous cycles of photocurrent at different power densities under a 1064 nm laser are shown in Fig. S2(d) (ESI[†]) at 500 mV bias and fitted with the power law for different voltages (Fig. 5(a)). The values of the exponents are 1.18, 1.22, 1.22, 1.08, 1.03, and 0.99 for the 0, 20,

50, 100, 250 and 500 mV biases, respectively. The value of the exponent tells a brief story of the photocurrent. The exponent value of 1.0 tells about the efficient separation of the carriers.⁵³ A brief study of the power law and the value of the exponent's variation with bias is discussed in a previous report.⁵⁴ After generating the carrier, their transport towards the channel and collection at appropriate electrodes give information about the scattering, defect, and trapping of the carrier. This information can be extracted from the value of the exponent. Here, the value of the exponent remains nearly the same with the bias (i.e., from 1.18 to 1.21 for 0 to 50 mV) and then gradually decreases from 1.21 to 0.99 with increasing bias potential from 50 to 500 mV.

Nearly the same value of the exponent from 0 to 50 mV indicates carriers' increasing average drift velocity within the transit length and, hence, the domination of the trap centres in the transport mechanism. These trapping states may arise due to the formation of secondary phase SnSe₂. Also, the decreasing value of the exponent can be inferred like this. After increasing a specific voltage, there starts de-trapping of the carriers. The exponent value reaches the ideal value (here 0.99 at 500 mV), indicating the de-trapping of the carriers at 500 mV, and this process is triggered from 100 mV bias. Trapping should be from shallow states that fade away with increasing bias. Hence, the value of voltage-dependent power law indicates the domination of the trap/defect states in the device. It is noticeable that SnSe on the glass substrate showed non-dominant behavior of trap/

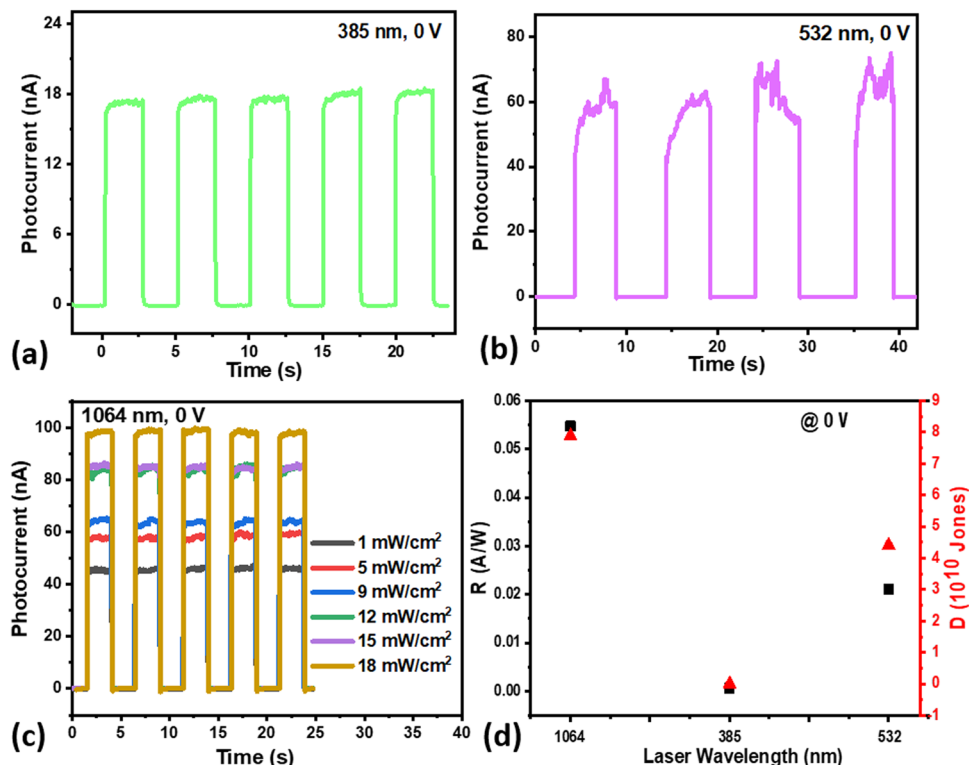


Fig. 4 Cyclic stability of the photoresponse of the device under (a) 385 nm, (b) 532 nm, (c) 1064 nm at different power densities, and (d) responsivity and detectivity of device at different lasers at zero bias.

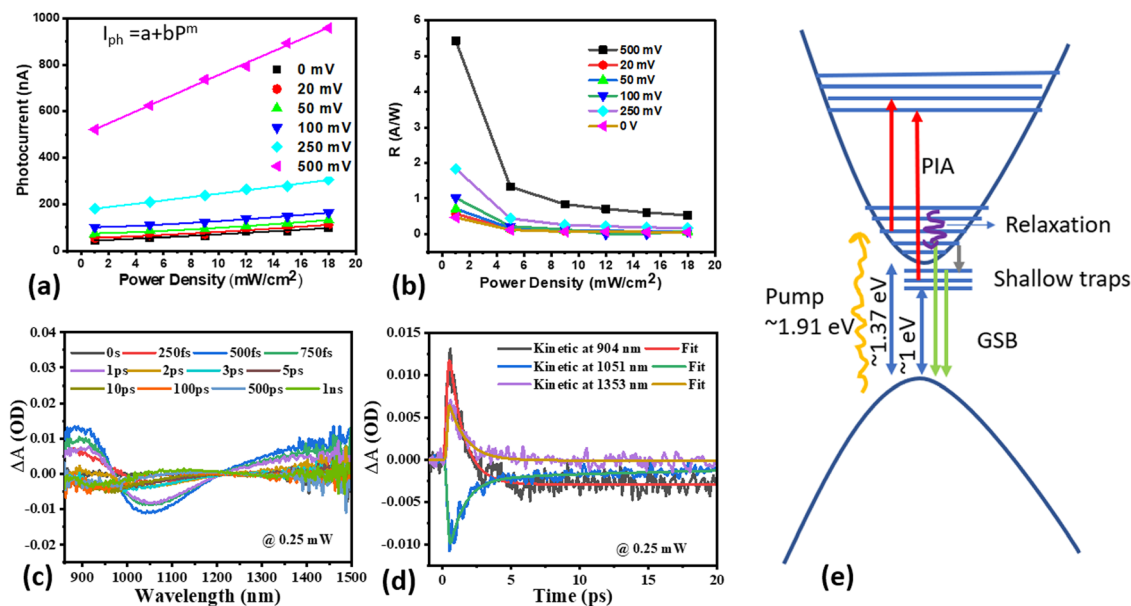


Fig. 5 (a) Power law fitting of the photocurrent at different voltages, and (b) R at different applied bias voltages under a 1064 nm laser, respectively. (c) Transient spectra and (d) kinetics at 0.25 mW fluence, and (e) schematic diagram of the possible trap states in the SnSe thin film.

defect states in the device. There was a regular pattern and increased exponent value in the voltage-dependent power law.³⁴

This difference may arise due to several reasons, such as substrate nature (stress or strain in the device by substrate), time of exposure of the device in the atmosphere, the thickness

of the film, etc.⁵⁵ Fig. 5(b) shows the relation between power density and R . Here, R decreases with increasing power density, which signifies the existence of trap or defect states responsible for this variation.⁵⁶ If trapping states exist in the device, these should be limited. Their trapping effect can be prominent at



lower power density due to less concentration of excited carriers. There are relatively more excited carriers at high power density whose trapping behavior is non-prominent. Hence, this trapping decreases the recombination of excited carriers. Therefore, the reduced behavior of the R with power density can be attributed to the trap or defect states in or at the device's interface.⁵⁶ These trap/defect states may arise due to the slight oxidation of the substrate surface under working conditions of SnSe.³⁹ To verify the existence of the shallow trapping states, ultrafast dynamics of the carriers were carried out. Transient spectra and kinetics at 0.25 mW fluence are shown in Fig. 5(c) and (d). There are three peaks: two positive and one negative. These positive and negative peaks are assigned to the photo-induced absorption and ground-state bleaching, respectively. The first positive peak corresponds to the absorption from either from conduction band or shallow trap states, and the peak beyond 1200 nm is due to absorption from the shallow trap states. Kinetics fitted at different wavelengths show the different relaxation mechanisms, recombination, and trapping. To confirm the existence of trap states, a pump fluence study was conducted at 0.50 and 1.0 mW. Transient spectra at a pump fluence of 0.25, 0.50, and 1.0 mW and their respective kinetic spectra are shown in Fig. S3 (ESI†). A detailed interpretation of the transient ultrafast data can be seen in the ESI.† A comparative transient spectrum at 500 fs probe delay is shown in Fig. S4 (ESI†). The kinetics at different wavelengths and their fitting at different pump fluences can be seen in Table S1 (ESI†).

In conclusion, the device has shallow traps, prominently due to the secondary phase (SnSe₂), confirmed by both XRD and Raman spectroscopy. Fig. 5(e) shows a schematic diagram that concludes the ultrafast study and shallow traps in SnSe thin film. Also, the existence of a shallow trap can be verified by the fact that with increasing bias voltage (from 0–50 mV), the decay time of the device is nearly the same (*i.e.*, ~47–42 ms) under 1064 nm, while a further increase in applied bias results in a significant decrease in decay time (from 42 to 23 ms), as shown in Fig. S5 (ESI†). Moreover, the absorption contribution of around 1.0 eV might be due to these shallow trap states (Fig. 1(c)). The recovery process of the photocurrent should also show the trapping of the defect history of the carriers.⁴⁷ Here, the decay nature of the photocurrent does not show the significant exponential tail that might be followed by shallow trapping of carriers. Furthermore, it is interesting to note the role of the n-Si substrate; the band alignment diagram is shown in Fig. S6(a) and (b) (ESI†) before and after the deposition.

An in-built electric field is formed due to the difference in the work function values of the material. The flow direction of the current is from p-SnSe to n-Si due to the higher value of the Fermi level energies. Thus, this spontaneous flow of electrons helps achieve the p-type nature of the SnSe; therefore, it enhances the photocurrent. Table 1 compares the device performance of SnSe-based PDs at 0 V bias.

Hao *et al.*³⁶ fabricated an atomically thin 2D material-based SnSe broadband PD on a Si substrate *via* the sputtering method and exhibited good R of 566.4 mA W^{−1} with ultrafast on/off time of 1.6/47.7 μs even at 0 V bias. Jung *et al.*⁴⁸ fabricated 2D GeSe material-based broadband photodetection and demonstrated the effect of asymmetric contact electrodes to achieve self-powered characteristics. Li *et al.*⁵⁷ developed SnSe/InSe van der Waals (vdW) heterojunction PDs to detect a broad spectral range with self-powered characteristics. Singh *et al.* reported a flexible SnSe–Sb₂Se₃ heterostructure PD device on a polyethylene terephthalate (PET) substrate using thermal evaporation, and the fabricated device exhibited a fast on/off time of 160/134 ms at 0 V applied bias with good stability even after 1500 cycles of bending; there was no remarkable change in device response.⁵⁸ Furthermore, Gao *et al.*⁵⁹ investigated the impact of an asymmetric contact structure with large work function differences on the performance of MoS₂-based PDs. They observed that the asymmetric metal contact structure, characterized by a significant work function difference, can detect light with wavelengths below 700 nm even when no external voltage (0 V) is applied.

4. Conclusion

A self-powered SnSe PD was designed using the thermal evaporation technique using asymmetrical metal contacts. The fabricated device showed a high R of 54.7 mA W^{−1}, D of 7.87×10^{10} , and fast response (on/off) times of 26/47 ms at 0 V bias conditions. The bias-dependent power law fitting indicates minor-dominated trap/defect states in the PD device and smooth charge transport. The same is supported by the ultrafast carrier dynamics and other inferences (like R vs. power density and variation of decay current time with voltage). The findings of this study could offer valuable insights for comprehending and advancing SnSe-based PDs. Moreover, the results strongly indicate that PDs based on SnSe fabricated *via* thermal evaporation hold significant promise toward developing cost-effective, next-generation, self-powered PDs.

Table 1 Comparison of the device performance of SnSe-based PDs at 0 V applied bias

Device architecture	Substrate	Laser source (nm)	P (mW cm ^{−2})	τ_r (ms)	τ_d (ms)	D (Jones)	R (mA W ^{−1})	Ref.
In/Si/SnSe/Pd vertical structure	Si	850	160	1.6×10^{-3}	47.7×10^{-3}	3.4×10^{12}	442.8	36
Cr/Au/GeSe/Cr/Pd	Si/SiO ₂	850	30	69	92	$\sim 9.5 \times 10^{10}$	~ 430	48
SnSe/InSe	Si/SiO ₂	808	100	1256	336	$\sim 2.1 \times 10^{11}$	~ 132	57
SnSe/Sb ₂ Se ₃ /Ag heterostructure	PET	1064	18	160	134	0.45×10^7	0.24×10^{-3}	58
Mo ₂ C/MoS ₂ /Au	SiO ₂	600	1.78	23 000	28 000	—	0.1	59
NbSe ₂ /MoSe ₂	Si/SiO ₂	650	0.19	0.18	0.8	2.39×10^{14}	64 920	60
PdTe ₂ /WSe ₂	Si/SiO ₂	780	0.0005	0.219	0.45	9.79×10^{13}	24 700	61
Pt/SnSe/Ag	Si	1064	18	26	47	7.87×10^{10}	54.7	This work



Conflicts of interest

The authors declare no conflict of interest.

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