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3, 3469

Wafer-scale pulsed laser deposition of ITO for solar cells: reduced damage vs. interfacial resistance†

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Transparent conducting oxides (TCOs) used in solar cells must be optimized to achieve minimum parasitic absorption losses while providing sufficient lateral conductivity. Low contact resistance with the adjacent device layers and low damage to the substrate during deposition of the TCO are also important requirements to ensure high solar cell efficiencies. Pulsed laser deposition (PLD) has been proposed as an alternative low-damage TCO deposition technique on top of sensitive layers and interfaces in organic and perovskite solar cells but is yet to be studied for the more mature silicon technology. Focusing on the PLD deposition pressure as the key parameter to reduce damage, we developed tin-doped indium oxide (ITO) with a sheet resistance of $60 \Omega \square^{-1}$ at different pressures and implemented it in silicon heterojunction (SHJ) solar cells. Buffer-free semi-transparent perovskite cells with the same PLD ITO electrodes were also fabricated for comparison. While in the perovskite cells increased ITO deposition pressure leads to an improved open circuit voltage and fill factor indicative of damage reduction, SHJ cells with PLD ITO at all conditions maintained a high passivation quality, but increased pressures lead to high series resistance. Transmission electron microscopy and time-of-flight secondary ion mass spectrometry confirmed the formation of a parasitic SiO_x at the ITO/a-Si:H interface of the SHJ cell causing a transport barrier. The optimized ITO films with the highest carrier density were able to obtain >21% SHJ efficiency with 75 nm-thick PLD ITO. Moreover, reducing the ITO thickness to ~45 nm and using TiO_x for optical compensation enables fabrication of SHJ devices with reduced indium consumption and efficiencies of >22%.

Received 22nd December 2021,
Accepted 28th February 2022

DOI: 10.1039/d1ma01225h

rsc.li/materials-advances

1. Introduction

Passivating contacts for a concerted recombination suppression and charge selectivity are considered to be the promising next step for the mature silicon photovoltaics.¹ Novel cell architectures using this concept, such as the silicon heterojunction (SHJ), rely on the transparent electrodes to enhance the carrier extraction from the passivating contacts. Sputtered transparent conducting oxides (TCOs) are the most widely implemented transparent

electrodes in such solar cells. Careful optimization of transparent electrode deposition for SHJ cells is crucial as the TCO/doped silicon contact is often referred to as the major source of resistive losses,^{2,3} whereas the sputtering process itself is prone to damage the c-Si/a-Si:H interface causing passivation losses and deteriorated device performance.^{4,5} In particular, sputtering may result in a trade-off between the desired optoelectronic properties of TCO and damage-free deposition due to the underlying physics of the magnetron sputter deposition.⁶ For instance, utilizing a higher deposition pressure during sputtering promotes thermalization of the arriving particles and reduces plasma-induced damage.⁷ However, optoelectronic properties of sputtered TCOs are known to deteriorate once fabricated at elevated pressures.^{7,8} Although there are successful strategies to mitigate the negative effects of TCO fabrication (mainly, thicker protective thin films and annealing/curing steps), the development of alternative soft-landing deposition methods is highly desirable. In this regard, pulsed laser deposition (PLD) has been demonstrated as a promising approach enabling low-damage deposition on top of

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† Electronic supplementary information (ESI) available. See DOI: 10.1039/d1ma01225h

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sensitive layers in organic^{9,10} and perovskite¹¹ solar cells and is yet to be studied for more mature silicon technology. A wider choice of working pressures for PLD, as compared to sputtering, enables more efficient thermalization of energetic particles¹² which is advantageous for damage mitigation. A more elaborated discussion of the origins and strategies to alleviate the plasma-induced effects was recently reviewed by Aydin *et al.*⁶

In this work Sn-doped In₂O₃ (ITO) films grown using wafer-scale PLD at different deposition pressures but with the same sheet resistance (R_{sh}) are implemented as both the front electrode and rear electrode in SHJ solar cells. To compare the effects of deposition pressure on other sensitive contact layers, buffer-free semi-transparent perovskite cells (ST-PSC) were fabricated with the same rear PLD ITO electrodes. The results suggest that an increased ITO deposition pressure leads to lower damage at the ITO/organic (BCP/C₆₀) interface in the perovskite cells. For the SHJ cells, no correlation was found between deposition pressure and passivation damage, but high pressures resulted in a high contact resistance. At optimized PLD ITO conditions, combining high pressure during deposition and high free carrier density (N_e) on the film level, leads to efficient proof-of-concept SHJ solar cells. These results motivate further studies of TCO low damage deposition techniques towards achieving low contact resistance when implemented in devices in addition to the optimum trade-off between transparency and conductivity in TCOs.

2. Results and discussion

2.1 Structural and optoelectronic properties of PLD-grown ITO at different pressure conditions

100 nm-thick ITO films (on planar substrates) with a targeted R_{sh} of 60 $\Omega \square^{-1}$ were developed using PLD at three different conditions at room temperature. Two main pressure conditions were studied: 0.005 mbar (100% O₂ background gas) denoted as the low-pressure condition (LP) and 0.02 mbar (50%/50% Ar/O₂) denoted as the high pressure condition (HP). A third condition consisted of a very thin (~ 2 nm as estimated from deposition rate of a thicker film) buffer layer deposited at 0.1 mbar (100% O₂) followed by the LP condition (LP with buffer). In all cases ITO was co-deposited onto glass substrates and a-Si:H(n/i)/c-Si/a-Si:H(i/p) stacks (planar and textured). We expect the thickness on the textured solar cell precursor to be thinner and about 67 nm.

Fig. 1 displays grazing incidence X-ray diffraction (XRD) patterns of the ITO films deposited on glass and planar Si wafers. All the as-deposited ITO films are amorphous regardless of the deposition pressure and substrate (Fig S1, ESI†). However, a 200 °C annealing step in air for 20 min (similar to the Ag metallization step of the SHJ cells) results in films with different structural properties. While on glass the HP ITO films remain amorphous, both LP and LP with buffer ITO films show (211), (222), (400) and (440) peaks corresponding to the In₂O₃ bixbyite reference pattern.¹³

Of note, the LP with buffer ITO films show enhanced crystallinity, as suggested by the higher intensity peaks as compared

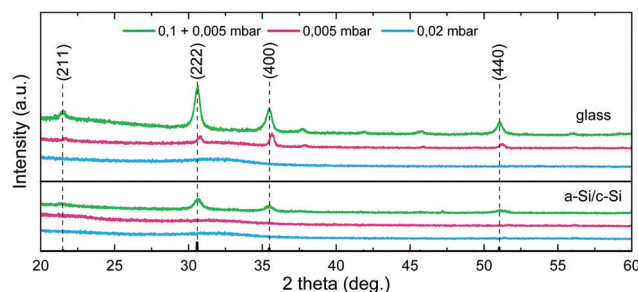


Fig. 1 Grazing incidence XRD spectra of ITO deposited following the LP (0.005 mbar); LP with buffer (0.1 + 0.005 mbar) and high pressure (0.02 mbar) conditions on glass (top) a-Si:H-coated c-Si substrates (bottom), all annealed at 200 °C in air.

to the LP films. Following previous reports on ITO crystallization, one can speculate that the LP ITO film is only partially crystallized.¹⁴ Interestingly, ITO films deposited on the a-Si:H(n/i)/c-Si/a-Si:H(i/p) stacks remain mainly amorphous, with only the LP with buffer showing In₂O₃ bixbyite reflections. These results suggest that the thin ITO buffer deposited at 0.1 mbar promotes the crystallization of the ITO, following observation of previous reports,¹⁵ which moreover affects the optoelectronic properties of the films as will be described below.

Hall effect measurements of the same ITO films were performed to evaluate the effect of pressure, substrate (glass *vs.* planar a-Si:H(p/i)/c-Si stacks) and the resulting structural properties on the free carrier density (N_e) and electron mobility (μ_e). Fig. 2(a) displays the R_{sh} of the films, all showing $\sim 60 \Omega \square^{-1}$ in the as-deposited state, which was the target value for the ITO development. Following that $R_{sh} = 1/(e\mu_e N_e d)$, with e being the fundamental charge and d the thickness of the ITO fixed at 100 nm, we can see that, while the LP and LP with buffer ITO show N_e above $3 \times 10^{20} \text{ cm}^{-3}$ in the as-deposited state, the HP ITO show N_e below $2.5 \times 10^{20} \text{ cm}^{-3}$. Conversely, μ_e is higher for the HP ITO reaching values $> 40 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, while μ_e for the LP and LP with buffer ITO films are all below $40 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$. The difference in N_e is likely caused by different oxidation conditions during PLD growth and is possibly defining the observed difference in μ_e for ITO films. In the case of HP ITO, μ_e is likely to be limited by dislocation scattering ($N_e < 3 \times 10^{20} \text{ cm}^{-3}$),¹⁶ whereas LP and LP with buffer ITO ($N_e > 3 \times 10^{20} \text{ cm}^{-3}$) are most probably primarily limited by ionized impurities scattering.¹⁷ Moreover, electrons tunnel through thin potential barriers formed at grain boundaries¹⁸ for films at higher N_e leading to a smaller contribution from dislocation scattering explaining the observed difference in the μ_e of ITO films.

After the 200 °C annealing step, μ_e and N_e of all PLD ITO deposited onto the a-Si:H(p/i)/c-Si stacks ITO remain virtually unchanged. Interestingly, similar trends are observed for the LP and LP with buffer ITO on glass, whereas the HP (0.02 mbar) ITO show variations in the properties after annealing. R_{sh} increases from 60 to 95 $\Omega \square^{-1}$ after the heat treatment on glass due to the drop of N_e (from 2.4 to $1.2 \times 10^{20} \text{ cm}^{-3}$). It is important to mention that the reported values represent an average of at least 2 glass samples which have been co-deposited with a SHJ



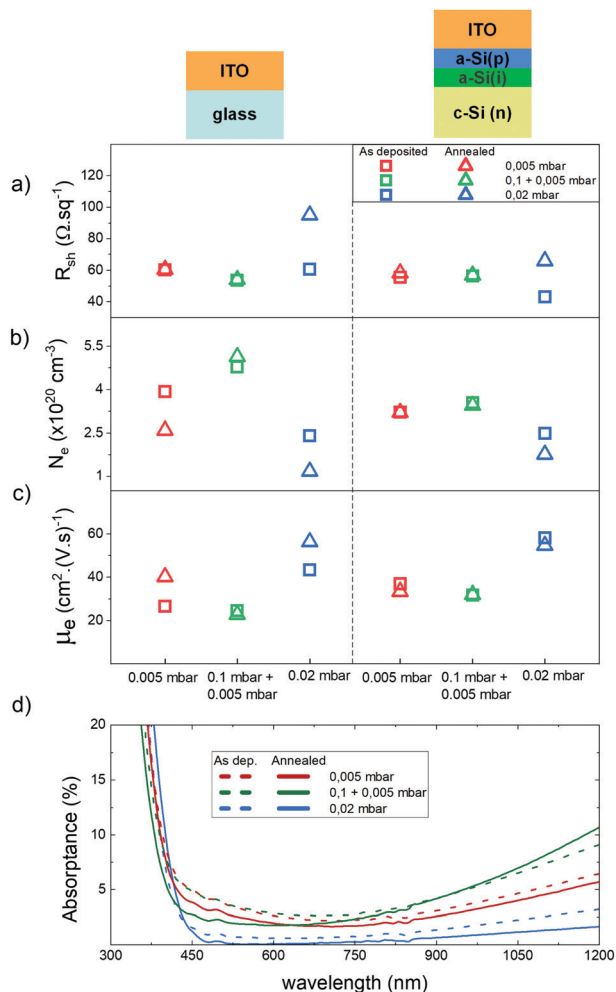


Fig. 2 (a) Sheet resistance (R_{sh}), (b) free carrier density (N_e) and (c) electron mobility (μ_e) of PLD ITO films deposited at LP (0.005 mbar); LP with buffer (0.1 + 0.005 mbar) and high pressure (0.02 mbar) on glass (left) and a-Si:H/c-Si stacks (right). (d) Absorbance as a function of wavelength of the ITO films.

cell stack. The electrical properties of the TCO layers have been previously shown to be dependent on the underlying substrate and to be sensitive towards hydrogen effusion from the hydrogenated a-Si:H layers beneath,^{19,20} however, this was not observed in our Hall effect measurements on the a-Si:H(p/i)/c-Si(n) stacks. Although this stack seems to be more representative of the device performance, we underline that the developed ITO electrodes were implemented in SHJ cells with a rear emitter design. Therefore, the front current collection would occur from a-Si:H(n/i)/c-Si(n) stack which cannot be reliably measured *via* the Hall effect due to substantial conduction through the Si substrate in this case.

Optical properties, as shown by the absorbance in Fig. 2(d) and transmittance in Fig. S2 (ESI[†]), followed the expected trend: a blue shift in the UV due to the Burstein–Moss effect^{21,22} and increased free carrier absorption (FCA) in the NIR with increased N_e . The lowest FCA and the narrowest optical band gap are observed for the HP film (lowest N_e , highest μ_e), whereas the highest FCA and widest optical band

gap are measured for the LP with the buffer film (highest N_e , lowest μ_e).

Interestingly, the use of a high-pressure ITO buffer layer promotes ITO crystallization resulting in films with high N_e . This is opposite to an expected increase in μ_e with enhanced crystallization as demonstrated in other works.^{15,23} The high N_e instead of high μ_e of our ITO films with the highest crystallinity indicates that there is a more complex interplay between oxygen vacancies²⁴, grain boundaries¹⁶ and ionized impurity scattering,¹⁷ in our films. Finally, the optoelectronic properties of the PLD grown ITO films at the three pressure conditions meet the requirements of a front electrode of the SHJ solar cells and/or a rear electrode in ST-PSC, *i.e.* $R_{sh} < 100 \Omega \square^{-1}$ and low FCA (absorbance below 10% in the spectral range from UV to NIR).^{25,26} In the following section, the performance of these PLD ITO films on devices will be compared to a sputtered ITO reference for SHJ cells ($R_{sh} \sim 100 \Omega \square^{-1}$ on textured substrate; $N_e \sim 1.5 \times 10^{20} \text{cm}^{-3}$, $\mu_e \sim 40 \text{cm}^2 \text{V}^{-1} \text{s}^{-1}$) or an opaque Ag electrode for the case of ST-PSC.

2.2 Effect of PLD ITO deposition pressure on buffer-free semi-transparent perovskite solar cells

Sputtered damage during the rear electrode TCO deposition in ST-PSC has been commonly mitigated by the use of an additional evaporated or atomic layer deposited metal oxide buffer layer.^{6,27} Recently, Zr-doped In_2O_3 (IZrO) fabricated by PLD at 0.02 mbar allowed damage-free deposition atop of the buffer-free solution-processed ST-PSC due to high (as compared to magnetron sputtering) deposition pressure.¹¹ To verify the effect of PLD pressure during transparent electrode deposition on the damage to the thin (<35 nm) organic electron transport layer, we fabricated semi-transparent vapor-deposited perovskite solar cells without a metal oxide buffer layer and applied the ITO electrodes at the three developed conditions. The cell stack (displayed in the inset of Fig. 3(b)) consisted of glass/ITO (160 nm)/ MoO_3 (6 nm)/

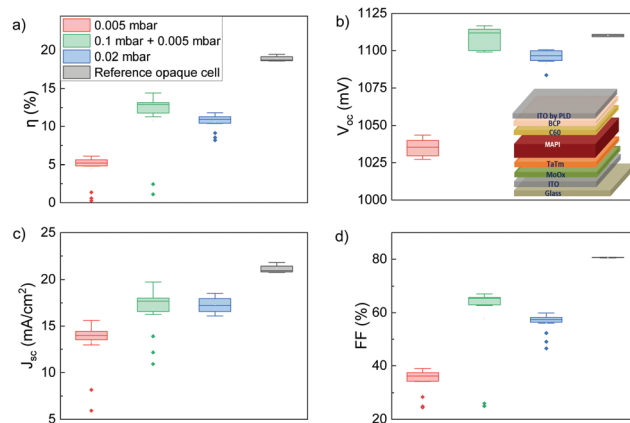
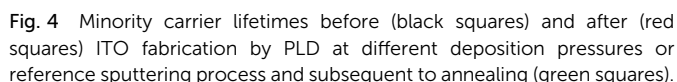


Fig. 3 Statistical distribution of the solar cell parameters for semi-transparent perovskite devices with the different PLD ITO deposition pressure conditions. (a) Power conversion efficiency, (b) open-circuit voltage (V_{oc}), (c) short-circuit density (J_{sc}), and (d) fill factor (FF). Deposition pressure for PLD ITO is indicated in the inset of the graphs. Reference opaque cells with the evaporated Ag rear electrode are shown in grey.

Fig. 3 displays the statistical distribution of the main solar cell parameters for devices with different rear ITO electrodes for the forward scan directions measured from the glass side (forward and reversed scan are presented in Fig. S3, ESI†). The results show clear improvements in open-circuit voltage (V_{oc}) and fill factor (FF) with the increase in the deposition pressure of ITO. Cells with LP ITO show a severe drop in V_{oc} and fill factor (FF) as compared to the opaque reference cell with evaporated Ag back contact. Of note, the thin layer deposited at 0.1 mbar for the case of LP with buffer ITO leads to significant improvements in power conversion efficiency (due to enhanced FF and V_{oc}) as compared to cells with LP ITO. Devices with HP and LP with buffer ITO show comparable short-circuit density (J_{sc}), yet the FF and V_{oc} are higher for the latter, thus leading to gains in PCE. Nevertheless, cells with HP ITO show only a small drop of V_{oc} and FF with respect to the best performing ST-PSC indicative of a good interface formation for the two conditions. The HP ITO results are in agreement with previous reports for solution-processed ST-PSC with PLD IZrO.¹¹ For the cells with LP ITO, an increased barrier height at the ETL/TCO interface may be causing the severe drop of FF and V_{oc} as suggested by Kanda *et al.*²⁸ Another possible explanation of the deteriorated cell performance is the penetration of the energetic ablated particles in the organic layer²⁹ due to the suppressed thermalization during PLD at low pressures. The PCE of the reference opaque cell is >19% for the champion cell highlighting the high quality of the halide absorber and relevant choice of the transport layers. Overall, the higher J_{sc} of the reference cell is related to Ag acting as a back reflector in the cells. Following the confirmation of the effect of increasing the ITO deposition pressure on the damage mitigation of the thin organic transport layers in ST-PSCs, we proceeded to study the effect on SHJ cells.

In SHJ solar cells, quantitative insights of the induced degradation of the a-Si:H/c-Si interface passivation during TCO deposition are obtained *via* effective minority carrier lifetime (τ_{eff}) measurements. We consistently measured τ_{eff} within the passivated c-Si wafer at a fixed minority charge carrier density of 10^{15} cm^{-3} on the SHJ cell precursors (shown in the inset of Fig. 4 before ITO deposition (PLD or sputtering)), afterwards and subsequent to the annealing step at 200°C . Please note that the front-side a-Si:H(n)/a-Si:H(i) stack was slightly thinner (9/8 nm, respectively) as compared to the one in the finished devices (14/10 nm) as described in Section 3.

As seen from Fig. 4, a similar drop of τ_{eff} from 2 ms to around 1.5 ms is observed for all PLD conditions. Interestingly, the sputtered reference is already deposited at 200 °C which is beneficial for *in situ* curing of the sputter-induced damage. Nevertheless, we observe the drop of τ_{eff} for the case of sputtered ITO as well. As expected, the annealing step at 200 °C recovers the passivation for cell precursors with all transparent electrodes



due to the possibility for the a-Si:H network to recover at such elevated temperatures.^{4,5,30,31} The τ_{eff} matches or even slightly exceeds the initial values of a-Si:H coated c-Si wafers (> 2 ms) for the case of the HP PLD ITO, whereas the passivation is only slightly improved (1.4 ms *vs.* 1.2 ms) for the case of sputtered ITO.

Following the lifetime evaluation, the ITO films have been implemented as the front and rear electrodes in SHJ solar cells. The solar cell parameters for the devices with PLD ITO, as well as the reference sputtered ITO, are summarized in Fig. 5. As explained in the experimental section and observed in Fig. 5(f), the wafer contains 7 cells with different Ag grid values (1 and 1.7 mm, respectively). Unless specified separately, we report the solar cell parameters for the devices with 1.7 mm pitch due to the optimized performance.

Remarkably, the cells with LP with buffer ITO demonstrate the highest efficiency among the ITO PLD devices (average across the 10 cells) of 21%, but lag behind the established reference cells with sputtered ITO by $\sim 1.2\%$ as displayed in Fig. 5(a). Devices with LP and HP ITO reach average efficiencies of 20% and 16.9%, respectively. As seen from Fig. 5(b and c), the cells with various ITO electrodes display similar V_{oc} values. Slight differences in J_{sc} follow the trend observed in Section 2.1 being inversely proportional to the ITO absorbance.

The differences in the overall efficiency originate from the variations in FF for the case of PLD ITO. These changes are mainly dominated by the trend observed in the series resistance (R_s) of the different PLD films. As shown in Fig. 5(e), cells with HP ITO display the most significant increase of R_s ($4.9 \, \Omega \, \text{cm}^{-2}$), while R_s for the cells with LP with buffer ITO is just a bit higher ($1.1 \, \Omega \, \text{cm}^{-2}$) than for cells with sputtered ITO ($0.86 \, \Omega \, \text{cm}^{-2}$). Interestingly, devices with LP ITO display R_s of $1.9 \, \Omega \, \text{cm}^{-2}$ which is notably higher than the cells with LP with buffer ITO. This confirms the importance of the thin buffer layer deposited at 0.1 mbar defining the structural and optoelectrical properties of the ITO film. These films have the highest N_e among the PLD ITO films and additionally display the highest crystallinity which

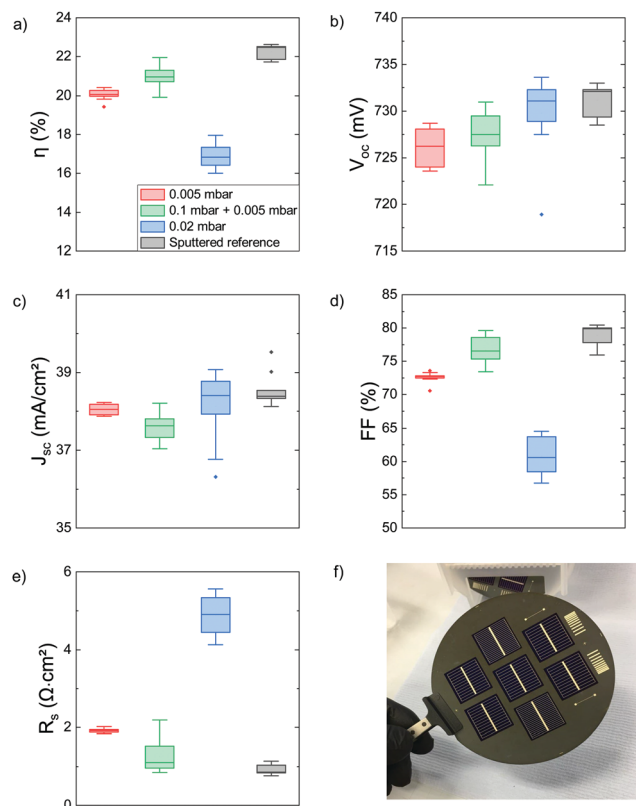


Fig. 5 Statistical distribution of silicon heterojunction solar cell parameters measured for the devices with the different PLD ITO deposition conditions. (a) Power conversion efficiency, (b) open-circuit voltage (V_{oc}), (c) short-circuit density (J_{sc}), (d) fill factor (FF), and (e) series resistance (R_s). Deposition pressure for PLD ITO indicated in the inset of the graphs. Reference cells with sputtered ITO is shown in grey. (f) Image of a 4'' silicon wafer with 7 finished silicon heterojunction cells (4 cm²) with PLD ITO.

may influence the ITO work function at the interface.^{32,33} Conversely, the highest R_s is observed for cells with the PLD ITO with the lowest N_c . It has been previously shown^{34,35} that the combination of N_c in TCO films and the activation energy of the a-Si:H films plays a significant role in increasing the contact resistivity (ρ_c) of the TCO/a-Si:H stack. However, we believe that the difference of $\sim 2 \Omega \text{ cm}^{-2}$ in ρ_c cannot be accounted for by the rather insignificant difference in N_c ($1.5 \times 10^{20} \text{ cm}^{-3}$ vs. $3.0 \times 10^{20} \text{ cm}^{-3}$ as measured on planar a-Si:H-coated Si wafers). For these N_c , the expected difference in ρ_c is in the orders of few tens of $\text{m}\Omega \text{ cm}^{-2}$ as calculated by Luderer *et al.*³⁶ Therefore, a more plausible contribution to the high R_s could be the potential formation of an amorphous silicon oxide layer at the a-Si:H/ITO interface during PLD which is performed at elevated oxygen partial pressures compared to sputtering. This parasitic oxide may hinder the carrier transport and lead to the increased contact resistance³ which is investigated in the next section.

2.5 Parasitic oxide at the a-Si:H/TCO interface as the source of high series resistance

As mentioned above, the inferior performance of the cells with PLD ITO, as compared to the reference cells with sputtered ITO,

is due to the loss of FF because of the compromised R_s . Since the device stacks (c-Si wafer, intrinsic and doped a-Si:H layers and Ag grid) were identical throughout the fabrication process, and considering the similar R_{sh} of the PLD and sputtered ITO ($60 \Omega \square^{-1}$ measured on the a-Si:H/c-Si, Fig. 2(a)), the resistive loss is likely to originate from the ρ_c at the doped a-Si:H/TCO interface. In particular, the ρ_c of a-Si(p)/ITO has been previously shown to have the highest contribution to R_s .^{2,3} Moreover, the ρ_c of doped a-Si:H/ITO is known to be very sensitive to the oxygen partial pressure during the ITO sputtering process. The process window in sputtering has been shown to be quite narrow, since the working atmosphere during sputtering primarily consists of Ar, whereas minor changes in O₂ content (by a few %) lead to the enhancement of ρ_c ³⁶ and severe deterioration of FF.³ This has been explained by the high work function of ITO sputtered at elevated oxygen partial pressures ($p(\text{O}_2)$) that increases the barrier height of the a-Si:H/TCO interface.³⁷ The PLD in this study was operated at a much higher oxygen supply and often pure oxygen in the background environment to enable optimized optoelectrical properties. Even for the LP ITO, the $p(\text{O}_2) = 0.005 \text{ mbar}$, whereas for HP ITO the $p(\text{O}_2) = 0.01 \text{ mbar}$, both significantly higher than the $p(\text{O}_2)$ for sputter processes.^{7,38} This may lead to the oxidation of a-Si:H layers (intuitively, more likely for HP ITO) suppressing the carrier extraction at the doped a-Si:H/TCO interface. Additionally, a thin native oxide layer can remain in the case of the delayed ITO deposition after the PECVD of the a-Si:H layers. The presence of native oxide typically does not increase the resistive losses as the (rather) high kinetic energy of the sputtered species is sufficient to break the thin SiO_x layer. However, that may not be the case of softer PLD due to the different plasma kinetics and requires further studies. In order to decouple the possible causes of increased R_s , a set of solar cells with an HF dip right before the HP ITO PLD deposition was prepared to eliminate the presence of the native oxide. As observed in Fig. 6, no drastic improvement in R_s is present on the device level which suggests that the parasitic oxide is

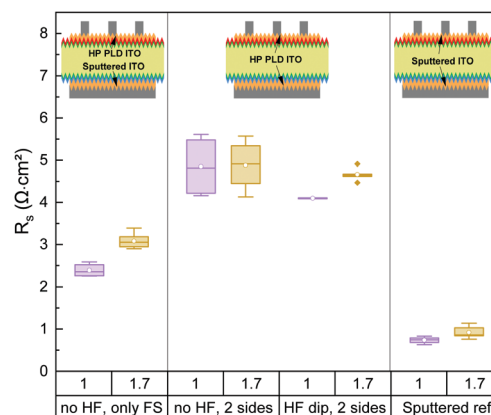


Fig. 6 Comparison of total series resistance (R_s) for a set of SHJ cells with PLD ITO at the front and PLD or sputtered rear electrode and Ag grid pitch of 1 and 1.7 mm, respectively. R_s of the cell with double-sided sputtered ITO is given for reference.

Chemical mapping of the ITO/a-Si:H(i/n) interface was furthermore performed *via* time-of-flight secondary ion mass spectrometry (ToF-SIMS) and high-resolution energy dispersive X-ray spectroscopy (EDX) under a scanning transmission electron microscope (STEM). Fig. 7(a and b) show the ToF-SIMS depth profile resulting from ablation using a Cs^+ source from the HP ITO towards the c-Si both before and after annealing. It should be noted that the annealed states resemble the situation in the solar cell for which JV measurements have been performed after annealing at 200 °C. One measurement frame was performed after every ablation while tracking In_2O_3^- , Si^- , H^- (Fig. 7(a)) and $\text{SiHO}_2^-/\text{SiO}_3\text{In}^-$ (Fig. 7(b)) signals, respectively, with a quadrupole mass spectrometer. SIMS measurement data for LP ITO and HP ITO are presented in Fig. S4 (ESI[†]). Si^- and In_2O_3^- signals do not change upon annealing for any samples and therefore allow identification of the ITO/a-Si:H interface. H^- is observed for all samples before and after annealing suggesting the presence of H^- in ITO already in the as-deposited state. In contrast to what has been shown by Cruz *et al.*²⁰ or Ritzau *et al.*,³⁹ we do not observe additional H^- diffusion into the ITO layer from the a-Si:H. This agrees with the Hall effect data, showing that N_e does not change upon annealing of the ITO/a-Si:H planar stacks. SIMS measurements also allowed detection of SiO_3In^- and SiHO_2^- signals which are indicative of oxygen presence. Fig. 7(b) and Fig. S4 (ESI[†]) show that the SiO_3In^- and SiHO_2^- signals are only present in the vicinity of the TCO/a-Si:H interface, both before and after annealing. The high intensity of the SiHO_2^- signal at the TCO/a-Si:H interface indicates the oxidation of the a-Si:H surface during ITO deposition. In order to compare the SiO_x layer for the three PLD conditions, before and after annealing, SiO_3In^- and SiHO_2^- signals were extracted and plotted independently for each sample in Fig. 7(c and d). The full width at half maximum

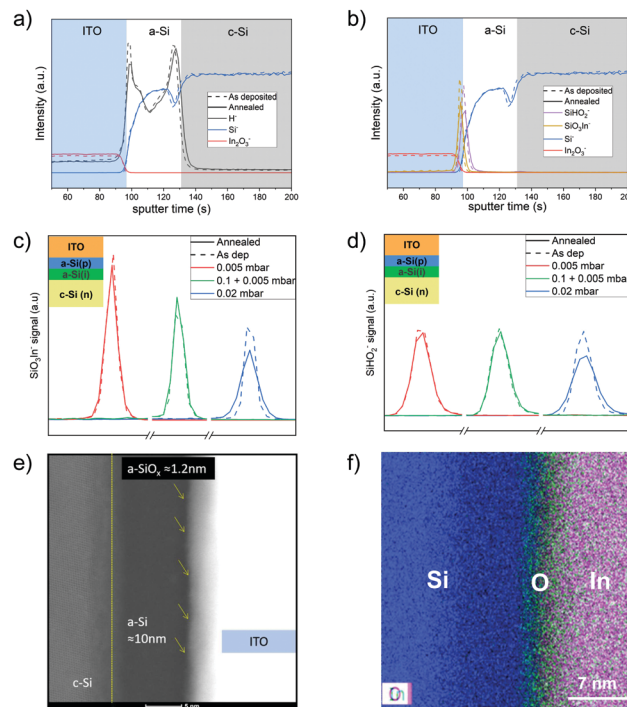


Fig. 7 Cross-sectional ToF-SIMS analysis of HP ITO on the SHJ solar cell stack before and after annealing. (a) H^- signal; (b) SiHO_2^- and SiO_3In^- signals. Signals plotted next to Si^- , In_2O_3^- signals to define the ITO/a-Si:H interface. Integrated ToF-SIMS intensity profiles for (c) SiHO_2^- and (d) SiO_3In^- signals indicative of oxygen presence. HRSTEM-HAADF analysis of the finished SHJ solar cells with HP ITO after metallization: (e) image of the a-Si:H/ITO interface; (f) energy dispersive X-ray chemical mapping of a chosen area within the sample where blue, green and pink represents silicon, oxygen and indium, respectively.

For both the SiHO_2^- and SiO_3In^- signals, the only significant change in FWHM is observed for the stack with HP ITO, showing broadening of the peak after annealing. This suggests possible oxygen migration at the ITO/a-Si:H interface, increasing the SiO_x barrier width after annealing. We observe this only for the HP ITO sample, which is the amorphous film before and after annealing. The density of the film or even its possible higher oxygen content (due to higher $p(\text{O}_2)$ during the deposition process) might be the cause of this effect. The observation also agrees with the higher R_s for the SHJ cells with the HP as ITO reported in Section 2.1. The ToF-SIMS measurements, the same as the Hall effect measurements, were all performed in the ITO/a-Si:H(n/i)/c-Si planar stacks. To verify the presence of the SiO_x also on the cells analyzed in Fig. 5 (textured wafers instead of planar), scanning transmission electron microscope high-angle annular dark field (STEM-HAADF) EDX was performed on FIB prepared lamellas taken directly from the cells. The contrast in STEM imaging of the amorphous layers next to the HP ITO films in Fig. 7(e) suggests the presence of ~ 1.2 nm a- SiO_x next to the

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determined using a Hall Effect measurement setup in the van der Pauw configuration, using $1 \times 1 \text{ cm}^2$ pre-diced samples. The structural properties of the films were investigated using an Xpert Pro diffractometer (Panalytical) in grazing incidence (GI-XRD) configuration where the incident angle is $\omega = 0.6^\circ$. The ITO thickness was determined by measuring the step height on glass samples with a Dektak stylus profilometer. Time-of-flight secondary ion mass spectrometry (ToF-SIMS) was performed by Tescan analytics. Ablation was done using a Cs^+ source on a $200 \mu\text{m}^2$ area. The process was performed such that one measurement frame was performed after every ablation frame while tracking $\text{H}^-/\text{In}_2\text{O}_3^-/\text{Si}^-/\text{SiHO}_2^-$ and SiO_3In^- signals with a quadrupole mass spectrometer.

Chemical analyses were performed in the scanning transmission electron microscope high-angle annular dark field (STEM-HAADF) imaging mode of a 200 kV Titan-Themis TEM/STEM electron microscope equipped with a Cs probe corrector and a ChemiSTEM Super-X detector. Prior to the analysis, the FIB technique was used for the preparation of cross-section lamellas of the considered sample. STEM-HAADF EDX chemical mapping was performed by considering the following elements of interest: the silicon K_{α} -1.73 keV ionization present in the substrate, the a-SiO_x layer and the a-Si:H layer, the oxygen K_{α} -0.523 keV ionization edge coming also from the thermal a-SiO_x and the indium L-3.28 keV ionization edge from the ITO layer.

Solar cells and lifetime test structures were fabricated on random pyramid textured $1 \Omega \text{ cm}$, and $180 \mu\text{m}$ thick n-type FZ silicon wafers at Fraunhofer ISE. The monofacial solar cells were fabricated in the rear-emitter design and metallized at the front and rear side *via* screen printing of an Ag paste cured at 200°C in a belt furnace. The a-Si:H layers are deposited by plasma-enhanced chemical vapor deposition (PECVD) using mixtures of SiH_4 , H_2 , TMB, and PH_3 . These precursors were later shipped to the University of Twente for PLD ITO. The reference ITO samples were deposited inline *via* DC magnetron sputtering from a rotary 97/3 wt% $\text{In}_2\text{O}_3/\text{SnO}_2$ target with a deposition substrate temperature of 190°C . The power of 4.4 kW was distributed along the 75 cm long target, and the deposition pressure was kept at 4×10^{-3} mbar in 5% O_2 in an Ar atmosphere.

Semi-transparent perovskite solar cells (ST-PSC) were fabricated by thermal evaporation in vacuum chambers at 10^{-6} mbar, which are integrated in a nitrogen-filled glovebox (H_2O and $\text{O}_2 < 0.1 \text{ ppm}$) at the University of Valencia. The vacuum chambers are equipped with temperature-controlled evaporation sources (Creaphys) fitted with ceramic crucibles, directed upward. Individual quartz crystal microbalance (QCM) sensors monitored the deposition rate of each evaporation source. As mentioned in Section 2.2, the evaporated perovskite solar cell stack consisted of glass/ITO (160 nm)/ MoO_3 (6 nm)/TaTm (10 nm)/ MAPbI_3 (500 nm)/ C_{60} (25 nm)/BCP (7 nm). The deposition of the perovskite and charge transport layers followed the same procedure described in our previous publications.^{43,44} The ST-PSC substrates were later shipped to the University of Twente for rear transparent ITO electrode fabrication by PLD.

For the ITO PLD deposition, the substrates were aligned to shadow masks to obtain the electrode layout indicated in Fig. S6 (ESI[†]) with sixteen rectangular pixels of 0.0825 cm^2 , eight on each side of the substrate, allowing for significant statistics. No silver grid lines surrounding the PLD-ITO contacts were used for semi-transparent cells in this study. An Ag grid for the reference opaque cells was fabricated by thermal evaporation at the University of Valencia.

4. Conclusions

We demonstrated ITO films at various deposition pressures with good optoelectrical properties by wafer-scale PLD at room temperature. PLD ITO fabricated at elevated pressures leads to the improvement in FF and V_{oc} for the semi-transparent perovskite solar cells suggesting the reduced damage at the transport layer/TCO interface. In contrast, no difference in passivation quality and, consequently, V_{oc} was observed for silicon heterojunction cells with different ITO electrodes, whereas increased PLD pressures lead to a high series resistance and deterioration in FF. The optimized crystalline ITO films enable sufficient transport across the a-Si:H/ITO interface despite the presence of a thin a-SiO_x as revealed by ToF-SIMS and STEM. We implemented ITO by PLD in silicon heterojunction cells to obtain high efficiencies of 21%. Finally, we demonstrated that thinning down the PLD ITO electrodes together with depositing a TiO_x capping layer is a feasible strategy to fabricate high-performing devices with efficiencies $> 22\%$ and a reduction in indium consumption. This study draws attention toward the common issue for 'soft landing' TCO deposition techniques utilizing a high process pressure with PLD serving as a case study. Our findings motivate further research activities in order to benefit from low-damage TCO deposition without compromising the contact resistance with adjacent transport layers in optoelectronic devices.

Author contributions

Y. S. and P.-A. R. share equal contributions for conceiving the idea and ITO characterization. P.-A. R. conducted ITO optimization by PLD, optoelectrical characterization and processed devices. Y. S. performed structural characterization of ITO, data visualization and solar cell analysis. L. T. fabricated and characterized silicon heterojunction solar cells and conducted lifetime measurements, I. F. performed HRSTEM-HAADF and EDX measurements and analysis, K. Z. and A. P. fabricated and characterized the perovskite solar cells. Y. S., P.-A. R. and M. M.-M. drafted the manuscript. All co-authors discussed the paper and revised the manuscript. H. B., P. C., M. B. and M. M.-M. were involved in designing the experiments and supervised the work.

Conflicts of interest

There are no conflicts to declare.



Acknowledgements

This work was partly supported by the Solar-ERA.NET CUSTCO project by the Netherlands Enterprise Agency (RVO) under contract SOL18001, the French Research National Agency (ANR) under contract ANR-19-SOL2-0002-04, and the German Federal Ministry for Economic Affairs and Energy under Contract 03EE1032. FIB lamella sample preparation and STEM-HAADF EDX characterizations were performed in the frame of the French Government program of investment for the future (Programme d'Investissement d'Avenir—TEMPOS Equipex—ANR-10-EQPX-50, pole NanoTEM). H. J. B. acknowledges the financial support from Project PCI2019-111829-2 funded by MCIN/AEI/10.13039/501100011033 and by the European Union, Project CEX2019-000919-M funded by MCIN/AEI/10.13039/501100011033, and the Comunitat Valenciana (IDIFEDER/2018/061 and Prometeu/2020/077). H. J. B. also acknowledges financial support by the King Abdullah University of Science and Technology (KAUST) Office of Sponsored Research (OSR) under award numbers OSR-2019-CARF/CCF-3097 and OSR-2019-CRG-4093. K. P. S. Z. acknowledges funding from Comunitat Valenciana (APOSTD/2021/368). A. P. acknowledges his Grisolia grant from the Comunitat Valenciana (GRISOLIAP/2020/134).

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