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PERSPECTIVE

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Recent trends in graphene supercapacitors: from large area to microsupercapacitors

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Supercapacitors are being increasingly used as energy storage systems. Graphene, with its huge specific surface area, superior mechanical flexibility and outstanding electrical properties, constitutes an ideal candidate for the next generation of wearable and portable devices with enhanced performance. Since Stoller described the first graphene supercapacitor in 2008, significant developments have been made during this last decade in the development of new graphene-based electrodes. In this way, the specific capacitance has been improved from 135 to 2585 F g⁻¹ and the cyclability has been enhanced from a capacitance retention of just over 80% after 1000 cycles to almost 100% after 20 000 cycles. This review describes how 3-dimensional porous graphene electrodes have been improved recently, from using large area processing techniques to microsupercapacitors. Specifically, (a) the use of graphene foam to obtain large area electrodes, (b) the development of the direct laser writing technique for fast, one-step, and low-cost production of graphene-based supercapacitors, (c) their miniaturization in the form of integrated microsupercapacitors and (d) their functionalization with different pseudocapacitive and electric double-layer capacitor materials to obtain higher capacitance values will be the topics discussed in this perspective.

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1 Introduction

Supercapacitors are energy storage devices, which, in contrast to batteries, show a high power performance, with short charge and discharge times and almost no degradation over long-term cycling.^{1–4} However, these devices cannot match the high energy density achievable by batteries.⁵ In order to get both high power and high energy density at the same time, the coupling of supercapacitors and batteries as hybrid energy storage systems has been actively explored during the last few years for new applications such as electric vehicles.^{6–8} The development of the next generation of supercapacitors that will be components in wearable electronics for self-charging devices, especially in the field of the Internet of Things (IoT), is also one of the fundamental challenges that are currently taken care of.^{9–13}

Supercapacitors can be divided in two types according to the charge storage mechanism: electric double-layer capacitors (EDLCs) and pseudocapacitors, plus their hybrid combinations. EDLCs store energy electrostatically as image charges to the ions of the electrolyte, by an electric double layer surrounding the electrode–electrolyte interface, as modeled by Gouy–Chapman–Stern.¹⁴ This physical process, through non-faradaic charge transfer, is reversible at very high rates and can reach very high powers, while the energy density is small, limited by the 0.18 electrons that every carbon atom can store.¹⁵ Fig. 1a shows a simplified scheme of an EDLC. Pseudocapacitors, on the other hand, make use of fast and reversible surface redox reactions of active materials in electrodes, such as conductive polymers or metal oxides, to increase the amount of charge that can be stored, at the expense of reducing the power and cyclability of the device^{16,17} (scheme represented in Fig. 1b). The main components of a supercapacitor are the electrodes, the corresponding current collectors, a separator between them and an electrolyte, as represented in the scheme shown in Fig. 1c. The electrolyte can be a liquid solvent or a solid-state or quasi-solid-state material^{18,19}. When the electrochemical performance of a material as a potential electrode is of interest, rather than the performance of the whole supercapacitor, the measurements are taken in a three-electrode configuration, as depicted in Fig. 1d. Here, the components are a reference electrode, which is used to be able to locate electrochemical features at the

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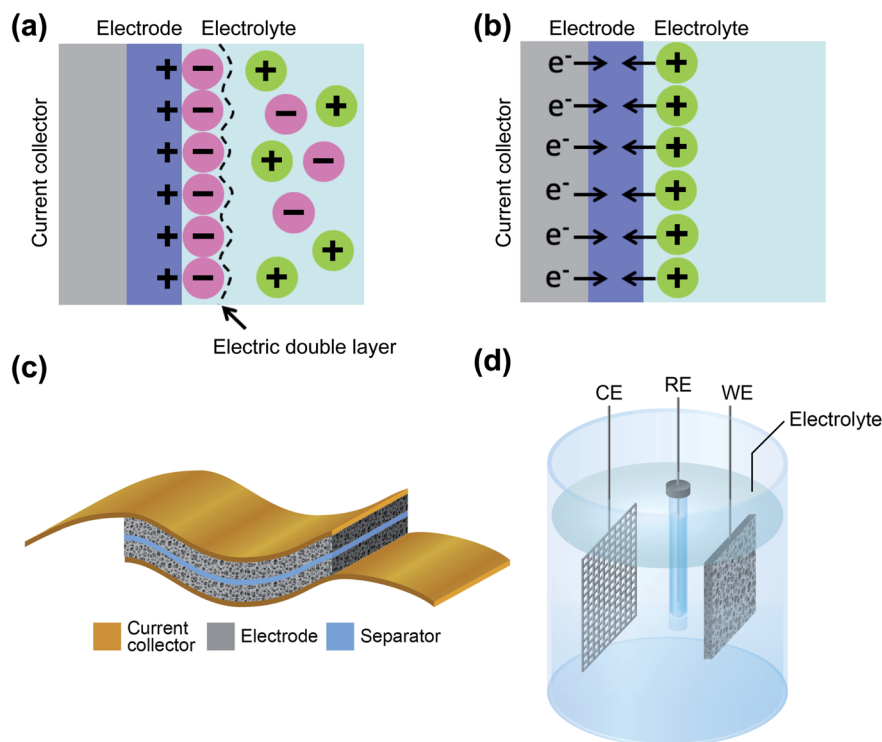


Fig. 1 Scheme of (a) an electric double-layer capacitor and (b) a pseudocapacitor. (c) Schematic drawing of an assembled supercapacitor with a sandwich structure showing current collectors, electrodes and the separator, as used in two-electrode measurements. (d) Schematic drawing of a three-electrode measurement set-up showing a reference electrode, a Pt mesh as the counter electrode and one supercapacitor electrode as the working electrode, all immersed in the electrolyte.

right potentials, a counter electrode, and the electrode under study as the working electrode, all immersed into the electrolyte. However, to determine the charge transfer performance of the full device, a two-electrode measurement is needed, using only a working and a counter electrode that correspond, respectively, to the cathode and anode of the supercapacitor²⁰. In order to compare the performance of supercapacitors built with different carbon materials and pseudocapacitive elements, their designs and standard figures-of-merit should match.²¹ The main relevant factor regarding supercapacitors is the capacitance, which is the amount of charge that the device can store in relation to the total change in its electric potential. However, the capacitance, in farads, is a meaningless number if it is not normalized by the total mass of the electrodes, or by the volume or area the device occupies. For regular supercapacitors, gravimetric or specific values are considered the most important figures-of-merit, normalized by the mass of the active material, in F g^{-1} . Other relevant factors, also reported in this normalized format, are the specific energy density, in W h kg^{-1} , and the specific power density, in W kg^{-1} , of the device. For the specific case of thin film electrodes and microsupercapacitors, gravimetric values are not as relevant as the mass of the electrodes is typically negligible; therefore these metrics are typically given as areal or volumetric values.^{22,23}

Among the several electrode candidates for optimal supercapacitors, graphene has attracted increasing attention due to its striking mechanical, optical and electrical properties and

several effective strategies to synthesize it have been developed and optimized, since its discovery in 2004.²⁴ Specifically in the energy storage field, its high potential as a next generation supercapacitor electrode is due to three main reasons:

(a) Graphene has the highest known surface area of $2630 \text{ m}^2 \text{ g}^{-1}$ (ref. 25) and a very low theoretical density of 2.28 g cm^{-3} .²⁶ With these characteristics, the material has simultaneously a high packing density capability and a very large specific surface area for the interaction of electrons and ions.

(b) It presents an excellent electrical conductivity, with very high mobilities that can reach $200\,000 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ at room temperature,^{27–29} which enables the fast transfer of electrons and ions through the different device interfaces (electrode/electrolyte and electrode/collector). Also because of the high electrical conductivity, graphene can act simultaneously as an active material and current collector, avoiding the use of extra materials such as binders and additives.

(c) Graphene also shows high mechanical strength, with a Young's modulus of 1 TPa and a demonstrated reversible strain up to 25%,³⁰ which makes it suitable for its application in flexible and printable electronics, maintaining a high capacitance retention.

Graphene in four dimensional structures (0D to 3D) has been explored as a component for supercapacitors, since the size and morphology of a material have influence on its physical and chemical properties.³¹ Among them, 3D graphene-based structures are the most studied because they have an intrinsic



interconnected pore structure that provides a large surface area for interaction with ions and electrons and allows a high mass loading of pseudocapacitive materials, characteristics that are fundamental for highly performing supercapacitors. Consequently, three-dimensional graphene structures^{32–34} constitute the focus of the present review, with a special emphasis on the most promising techniques for making 3D graphene electrodes in terms of the achievable surface area, capacity, cyclability, cost-effectiveness and their integration in novel functional applications: (a) graphene foam (GF), (b) graphene materials fabricated by the direct laser writing method (DLW) and (c) graphene as an electrode in microsupercapacitors (MSCs).

(a) Within the family of 3D sponge graphene structures,³⁵ graphene foam (GF) obtained from the reduction of graphene oxide (rGO) by chemical methods^{36–39} and by chemical vapor deposition (CVD)^{40–43} represents the focus in Section 2.1. The improvement of its energy density and operating voltage window as a supercapacitor by filling it with other carbon-based materials such as carbon nanotubes (CNTs)^{44–46} or pseudocapacitive materials such as metal oxides,^{47–49} metal hydroxides^{50–53} and conductive polymers^{54–57} is also discussed within the section.

(b) The direct laser writing method (DLW) applied to produce materials such as laser reduced graphene oxide (LrGO) or laser induced graphene (LIG) was developed by El-Kady *et al.*^{58,59} and constitutes the focus of Section 2.2. The optimization of the fabrication process will lead to the one-step, low-cost and fast production of porous graphene-based supercapacitors. Moreover, the DLW technique bridges the gap between large area electrodes and microelectrodes,⁶⁰ making it a very versatile technology worth developing further. As in the case of graphene foam, the performance improvement of supercapacitors based on LrGO or LIG by the addition of pseudocapacitive or EDLC materials^{61,62} is discussed within the section.

(c) Finally, in Section 2.3, microsupercapacitors (MSCs) based on 3D graphene are discussed, since they are fundamental for enabling new applications that are useful in many industries, from complementing batteries for better power management to their integration with energy harvesters in self-powering modules. MSCs can be fabricated on flexible substrates and show better power performance and cyclability than batteries, extending the lifetime of the system and making them virtually maintenance-free, being the last requirement fundamental for medical applications. Wireless sensors for the Internet of Things, wearable electronics and implantable medical devices are examples of fields where microsupercapacitors play a key role.^{22,63}

In Section 3, the regions in the Ragone plot show the evolution and future tendencies of the graphene electrodes described in Section 2.

2 Graphene electrodes

2.1 Electrodes fabricated with graphene foam

As mentioned in the Introduction, due to its large surface area and high conductivity, graphene was used for the first time as

an electrode for supercapacitors by Ruoff's group.²⁵ Since then, to reduce the agglomeration of graphene sheets to maximize the accessible surface area and the creation of large area supercapacitors constitute strategies that are continuously sought. One of the successful approaches up to now has been the use of graphene foam (GF) synthesized by CVD.⁶⁴ By means of CVD processing, graphene can be grown on copper or nickel foam, and also to a lesser extent on other catalytic metal foam. High quality, single layer graphene can be grown on copper foam, but this thin structure collapses easily when removing the metal support. By using nickel foam, multilayer graphene is grown and by removing carefully the metal template, a self-standing 3D graphene foam structure is formed. This graphene foam allows building flexible, lightweight electrodes with an area of several cm² and heights in the order of millimeters with a high conductivity. The interconnected network of macroscopic, open pores allows a rapid diffusion of the electrolyte ions. Nickel foam can also be used as a template to form rGO foam from chemical synthesis.^{65,66} The disadvantage of this type of graphene foam is that the typical diameter of its pores, in the range of 100 to 500 microns, is quite large. This size of the pores is determined by the diameter of the pores of the initial metal foam. A solution is using graphene foam as a conductive scaffold for the deposition or filling of pseudocapacitive and/or other carbon elements to create highly efficient composites. The graphene foam provides outstanding conductivity and structural stability. The added material of the composite, in intimate contact with graphene, creates faster and shorter transfer paths of electrons and ions for achieving a higher power density. The common composite partners of graphene foam have been metal oxides,^{67–71} conductive polymers^{57,72–76} or metal hydroxides^{53,77,78} mainly because both types of materials are low-cost and already well known in the field of pseudocapacitors. Among the metal oxides, several bimetallic oxides have demonstrated higher electrical conductivity and redox activity than monometallic oxides such as ZnO, NiO or Co₃O₄. Therefore, bimetallic oxides have been used for filling the graphene foam.^{79–83} Yu *et al.*⁷⁹ deposited NiCo₂O₄ nanoneedles on graphene foam, achieving a specific capacity of 1588 F g⁻¹ at 1 A g⁻¹. Sun *et al.*⁸⁰ used NiCo₂O₄ nanoparticles as pseudocapacitive materials and achieved a specific capacity of 2300 F g⁻¹ at 1 A g⁻¹, with a capacitance retention of up to 92% after 4000 cycles at 10 A g⁻¹. Conductive polymers provide greater electrical conductivity and structural stability than some metal oxides. Pedrós *et al.*⁵⁷ electrodeposited a sponge of polyaniline (PANI) nanofibers, 100 nm thick and several microns long, that filled partially the GF pores. They demonstrated that with only an 11% coverage of the pores, it is possible to obtain high gravimetric and volumetric capacitances of 1474 F g⁻¹ and 86 F cm⁻³ at 0.47 A g⁻¹, respectively. In this case the capacitance retention was up to 83% after 15 000 cycles. In some studies, ternary composites have been explored as well,^{84,85} where graphene foam is paired with two different pseudocapacitive elements or a pseudocapacitive element plus an electric double-layer one. For example, Tseng *et al.*⁸⁵ added activated carbon (electric double-layer material) and MnO₂ (pseudocapacitive material) to graphene foam to form a composite.



One of the strategies for increasing the power density and the operating potential window of graphene foam-based electrodes, while maintaining the highest energy density possible, consists in the fabrication of asymmetric supercapacitors.^{77,86–90} In the work of Zhang *et al.*,⁸⁶ an asymmetric supercapacitor composed of graphene foam/MnO₂ and graphene foam/polypyrrole electrodes demonstrated a stable operating voltage window of 1.8 V and a capacitance retention of around 90% after 10 000 cycles. In another remarkable study, Saeed *et al.*⁹⁰ fabricated an asymmetric supercapacitor consisting of a novel graphene foam/CNTs/MoO₃ ternary composite as the positive electrode and a graphene foam/CNT composite as the negative electrode. The scheme of the fabrication process is shown in Fig. 2a. This asymmetric cell demonstrated an outstanding capacitance retention of 94% after 10 000 cycles and an operating voltage window of 1.6 V. The graphene foam/CNTs/MoO₃ electrode exhibited a high specific capacitance of 1503 F g⁻¹ at 1 A g⁻¹, see Fig. 2b, as well as an exceptional capacitance retention of 96.5% after 10 000 cycles.

In this section, we have observed that the fabrication of graphene foam/pseudocapacitive material-based composites is the strategy of choice to optimize the performance of graphene as an electrode for supercapacitors, but generally at the expense of a cycling stability reduction. Therefore, new materials such transition metal sulfides are investigated as potential pseudocapacitive elements.^{91–100} As represented in the scheme shown in Fig. 2c, Kamali-Heidari *et al.*⁹⁶ grew hierarchically Ni₃S₂ nanorods on CVD graphene foam, to achieve a specific

capacitance of 1900 F g⁻¹ at a current density of 1 A g⁻¹ (Fig. 2d) and a capacitance retention of 96% after 2000 cycles. Kang *et al.*⁹⁷ grew NiCo₂S₄ nanowires on CVD graphene foam, which provided a specific capacitance of 1454 F g⁻¹ at 1.3 A g⁻¹ and a capacitance retention of 96% after 3000 cycles at 13 A g⁻¹. These recent results show that under optimization, new material combinations could provide higher electrical conductivity and structural stability than metal oxides and conductive polymers.

Another direction to improve the capacitance of graphene foam based supercapacitors is the maximization of the electrochemically available surface area and the minimization of the collapse or restacking of the graphene foam pores. This can be achieved by optimizing the metallic foam used as the template. As we explained at the beginning of this section, the diameter of the pores from typical commercial Cu or Ni foam lies in the range of 100 to 500 microns. The density and the diameter of Cu or Ni foam can be tuned by adjusting the catalyst particle size and the growth temperature. In this way, Drieschner *et al.*¹⁰¹ fabricated Cu and Ni foam with smaller pores that were produced by CVD, and graphene foam with pore sizes in the range of 0.5 to 30 microns, which presented an electrochemically available surface area 10 000 times larger than that of the graphene foam fabricated by using commercial metal templates. These small pore GFs also presented a higher mechanical stability after the removal of the template by etching and a capacitance retention of almost 100% after 10 000 cycles at 4 mA cm⁻³. Another group achieved GF with 1 μm size



Fig. 2 Different examples of graphene foam (GF) supercapacitors: (a) schematic diagram illustrating all steps involved in the synthesis of 3D GF/CNTs/MoO₃ hybrid film-based electrode and the asymmetric supercapacitor device and (b) specific capacitance vs. current density obtained with this supercapacitor. (a) and (b) are reproduced with permission from ref. 90. Copyright 2018 Elsevier B.V. (c) Schematic diagram of the fabrication process of the Ni-GF/Ni₃S₂ nanocomposite electrode and (d) specific capacitances of different electrode materials plotted as a function of current density. (c) and (d) are reproduced with permission from ref. 96. Copyright 2018 Elsevier B.V.



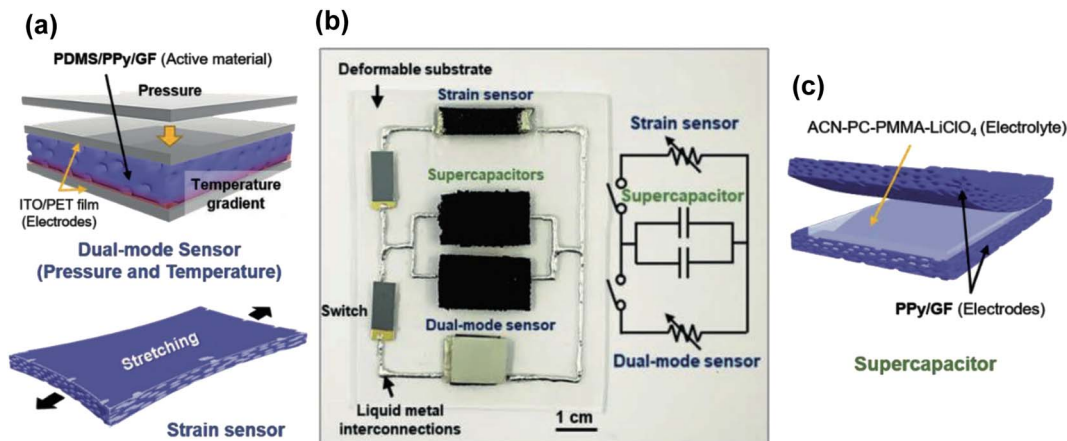


Fig. 3 (a) Schematic drawing of a pressure/temperature sensor and a strain sensor based on polypyrrole/graphene-foam material. (b) Picture showing the electric circuit connecting these elements with two supercapacitors using liquid metal interconnects. (c) Schematic drawing of the PPy/GF sandwich supercapacitor. Reproduced with permission of ref. 106. Copyright 2018 Wiley-VCH Verlag GmbH & Co. KGaA, Weinheim.

pores by CVD using sintered Ni nanoparticles as template.¹⁰² Recently, a graphene foam composed mainly of mono- and bi-layers was achieved by designing a Ni-Cu foam alloy.¹⁰³ Another group, starting from commercial Ni foam, created a Ni-Cu foam alloy where they further synthesized Cu-Ni micro-branches of 2–5 microns in diameter and 20–30 microns in length. These highly dense microbranches reduced the percentage of empty porous space in the commercial Ni foam by

up to 63%.¹⁰⁴ 3D-printed porous silica templates designed with different pore/void sizes and complex architectures were used to obtain GF by CVD with a large surface area, 994.2 m² g⁻¹, and good mechanical properties.¹⁰⁵

Finally, graphene foam and its composites have been proven to have high potential in advanced applications. For example, GF synthesized by CVD and coated with polypyrrole (PPy) has shown outstanding flexibility (180°) and stretchability (up to

Table 1 Graphene foam-based electrodes

Graphene foam fabrication method	Active material	Specific capacitance (F g ⁻¹)	Retention (%) after a number of cycles	Year	Ref.
CVD	Ni(OH) ₂ nanoflakes	900 at 1 A g ⁻¹	98% at 2 A g ⁻¹ after 3000	2015	53
rGO	NiCo ₂ S ₄ nanoparticles	1526 at 1 A g ⁻¹	83% at 10 A g ⁻¹ after 2000	2015	98
CVD	NiCo ₂ O ₄ flowers	1402 at 1 A g ⁻¹	76.6% at 5 A g ⁻¹ after 5000	2015	81
CVD	NiCo ₂ O ₄ nanoneedles	1588 at 1 A g ⁻¹	85.7% at 10 A g ⁻¹ after 2000	2015	79
CVD	PANI nanofibers	1474 at 0.47 A g ⁻¹	83% at 28 mA cm ⁻² after 15 000	2016	57
rGO	Ni-Co layered double hydroxide nanowires	1041 at 2.5 mA cm ⁻²	95% at 12.5 mA cm ⁻² after 2000	2016	77
rGO	NiCo ₂ O ₄ nanoparticles	2300 at 1 A g ⁻¹	92% at 10 A g ⁻¹ after 4000	2016	80
CVD	Co(OH) ₂ nanoflakes	1636 at 0.5 A g ⁻¹	75% at 20 mV s ⁻¹ after 1000	2017	78
CVD	V ₂ O ₅ flowers	1235 at 2 A g ⁻¹	92% at 4 A g ⁻¹ after 5000	2017	70
CVD	Mn _{3.63} Co _{5.37} S ₈ nanohoneycombs	1938 at 5 A g ⁻¹	82% at 50 A g ⁻¹ after 4000	2017	92
RGO	H-CoMoO ₄ nanoplates	1472 at 1 mA cm ⁻²	92.3% at 15 mA cm ⁻² after 3000	2017	88
CVD	NiO nanoflakes	1782 at 1 A g ⁻¹	90.2% at 1 A g ⁻¹ after 5000	2017	71
CVD	ZnCo ₂ O ₄ nanowires	1626 at 1 A g ⁻¹	76.4% at 1 A g ⁻¹ after 5000	2017	82
CVD	NiCo ₂ S ₄ /Co _x Ni _(3-x) S ₂ core/shell nanowires	1950 at 1.25 A g ⁻¹	93% at 50 mA cm ⁻² after 5000	2017	91
RGO	Co ₃ S ₄ nanoflakes	2314 at 2 mV s ⁻¹	92.6% at 100 mV s ⁻¹ after 1000	2017	94
CVD	NiCo ₂ O ₄ @MnO ₂ core-shell nanowires	2125 at 1 A g ⁻¹	93.4% at 20 A g ⁻¹ after 5000	2017	84
CVD	Activated carbon and MnO ₂ flowers	813 at 1 A g ⁻¹	98.4% at 20 mV s ⁻¹ after 1000	2018	85
CVD	NiO nanoflakes	1062 at 1 A g ⁻¹	90.6% at 1 A g ⁻¹ after 5000	2018	68
CVD	CNT and MoO ₃ nanoplates	1503 at 1 A g ⁻¹	96.5% at 8 A g ⁻¹ after 10 000	2018	90
CVD	Ni ₃ S ₂ nanorods	1900 at 1 A g ⁻¹	96% at 5 A g ⁻¹ after 2000	2018	96
CVD	NiCo ₂ S ₄ nanowires	1454.6 at 1.3 A g ⁻¹	96% at 13 A g ⁻¹ after 3000	2018	97
RGO	NiCo ₂ S ₄ nanosheets	2526 at 2 A g ⁻¹	77% at 20 A g ⁻¹ after 2000	2018	93
RGO	NiGa ₂ S ₄ nanoparticles	2124.34 at 1 A g ⁻¹	94.2% at 9 A g ⁻¹ after 5000	2019	99
CVD	Ni ₃ S ₂ nanoparticles	2585 at 1 A g ⁻¹	89% at 20 A g ⁻¹ after 5000	2020	100
CVD	N-doping NiMoO ₄ nanoflakes	1913 at 1 A g ⁻¹	90.7% at 100 mV s ⁻¹ after 3000	2020	83



50% strain).⁷⁴ These properties were used to fabricate a device with a pressure-temperature dual sensor connected to a supercapacitor, where the common active material of both components was a PDMS/PPy/GF/composite.¹⁰⁶ These devices could be bent up to 5% and exhibit almost no performance degradation over 10 000 cycling under load pressure, which shows their suitability for stretchable and wearable electronics. This integrated device is shown in Fig. 3.

To conclude this section, we provide Table 1, where we have summarized the graphene foam based electrodes (with their corresponding references) that we have found to present the best performance in terms of specific capacitance and cyclability during the last 5 years. We have arranged them in chronological order, to track the trends in performance and chosen composite materials over time.

2.2 Electrode fabricated by laser direct writing

Graphene foam described in the previous section produces supercapacitors with high specific energy, capacitance retention and a large area. GF also presents a high surface area, which can be further optimized by designing the size of the pores. However, the total processing time of CVD is long and the cost is still relatively high compared to other techniques. Regarding the obtention of rGO foam by chemical reduction of GO, it is more cost-effective and also suitable for large area production compared to CVD, but this method has not yet optimized the reduction efficiency, which hinders the conductivity of the material. Hence, an alternative technique is direct laser writing (DLW) processing, which constitutes a low-cost, fast, single step, large area reduction technique to produce 3D porous graphene. DLW can be applied to reduce GO under laser irradiation (LrGO).⁵⁸ First, a thin layer of graphene oxide is deposited onto a substrate. Then, the surface of the GO is irradiated with a commercial laser. The incident light exfoliates and reduces the stacked GO sheets simultaneously, creating a porous, conductive material, only in the areas exposed to the laser. The process increases the surface area available for the electrolyte ions and leaves a very low percentage of oxygen content. A scheme of the process is summarized in Fig. 4a. Another approach to obtain 3D porous graphene is the production of laser-induced graphene (LIG) from a polymer substrate by pyrolysis,^{107–113} as presented in Fig. 4b. Finally, the graphene oxide (in the LrGO case) or initial polymer substrate (in the LIG case) can be functionalized/mixed with another active material before the laser irradiation in order to control the morphology of the produced graphene, dope it or produce a composite in a single step, as summarized in Fig. 4c.

The main mechanisms to reduce graphene oxide by DLW are through the photochemical effect (laser wavelengths (λ) below 400 nm), the photothermal effect (laser wavelengths (λ) above 400 nm) and under certain wavelengths and irradiation conditions, through both effects.^{114,115} In the case of the photochemical effect, for lasers with the wavelength in the ultraviolet (UV) or near UV region, the energy of the incident photons breaks the chemical bonds of the oxygen-containing radicals that are attached to the graphene surface such as $-\text{COOH}$ or



Fig. 4 Schemes of direct laser writing (DLW) applied on (a) graphene oxide to reduce it (LrGO), (b) a polymer substrate to produce graphene (LIG), and (c) graphene oxide or a polymer substrate functionalized with a material deposited onto them (represented by the green spheres) before the laser irradiation, in order to influence the final morphology of the produced graphene or fabricate a graphene/pseudocapacitive composite in a single step. In most of the cases, the functionalizing material is chemically modified during the DLW process, as indicated by the change of the color of the spheres after laser irradiation.

$-\text{OH}$, producing LrGO. In the case of the photothermal effect, for lasers with higher wavelengths, especially in the infrared (IR) or near infrared (NIR) region, the high energy density of the incident light is absorbed by the graphene oxide and as a consequence, a very high thermal heat is concentrated locally. This generated heat breaks the chemical bonds of the oxygen-containing radicals, producing LrGO. To produce LIG from an initial polymeric substrate, a high temperature is needed to rearrange the chains into the graphene structure; therefore, lasers with wavelengths that induce the photothermal effect are required.^{62,116} The most commonly used polymeric substrate is commercial polyimide. However, LIG has been fabricated from a wide variety of substrates, such as wood,¹¹⁷ phenolic resin,¹¹⁸ lignin,¹¹⁹ polyether ether ketone (PEEK)¹¹⁰ or carbon nanospheres.¹²⁰

The laser irradiation parameters such as the incident wavelength, power intensity, scanning speed and step size have a strong influence on the quality of the produced 3D porous graphene and its performance as a supercapacitor. Therefore, the parameters of each laser setup must be calibrated first to obtain optimized LrGO or LIG. For example, Bhattacharjya



Table 2 LIG and LrGO-based electrodes

Starting material	Added active material to LIG/LrGO	Final material	Laser (wavelength & writing parameters)	Capacitance	Energy density	Power density	Retention (%) after a number of cycles	Year	Ref.
Polyimide	—	LIG	522 nm, 140 mW, 500 fs pulse, 1 MHz, 2 J cm ⁻² , 1 mm s ⁻¹ speed	800 μF cm ⁻² at 10 mV s ⁻¹	—	—	96% after 3000 at 0.3 mA cm ⁻²	2015	112
GO	—	LrGO	Nd:YVO ₄ , 355 nm, 244 mW, 20 ns pulse, 0.9 kHz, 2.5 mm s ⁻¹ speed, 2.5 × 10 ⁻⁴ J mm ⁻²	106 μF cm ⁻² at 0.3 V s ⁻¹	—	—	92.5% after 5000 at 0.6 V s ⁻¹	2016	125
GO	MnO ₂	LrGO + MnO ₂	CO ₂ , 10.6 μm, 2 W, 1 pass, 394 p cm ⁻¹ pulse density	172 F g ⁻¹ at 1 mA cm ⁻²	0.409 mW h cm ⁻³	537 W cm ⁻³	95% after 2000 at 1 mA cm ⁻²	2016	136
GO	—	LrGO	355 nm, ns frequency, 5 m s ⁻¹ speed	3.9 mF cm ⁻² at 0.3 mA cm ⁻²	0.98 mW h cm ⁻³	300 mW cm ⁻³	93% after 20 000 at 1 V s ⁻¹	2016	128
GO	PEDOT	LrGO + PEDOT	IR laser 788 nm, 100 mW	43.75 mF cm ⁻² at 0.2 mA cm ⁻²	—	—	83.6% after 1000 at 0.2 mA cm ⁻²	2017	130
V ₂ O ₅ nanobelts and GO mixture	—	LrGO + VO _x	1064 nm, 3 W, 10 μs pulse, 20 kHz, 2 m s ⁻¹ speed	252 F g ⁻¹ at 1 A g ⁻¹	—	—	92% after 10 000 at 100 A g ⁻¹	2017	140
Polyimide	N-doping/PEDOT	N-LIG + PEDOT	CO ₂ , 10.6 μm, 5 W, 1 pass, 14 μs pulse	790 μF cm ⁻² at 50 μA cm ⁻²	—	—	96% after 10 000 at 0.2 mA cm ⁻²	2018	137
Phenolic resin	—	LIG	405 nm, 500 mW, 200 mm s ⁻¹ speed	0.78 mF cm ⁻² at 500 mV s ⁻¹	—	—	93.78% after 2000 at 1 mA cm ⁻²	2018	118
Polyimide	—	LIG	Nd:YAG, 1064 nm, 7.5 A, 11 kHz, 80 mm s ⁻¹ speed	34.7 mF cm ⁻² at 0.1 mA cm ⁻²	1 mW h cm ⁻³	11 mW cm ⁻³	80% after 3000 at 0.15 mA cm ⁻²	2018	107
GO/Co ₃ O ₄ nanorod mixture	—	LrGO + Co ₃ O ₄ nanoparticles	KrF excimer, 248 nm, 25 ns, 10 Hz, 400 mJ per pulse per cm	978.1 F g ⁻¹ at 1 A g ⁻¹	—	—	99.3% after 20 000 at 10 A g ⁻¹	2018	142
Lignin/PVA mixture	—	LIG	CO ₂ , 10.6 μm, 1.8–3 W, 2.25 inches s ⁻¹ speed	25.1 mF cm ⁻² at 0.05 mA cm ⁻²	1 mW h cm ⁻³	2 W cm ⁻³	99.2% after 12 000 at 2 mA cm ⁻²	2018	119
GO	—	LrGO	308 nm, 5 MW, 105 mJ, 100 Hz	299 F g ⁻¹ at 2 mV s ⁻¹	—	—	97.8% after 4000 at 50 mV s ⁻¹	2018	127
GO paper	—	LrGO	Nd:YVO ₄ pulsed laser, 355 nm, 20 ns pulse, 325 mW, 2 mm s ⁻¹ scan speed	13.8 μF cm ⁻² at 10 mV s ⁻¹	—	—	95% after 3400 at 10 mV s ⁻¹	2019	129
ErGO	—	LrErGO	CO ₂ , 10.6 μm, 30 W, 0.5 m s ⁻¹ speed, 20 000 Hz, 1 pass	2.32 mF cm ⁻² at 10 μA cm ⁻²	0.46 mW h cm ⁻²	0.57 W cm ⁻²	100% after 100 000 at 0.166 mA cm ⁻²	2019	149
GO	PANI nanofibers	LrGO + PANI	788 nm, 5 mW, 1 pass, 25 min cycle ⁻¹	442 F g ⁻¹ at 0.18 A g ⁻¹	—	—	84% after 2000 at 3.7 A g ⁻¹	2019	139
Polyimide	—	LIG	1064 nm, 7 W, 800 mm s ⁻¹ speed	995 mF cm ⁻² at 1 mA cm ⁻²	55.9 μW h cm ⁻²	9.39 mW cm ⁻²	98% after 10 000 at 1 mA cm ⁻²	2019	131
GO/urea mixture	—	N-LrGO	1064 nm, 0.2 W, 10 ps pulse, 1 MHz, 100 mm s ⁻¹ speed	60.7 mF cm ⁻² at 0.5 mA cm ⁻²	6.8 μW h cm ⁻²	0.25 mW cm ⁻²	98.7% after 25 000 at 10 mA cm ⁻²	2019	132
Carbon nanospheres	—	LIG	CO ₂ , 10.6 μm, 3.4 W, 1 pass, 3% speed	0.8 mF cm ⁻² at 0.078 mA cm ⁻²	0.18 mW h cm ⁻³	28 W cm ⁻³	98.6% after 12 000 at 1.56 mA cm ⁻²	2019	120



Table 2 (Contd.)

Starting material	Added active material to LIG/LrGO	Final material	Laser (wavelength & writing parameters)	Capacitance	Energy density	Power density	Retention (%) after a number of cycles	Year	Ref.
Polyimide coated with Mo ⁶⁺ , Mn ²⁺ and S ²⁻ solution	—	LIG + MoS ₂ + MnS	CO ₂ , 10.6 μm, 5–10 W, 14 μs pulse, 5–180 cm s ⁻¹	58.3 mF ULLK/m ⁻² at 50 μA cm ⁻²	7 μW h cm ⁻²	49.9 μW cm ⁻²	93.6% after 10 000 at 1 mA cm ⁻²	2019	145
GO/TiO ₂ nanocrystal solution	—	LrGO + O-deficient TiO ₂	KrF excimer, 248 nm, 5 Hz, 25 ns pulse, 300 mJ per pulse cm ²	401.7 F g ⁻¹ at 1 A g ⁻¹	14.1 W h tkg ⁻¹	8.5 kW kg ⁻¹	≈ 100% after 20 000 at 10 A g ⁻¹	2019	141
Polyimide	PEDOT	LIG + PEDOT	CO ₂ , 10.6 μm, 4.5 W, 20% speed, 1000 pulses per inch	115.2 F g ⁻¹ at 0.5 A g ⁻¹	—	—	97.5% after 4000 at 2 A g ⁻¹	2020	138
Kraft lignin	—	LIG	CO ₂ , 10.6 μm 32 W, 20 cm s ⁻¹ speed, 1000 pulses per inch	880 μF cm ⁻² at 10 mV s ⁻²	0.152 μW h cm ⁻²	25 μW cm ⁻²	91% after 10 000 at 0.02 mA cm ⁻²	2020	124
GO	PANI	LrGO + PANI	850 nm, 1200 mW	72 mF cm ⁻² at 35 μA cm ⁻²	2.7 mW h cm ⁻³	81.4 mW cm ⁻³	93.5% after 1000 at 0.7 mA cm ⁻²	2020	133
Polyimide coated with Ni and Co ion ink	—	NiO/Co ₃ O ₄ /LIG	CO ₂ , 10.6 μm 2.5 W, 100 mm s ⁻¹ speed	29.5 mF cm ⁻² at 0.05 mA cm ⁻²	—	—	99.1% after 10 000 at 0.05 mA cm ⁻²	2020	150

*et al.*¹²¹ found the optimum conditions of their system, a low-cost CO₂ commercial laser ($\lambda = 10.6 \mu\text{m}$), under an incident power of 30 W, scanning speed of 10 mm s⁻¹ and step size of 100 μm and the worst conditions under an incident power of 30 W, scanning speed of 500 mm s⁻¹ and step size of 500 μm, in order to produce LrGO. This clearly means that under the same incident power, a slower scan speed and a smaller step size mean a higher irradiation time of GO and consequently, a higher removal efficiency of the oxygen-containing radicals from the graphene surface.

Currently, a CO₂ commercial laser ($\lambda = 10.6 \mu\text{m}$) is the most widely used to produce both LrGO^{121,122} and LIG.^{123,124} Then, a pulsed laser with the wavelength in the UV region has been used in several studies in order to produce LrGO.^{125–129} The principal motivation to use this type of laser is that the level of reduction is very high at short times, since pulses in the order of nanoseconds/femtoseconds can produce incident powers in the order of MW. Different laser systems with intermediate wavelengths between the IR and the UV region have also been employed to produce 3D porous graphene.^{130–133} Similar to the DLW approach, as an attempt to maximize the rGO area in minimum time, Xiang *et al.*¹³⁴ irradiated GO films electro-sprayed onto stainless steel sheets of 2 cm² area for 3 min inside a home-use convection oven (far infrared irradiation) under air conditions. During the irradiation process, within which the chamber of the oven reached 200 °C, the GO film was exfoliated and the oxygen-containing functional groups were decomposed, obtaining rGO porous electrodes with a specific capacitance of 320 F g⁻¹ at 0.2 A g⁻¹ and a capacitance retention of 94% after 10 000 cycles. Some of the recent examples with all the different lasers are summarized in Table 2, shown at the end of this section.

In the case of electrodes based on 3D porous graphene synthesized by DLW, the pseudocapacitive material fillings have another role in addition to the enhancement of the device performance: their presence minimizes the restacking of the graphene sheets during charge/discharge cycling.^{108,135–138} Ladrón-de-Guevara *et al.*¹³⁹ coated LrGO ($\lambda = 788 \text{ nm}$) with PANI nanofibers by electrodeposition. The composite electrode presented a specific capacitance of 442 F g⁻¹, as opposed to the 81 F g⁻¹ value achieved by the bare LrGO electrode, as shown in the graph of Fig. 5a. Fig. 5b to d show the scanning electron microscope (SEM) images of the electrodes in different processing stages. In Fig. 5b, a difference in the morphology before (GO) and after (LrGO) laser irradiation is observed. Fig. 5c shows a close-up image of the exfoliated and reduced GO, marked with a white area in Fig. 5b. Finally, in Fig. 5d, the densification and consequent increase of the surface area in the electrode after the deposition of PANI nanofibers can be observed as compared with the bare LrGO electrode shown in Fig. 5c.

As mentioned above and shown in Fig. 4c, DLW allows the fabrication of a 3D porous graphene/pseudocapacitive material composite in a single step, by irradiating a GO/pseudocapacitive material mixture in the case of LrGO^{140–142} or a polymer surface coated with a pseudocapacitive material layer in the case of LIG.^{143–145} Yang *et al.*¹⁴¹ irradiated a TiO₂ nanocrystals/GO



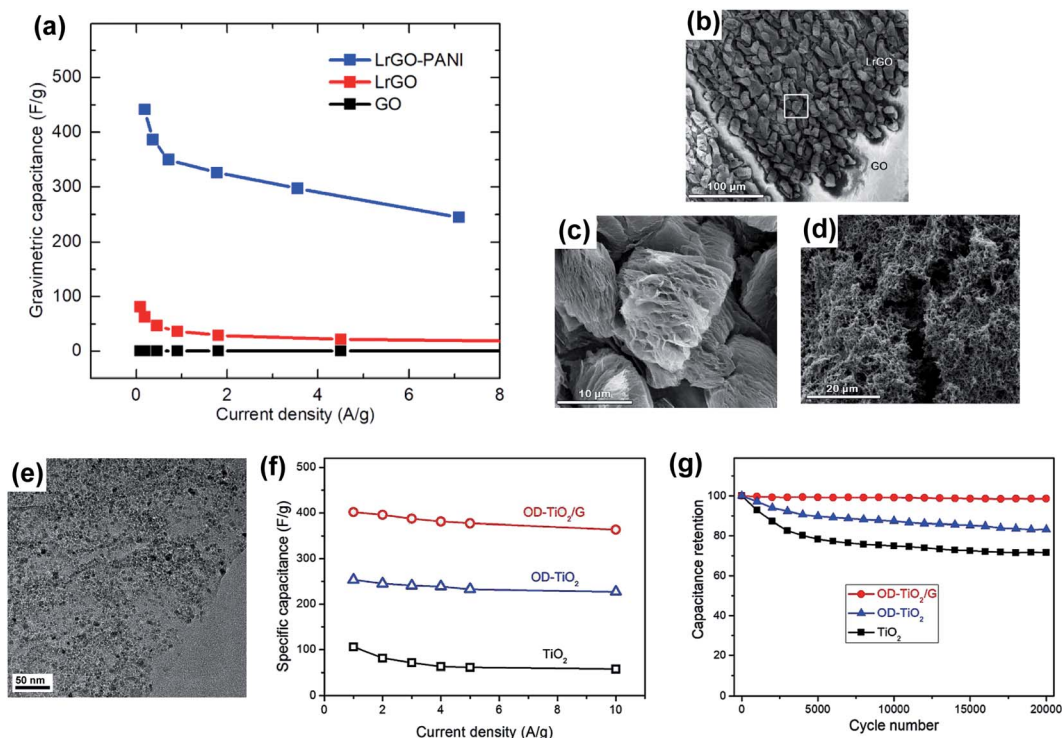


Fig. 5 (a) Specific capacitance as a function of the current density obtained with a GO electrode (black line), a rGO electrode (red line) and a rGO/PANI nanofiber composite electrode (blue line). This comparison shows the superior performance of the rGO electrode when was coated with the PANI nanofibers. (b) SEM secondary image showing an area before (GO) and after (LrGO) the laser irradiation. (c) SEM secondary image showing an area with LrGO flakes, after laser irradiation. It corresponds to the white area marked in (b). (d) SEM image showing an area with LrGO flakes coated with PANI nanofibers after electrodeposition. (a)–(d) are reproduced with permission from ref. 139. Copyright 2018 Elsevier B.V. (e) A TEM image of an oxygen-deficient TiO_2 nanocrystals/rGO composite, which shows a uniform distribution of the nanocrystals over the rGO surface. (f) Specific capacitance as a function of the current density obtained with a TiO_2 electrode (black line), an oxygen-deficient TiO_2 electrode (blue line) and an oxygen-deficient TiO_2 /rGO composite electrode (red line). This comparison was made to show the superior performance of the oxygen-deficient TiO_2 /rGO composite electrode with respect to the other compositions. (g) Cycling stability plot of the composites, where the oxygen-deficient TiO_2 /rGO composite electrode shows almost 100% capacitance retention after 20 000 cycles. (e)–(g) are reproduced with permission from ref. 141. Copyright 2019 Elsevier B.V.

composite with a KrF excimer laser ($\lambda = 248$ nm, 25 ns pulse), enabling simultaneously the reduction of the GO and the creation of oxygen vacancies on the TiO_2 nanocrystals. A TEM image of the final oxygen-deficient TiO_2 nanocrystals/LrGO composite, presented in Fig. 5e, shows the uniform distribution of the nanocrystals over the LrGO surface. These defects were introduced to increase both the active sites and the electrical conductivity. Measuring this electrode, a specific capacitance of 400 F g^{-1} (Fig. 5f) and a barely disturbed capacitance retention after 20 000 cycles (Fig. 5g) were obtained. Clerici *et al.*¹⁴³ covered the initial polyimide substrate with a MoS_2 dispersion and then, they irradiated the mixture with a CO_2 laser, producing a MoS_2 sheets/LIG composite. The introduction of an extra material into the initial mixture with GO before laser irradiation can also be done to influence the final structure of LrGO. For example, Lee *et al.*¹⁴⁶ infiltrated Zn by atomic layer deposition on GO before laser irradiation. The presence of Zn influenced the morphology of the obtained LrGO by increasing the total specific surface area. The effect was a four-fold increase in the specific capacitance: from 21 F g^{-1} (GO \rightarrow LrGO) to 83 F g^{-1} (GO/Zn \rightarrow LrGO/Zn).

Finally, 3D porous graphene electrodes fabricated by DLW have been successfully transferred to flexible and stretchable substrates, in view of the rise of wearable and portable electronics. For example, Lamberti *et al.*¹¹³ developed a method to transfer LIG produced from a polyimide substrate onto polydimethylsiloxane (PDMS), as described in the scheme shown in Fig. 6a. They used the LIG/PDMS substrate to fabricate a supercapacitor (Fig. 6b), which presented a capacitance retention of 96% after 10 000 cycles (Fig. 6c) under static conditions and of almost 90% after 1000 cycles under bending conditions (from 0° to 160°). Moreover, several prototypes of ‘self-charging power unit’ devices, where DLW fabricated LrGO or LIG supercapacitors were coupled to a solar cell, have been recently demonstrated.^{147–150} In Fig. 7a, a scheme of one of these self-powered integrated devices is shown: the LIG supercapacitor stores energy from the solar cell and illuminates an LED.¹⁴⁹ Fig. 7b shows a real device. In Fig. 7c, it is shown that the LIG-based supercapacitor presented a capacitance retention of almost 100% after 100 000 cycles at 0.166 mA cm^{-2} .

To conclude this section, we provide Table 2, where LrGO and LIG electrodes fabricated with different laser systems and





Fig. 6 (a) Scheme of the transfer process of LIG fabricated by DLW on polyimide onto a flexible and stretchable PDMS substrate. (b) Scheme of the structure of a stretchable supercapacitor fabricated using the LIG/PDMS substrates. In the bottom right corner, the photograph of a real device is shown. (c) Cycling stability of a supercapacitor showing a capacitance retention of 96% after 10 000 cycles. The graph in the inset corresponds to a set of charge/discharge cycles, which shows a highly stable performance. Reproduced with permission from ref. 113. Copyright 2016 Wiley-VCH Verlag GmbH & Co. KGaA, Weinheim.

with/without added active materials to form a composite in the last few years are represented, together with the used laser system and conditions, the device performance and the corresponding references. We have ordered the electrodes in chronological order to track the evolution of the electrodes over time.

2.3 Microelectrodes

Microsupercapacitors (MSCs) are miniaturized supercapacitor devices. Compared to standard supercapacitors, MSCs use the same graphene-based materials and electrolytes, and include the same components; their performance can also be enhanced by the use of pseudocapacitive elements and their working principle is unchanged, while the scale is greatly reduced. This change in size comes not only in the in-plane dimensions, but more importantly in the out-of-plane one, its thickness. MSCs are thin-film, planar devices, a characteristic that allows them to

be integrated much more easily in a variety of applications and processes, including flexible and wearable devices⁶³ or placed alongside silicon using the same CMOS-compatible processes for on-chip energy storage.¹⁵¹

While standard supercapacitors use sandwiched/stacked parallel electrodes in planar or rolled-up configurations for coin-like cells, MSCs are typically designed as interdigitated electrodes (IDEs), since this configuration presents several advantages over stacked ones. Fig. 8a depicts the comparison between the traditional sandwich (top) and the IDE (bottom) geometries.²² In Fig. 8b, the ion transport in both device geometries is compared as well.²² This interdigitated design is used because it provides two clear advantages, while also adding some challenges. First, it allows processing the full device on the same surface, eliminating the need for an extra assembly step, which is key for being integrated into Si-based CMOS processes on an industrial scale. It is also beneficial for ionic transport, as the perimeter of both electrodes is maximized with the number of fingers and provides a better contact and accessibility for the electrolyte. Structuring the electrode in fingers shortens ion diffusion paths, which allows for better rate performance and enables the use of certain materials that would have poor electrolyte infiltration if used in bulk. It also avoids the need of using a separator, as both electrodes are separated in-plane by a controllable gap, adding new possibilities to the design and optimization of these structures to maximize the performance. The main challenge, however, is the need to define the designed geometry for all the layers of the device, from the current collectors to the active material. This can be done through standard lithography and etching processes or through other less conventional methods, including laser ablation,^{152–158} mask-assisted filtration,^{159–162} screen printing,^{163,164} extrusion printing,¹⁶⁵ electrodeposition^{156,166,167} and others.

Typically, current collectors are fabricated by lithography, by means of photoresist layer deposition, metal evaporation/sputtering and liftoff. This allows transferring the interdigitated design from an optical mask to the sample and can be done on different substrates depending on the final application. Differences arise from the deposition of the electrode materials, which are conductive porous carbons that also need to be geometrically defined. Starting from a dispersion contained in a solvent, deposition and filtration through a hard mask can create such electrodes, the limitation being the size control and resolution of this physical hard mask that needs to be machined or 3D printed. Screen printing or doctor blade coating can also be used, though they share the same limitation. Chemical vapor deposition allows depositing graphene materials from gaseous carbon precursors onto a substrate at a very high temperature and offers great control over the porosity and microstructure, and even more if plasma is added to the chamber. The huge number of parameters that can be tuned makes it a very versatile but complex process, which in most cases needs extra steps to eliminate carbon materials from the surface, as the deposition occurs in the whole surface of the substrate. Electrodeposition can also be used to form porous and conductive electrodes from graphene materials in solution, and it is also





Fig. 7 (a) Schematic drawing of a circuit including an energy storage element (based on LIG microsupercapacitors), a solar panel and an LED. (b) Labeled pictures showing the working elements of this circuit. (c) Graph showing the galvanostatic charge–discharge cycling test for the microsupercapacitor module, keeping an outstanding capacitance retention for up to 100k cycles. Reproduced with permission from ref. 149. Copyright 2019 Royal Society of Chemistry.

a common method to deposit pseudocapacitive elements, facilitating the production of hybrid and asymmetric devices.

Some of these carbon materials can be used as-fabricated, due to their good properties regarding conductivity, porosity and geometry definition, while others such as graphene oxide, a very common and accessible graphene material, need to be further processed. Graphene oxide in this case can be reduced

chemically or thermally. Direct laser writing appears as a very versatile technique which can be used to synthesize new graphene materials (a) from polymers, as is the case of LIG,¹¹² (b) by processing GO into LrGO⁵⁸ and (c) by even processing pseudocapacitive elements, for example, modifying manganese acetate into MnO₂.¹⁶⁸ Furthermore, it can also be used to define the electrode geometry in a very precise and controlled way. When



Fig. 8 Schematic drawing of (a) two assembled microsupercapacitors (MSCs) with sandwich (top) and interdigitated (bottom) geometries, and of (b) the ion transport at the nanometric scale in both configurations: sandwich (top) and interdigitated (bottom) geometries. Reproduced with permission from ref. 22. Copyright 2014 Royal Society of Chemistry.



a laser is used to define the geometry of a previously processed material, the process will be referred to as 'laser ablation' or 'laser etching', while 'laser processing' will be used to refer to processes where the laser modifies the active electrode materials, such as LrGO or LIG.

Tiliakos *et al.*¹⁶⁹ explored in 2018 the versatility of the DLW technique in order to create increasingly intricate fractal architectures which increased the perimeter of electrodes for better electrolyte accessibility, achieving 6.1 mF cm^{-2} . In order to increase the performance of these devices, pseudocapacitive elements are added. Using environmentally friendly biomass as a precursor, Zhang *et al.*¹¹⁹ fabricated MSCs from a lignin/PVA film. The porous laser scribed lignin device achieved a surprising 25 mF cm^{-2} capacitance with a high power density of 8 mW cm^{-2} and energy density of $4 \mu\text{W h cm}^{-2}$. Wu *et al.*¹⁷⁰ grew selectively, by solvothermal synthesis, conductive 2D metal-organic framework (MOF) structures on a LIG substrate, which acts also as the current collector in these MSCs, getting an areal capacitance of 15.2 mF cm^{-2} and a decent energy density of $4.1 \mu\text{W h cm}^{-2}$ at a high-power density of 7 mW cm^{-2} . Similarly, but with a simpler approach, Bhattacharya *et al.*¹⁷¹ used a LIG interdigitated MSC and enhanced its performance with 'red mud', a mining by-product containing metallic oxides, known to be good pseudocapacitive elements. By simple dip-coating, the capacitance reached 203 mF cm^{-2} at 0.1 mA cm^{-2} , with an energy density of $18 \mu\text{W h cm}^{-2}$ at 0.66 mW cm^{-2} . It is important to highlight the work of Yu *et al.*,¹⁷² in which a commercial PI film was modified to make it more sensitive to a laser and achieve thicker 3D LIG frameworks of up to $320 \mu\text{m}$. Adding a high mass loading of pseudocapacitive polypyrrole, the devices reached a very high energy density of $134.4 \mu\text{W h cm}^{-2}$ at 0.3 mW cm^{-2} , with a great cycling stability of 95.6% at 2.5 mA cm^{-2} after 10 000 cycles.

Direct laser writing can also be used to process and transform not only substrates, but also pseudocapacitive elements. Zhu *et al.*¹⁶⁸ fabricated a LIG MSC from PEEK, a technical polymer, and afterwards deposited on top of a solution of manganese acetate, transforming it into MnO_2 by a second pass of the laser. This electrode showed a high capacitance of 49 mF cm^{-2} and the symmetric MSC delivered a maximum energy density of $3.1 \mu\text{W h cm}^{-2}$ and a power density of 2.5 mW cm^{-2} . Following a similar approach but condensing the two laser irradiations into one, Liu *et al.*¹⁷³ and Xu *et al.*¹⁷⁴ pre-treated a polyimide film with metallic precursors, which became metallic oxides at the same time as the polymer transformed into LIG. An Fe_3O_4 NP decorated LIG MSC achieved an outstanding capacitance of 719 mF cm^{-2} , and an energy density of $60.2 \mu\text{W h cm}^{-2}$ at 0.3 mW cm^{-2} power density,¹⁷³ while LIG with Co_3O_4 achieved a much higher energy density of $286.38 \mu\text{W h cm}^{-2}$ at a similar power.¹⁷⁴ However, the achieved capacitance was much smaller, 1.9 mF cm^{-2} .

Regarding the application of DLW to reduce GO and fabricate LrGO MSCs, Shen *et al.*¹⁷⁵ used a femtosecond laser to define interdigitated LrGO regions with ultra-short spacing, reaching 6.3 mF cm^{-2} after accurately applying a gel electrolyte by laser-induced micro-droplet transfer. In a single step, Gholami *et al.*¹³³ laser reduced a mixture of GO and PANI after

having used a hard mask to define the IDE geometry of the drop cast slurry, achieving 72 mF cm^{-2} and a decent cycling stability of 93.5% at 0.7 mA cm^{-2} after 1000 cycles, which is still challenging for conductive polymers.

Using a hard mask to define the geometry of the device, both for contacts and active materials, is a very valid, scalable and useful method to fabricate MSCs. Couly *et al.*¹⁶⁰ spray coated rGO and MXenes on a flexible substrate using a hard mask, showing that the IDE geometry achieved had better power performance than its sandwiched counterpart. Chih *et al.*¹⁶⁴ used a screen printing method to deposit electrochemically exfoliated graphene and CNTs into IDEs and after adding a gel electrolyte achieved 7.7 mF cm^{-2} , with an excellent cycling stability >99% at 200 mV s^{-1} after 15 000 cycles. Functionalizing chemically graphene in solution was the approach used by Zhou *et al.*,¹⁵⁹ who filtered through a mask a solution of fluorine-doped electrochemically exfoliated graphene, which increased the capacitance of the device up to 17.4 mF cm^{-2} and reached an energy density and power density of $7.67 \mu\text{W h cm}^{-2}$ and 2.73 mW cm^{-2} , respectively. Again, using mask-assisted filtration, Xiao *et al.*¹⁶¹ fabricated MSCs made of exfoliated graphene, CNTs and PEDOT:PSS, on top of a pre-stretched substrate, which ensured good conductivity and porosity after relaxation. The device reached an exceptional capacitance of 107 mF cm^{-2} .

For the cases where hard masks were not used to define the geometry of these devices, laser etching arises as a versatile technique for research, as it allows quick modification of the design for optimization and rapid prototyping. In general, laser etched devices achieve very good electrochemical properties as the focus can be kept on optimizing the material, regardless of the future interdigitated geometry that will be applied. Shao *et al.*¹⁵² prepared a thin film of 3D cellular graphene which after laser ablation reached 2.7 mF cm^{-2} but with a high-power performance of 5.3 mW cm^{-2} . By patterning several CVD graphene monolayers *via* laser etching, Ye *et al.*¹⁵⁵ could exploit the edge effects of the geometry and achieved 2.8 mF cm^{-2} by adding PANI to the device. They also used their device to demonstrate an integrated energy harvesting sensor platform. Similarly, Yoo *et al.*¹⁵⁴ also exploited the edge effects of patterned heat-treated graphene oxide to fabricate a MSC with 40 mF cm^{-2} and an energy density of $5.4 \mu\text{W h cm}^{-2}$, outstanding values taking into account that no pseudocapacitive elements were added. By compositing an rGO film with polypyrrole and sputtering Au nanoparticles on top to ensure a good contact, Purkait *et al.*¹⁵⁶ reached an energy density of $11.82 \mu\text{W h cm}^{-2}$ and a power density of 2.36 mW cm^{-2} . By using ionic liquid electrolytes and increasing the voltage window up to 3 V, Gao *et al.*¹⁵⁷ produced an outstanding laser etched rGO supercapacitor with an energy density of $32 \mu\text{W h cm}^{-2}$ at a power density of 1 mW cm^{-2} .

Alternative techniques are always being developed,^{176,177} and the MSC field is growing constantly. By interference lithography, Kim *et al.*¹⁷⁸ patterned a photoresistive polymer, which after development and carbonization formed a conductive solid backbone with patterned pores for excellent electrolyte accessibility. In fact, the fabricated thick interdigitated MSC shows an outstanding power density of 165 mW cm^{-2} , with a high



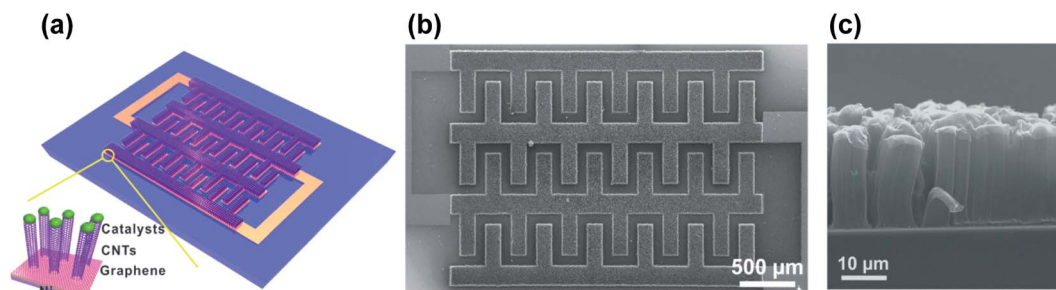


Fig. 9 (a) Schematic structure of a graphene-CNT MSC, grown on a Si substrate by chemical vapor deposition, with an inset showing the catalyst nanoparticles. (b) SEM image of the fabricated MSC, top view. (c) Cross-sectional SEM image of the graphene/CNT supercapacitor grown on a Ni catalyst/metal electrode. Reproduced with permission from ref. 180. Copyright 2012 American Chemical Society.

energy density of $17.75 \mu\text{W h cm}^{-2}$. Using MEMS-based Si etching techniques to provide a 3D support for capacitive carbon materials, as Li *et al.* proposed,¹⁷⁹ the fabricated supercapacitor reached remarkably high energy and power densities of $15.1 \mu\text{W h cm}^{-2}$ and 2.45 mW cm^{-2} respectively, due to a high RuO_x loading and an improved hierarchical carbon microstructure on Si. Fig. 9 show an example of a hierarchic graphene/CNT-based MSC composite produced on a Si/SiO₂ substrate.¹⁸⁰ This composite was grown directly by CVD from Ni interdigitated electrodes fabricated on the Si layer by conventional photolithography. In Fig. 9a, the scheme of the device design is shown. Fig. 9b shows a SEM image of a fabricated MSC and Fig. 9c a cross-sectional SEM image of the graphene/CNT structure, grown on a Ni catalyst/electrode layer by CVD.

MSCs were initially proposed to be integrated on-chip in Si-based platforms, using the CMOS technology for mass production.¹⁸¹ However, motivated by the rise of wearable technology, new improvements have emerged based on flexible, stretchable and bendable substrates. Thus, many of the techniques developed recently to fabricate MSCs have been applied to add a flexibility component to the performance of the device.³⁵ Together with gel-based, quasi-solid-state, and solid-state electrolytes, these devices have demonstrated to keep their performance after cyclability bending stress tests over

a large number of cycles. In Fig. 10a, several photographs show the bending of a supercapacitor at different angles.¹⁶³ The cyclic voltammograms shown in Fig. 10b, taken at different bending states, show that the rectangular shape is kept regardless of the bending angle of the device. The capacitance retention under the different bending angles is also complete, compared to the value in the flat state.¹⁶³ These developments pave the way for flexible supercapacitors as energy storage elements that can be easily integrated into wearable electronics.

To conclude this section, we provide Table 3, where MSCs synthesized by different processes and with/without added active materials to form a composite in the last few years are represented, together with the device performance and the corresponding references. We have ordered the MSCs in chronological order to track the trend over time.

3 Results and discussion: Ragone plots of the latest graphene-based supercapacitors

Graphene supercapacitors are rapidly evolving from laboratory prototypes to final devices that will complement or even perhaps compete with commercial batteries in the near future. This is because their properties and performance have greatly

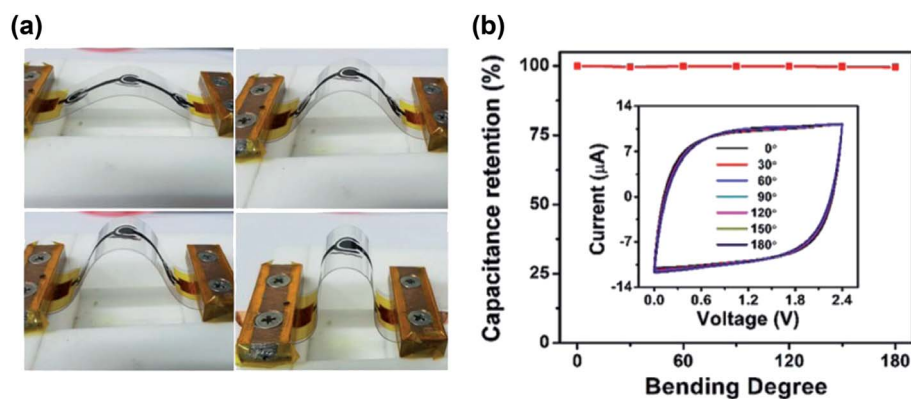


Fig. 10 (a) Photographs of the bending cycle of a flexible microsupercapacitor at different angles. (b) Capacitance retention graph across several bending angles. The inset shows the cyclic voltammetry curves of the device at those angles, showing a very stable performance. Reproduced with permission from ref. 163. Copyright 2019 Royal Society of Chemistry.



Table 3 Summary of MSCs

Synthesis method	Active materials	Capacitance (mF cm ⁻²)	Energy density (μW h cm ⁻²)	Power density (mW cm ⁻²)	Retention (%) after a number of cycles	Year	Ref.
Spray coating through a mask	rGO and Ti ₃ C ₂ T _x	2.4 at 2 mV s ⁻¹	0.26	0.01	97% after 10 000 at 0.2 mA cm ⁻²	2017	160
Freeze-casting assisted infiltration assembly method and laser ablation	Cellular graphene	2.47 at 5 mV s ⁻¹	0.34	5.3	97.6% after 10 000 at 1 mA cm ⁻²	2017	152
Electrodeposition	rGO and MnO _x	1.63 at 0.3 mA cm ⁻²	0.27	0.19	85% after 1000	2018	166
Electrodeposition and laser ablation	rGO, PPY and nanoporous Au	29.21 at 0.07 mA cm ⁻²	11.72	2.36	85.9% after 10 000 at 5 mA cm ⁻²	2018	156
MPECVD** and Si-based microfabrication techniques	Graphene nanowall and RuO ₂	109 at 5 mV s ⁻¹	15.10	2.49	90.2% after 3000 at 50 mV s ⁻¹	2018	179
Screen-printing with graphene ink	Screen-printed graphene	0.89 at 5 mV s ⁻¹	0.91	0.15	91.8% after 10 000 at 0.2 mA cm ⁻²	2018	163
DLW	LIG	25.1 at 0.05 mA cm ⁻²	4	8	98.5% after 12 000 at 2 mA cm ⁻²	2018	119
Interference lithography and plasma etching	Boron-doped porous carbon	7.15 at 100 mV s ⁻¹	17.75	165	85% after 12 000 at 5000 mV s ⁻¹	2018	178
Mask-assisted filtration of ink	Fluorine doped graphene	17.4 at 1 mV s ⁻¹	7.67	2.73	93% after 5000	2018	159
DLW	LIG	6.1 at 20 mV s ⁻¹	0.96	0.25	—	2018	169
CVD and laser ablation	Graphene	45.2 at 10 mV s ⁻¹	0.04	3.17	—	2018	155
Mask-assisted filtration of ink	Graphene, CNTs and PEDOT	107.5 at 5 mV s ⁻¹	1.27	0.12	—	2018	161
DLW	LrGO	6.3 at 1 mV s ⁻¹	0.54	1.22	100% after 1000 at 500 mV s ⁻¹	2018	175
Biomimetic microfluidic fabrication	rGO and metallic MoS ₂ microfibers	48.4 at 1 V s ⁻¹	0.42	0.00	97% after 10 000	2018	176
Conventional lithographic techniques and laser ablation	rGO	40 at 10 mV s ⁻¹	5.40	1.48	83.5% after 10 000 at 100 mV s ⁻¹	2018	154
Freeze-drying and laser ablation	rGO and Ti ₃ C ₂ T _x aerogel	34.6 at 1 mV s ⁻¹	2.18	0.06	91% after 15 000 at 2 mA cm ⁻²	2018	153
Ion-liquid reduction and laser ablation	rGO and carbon paper	27.4 at 0.7 mA cm ⁻²	32.10	0.18	—	2019	157
	Graphene and CNTs	7.7 at 5 mV s ⁻¹	27.40	1.05	> 99% after 15 000 at 200 mV s ⁻¹	2019	164
Screen-printing	LIG and Ni-catecholate MOF***	15.2 at 0.2 mA cm ⁻²	1.07	0.32	87% after 5000 at 2 mA cm ⁻²	2019	170
DLW	nanorods	—	4.1	7	—	2019	170
Slurry coating and laser ablation. Electrodeposition.	n-doped graphene and MnO ₂	13.3 at 0.25 mA cm ⁻²	5.88	1.13	84% after 5000 at 2 A g ⁻¹	2019	158
DLW	LIG and MnO ₂	48.9 at 10 mV s ⁻¹	3.1	2.5	94.3% after 3000 at 0.5 mA cm ⁻²	2020	168
DLW	LIG and Fe ₃ O ₄ nanoparticles	719.28 at 1 mV s ⁻¹	60.2	0.3	74% after 900 at 20 mV s ⁻¹	2020	173
DLW	LIG and Co ₃ O ₄	10.9 at 5 mV s ⁻¹	286.38	0.75	97.8% after 10 000 at 0.08 mA cm ⁻²	2020	174
	LIG and PPY	2412.2 at 0.5 mA cm ⁻²	10.94	0.7	95.6% after 10 000 at 2.5 mA cm ⁻²	2020	172
DLW	LIG and red-mud	203 at 0.1 mA cm ⁻²	18	0.66	81% 4000 at 0.3 mA cm ⁻²	2020	171
Drop casting over carbon fiber. Electrodeposition	rGO, PEDOT:PSS and MnO ₂	550 at 3 mA cm ⁻²	2	1.36	95% after 5000 at 12 mA cm ⁻²	2020	167
	EG + CNT + rGO/PPY	38 at 1 mV s ⁻¹	295	2.9	82% after 4500	2020	162
Mask-assisted deposition	GO, graphene aerogel and MnO ₂	14.2 at 10 mV s ⁻¹	150	14.1	85% after 25 000	2020	177
Slurry coating on an imprinted substrate	—	—	0.306	0.135	—	—	—
—	—	—	1.2	0.043	—	—	—
—	—	—	0.66	0.436	—	—	—



improved over the last decade. The three technologies that have contributed the most to their development, namely, graphene foam, laser reduction and microsupercapacitors, have been reviewed in detail and are summarized below.

Graphene foam made by CVD from sacrificial templates enables making high capacity electrodes for large area supercapacitors, with a demonstrated gravimetric capacitance of up to 2585 F g^{-1} (ref. 100) and a capacitance retention of 96.5% after 10 000 cycles.⁹⁰ Graphene provides high electrical conductivity and its porous structure presents a large surface area and allows an easy transit of electrolyte ions. In this way, a light and flexible 3D electrode can be obtained. DLW represents a low-cost, fast, and single step technique to produce large area and flexible electrodes based on laser reduced GO or laser induced graphene. Despite achieving electrodes with very good capacitance retention (see Table 2), the laser wavelength and processing parameters required to produce supercapacitors with higher specific capacitance than those using graphene foam fabricated by CVD have not been optimized yet. All these graphene forms, namely, graphene foam, LrGO and LIG, have an empty pore area that can be functionalized with various pseudocapacitive materials. The most widely used so far are metallic oxides and conductive polymers, although recently

transition metal sulfides are being explored to push further the achievable power density, cycling stability and operating voltage window.

Miniaturized supercapacitors, or MSCs, are becoming increasingly popular due to their integration possibilities. The rise of wearable electronics is driving this approach to grow rapidly, and scaling down the supercapacitor technology to the microscale has revealed some advantages. The use of IDEs eases ion accessibility, improving power performance, and gel and solid-state electrolytes allow these devices to be easily encapsulated in bendable or stretchable modules. Electrodes can be fabricated by scalable, cheap and quick processes such as screen printing or drop casting, and can be grown on top of or transferred to almost any target substrate. Laser writing has emerged recently as a quick and easy way to fabricate supercapacitor electrodes from inexpensive materials and to pattern them easily into interdigitated microsupercapacitor electrodes. This flexibility is ideal for their integration into the growing industry of wearable electronics, where MSCs are expected to play a crucial role in the future.

As the Ragone plot in Fig. 11a shows, the energy and power density of new supercapacitor devices are already approaching energy density performances that were typically restricted to

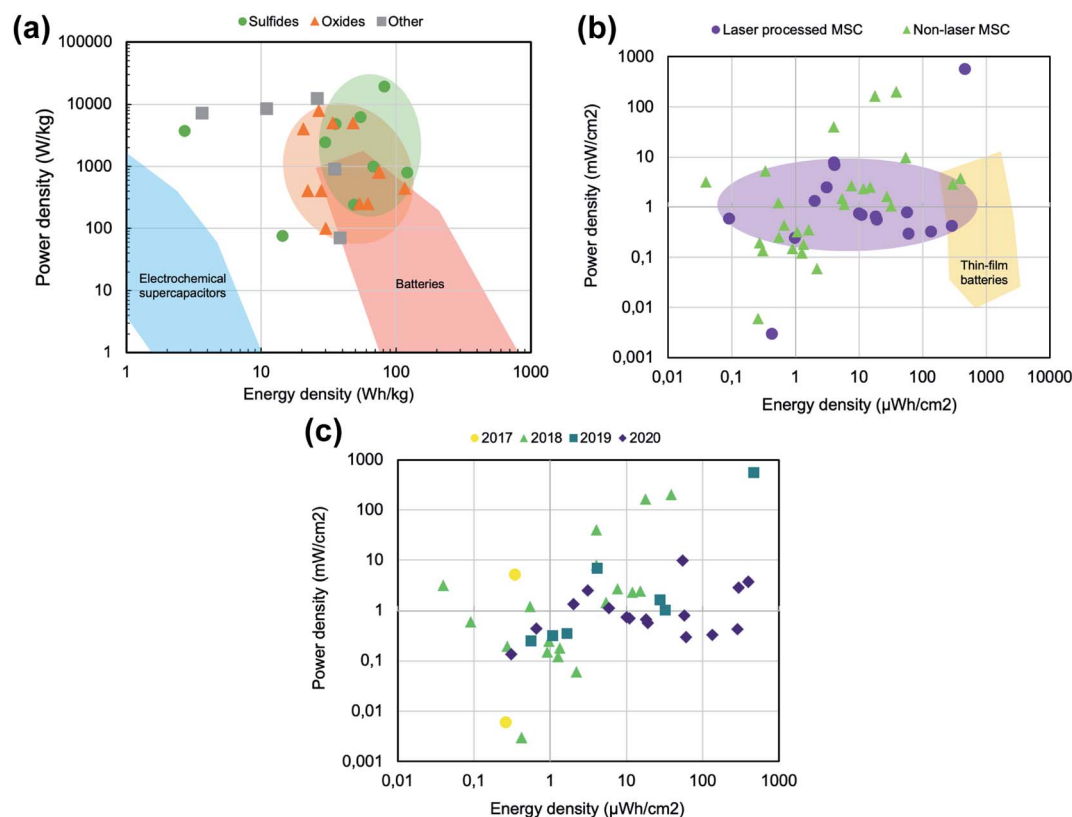


Fig. 11 (a) Schematic gravimetric Ragone plot where the typical working regions for commercial supercapacitors and batteries are highlighted. Each dot represents one working point of a graphene-foam based device classified by the type of its active pseudocapacitive material, green for sulfides, orange for oxides and grey for other compounds, such as metal hydroxides or metallic nanoparticles. (b) Schematic areal Ragone plot. Each dot represents one thin-film device reported in the literature, purple for laser processed micro-supercapacitors and green for non-laser microsupercapacitors. The yellow region represents the typical working point of thin-film batteries as reported by ref. 182. (c) Schematic areal Ragone plot of the same data used in Fig. 11b, classified by the publishing date.



batteries, but with better power performance. Each dot in this graph represents an energy-power working point of the fabricated devices included in Table 1. Most of these devices have been fabricated using graphene foam of CVD graphene or rGO, but are always complemented with pseudocapacitive elements. CVD graphene shows a more open-pore interconnected network and is of very high quality, while several key properties depend to great extent on the metallic foam template on which it is grown, such as the surface area, or the pore size and distribution. On the other hand, rGO typically creates compact 3D porous structures with smaller pores and a higher surface area, while being less accessible to the electrolyte and having worse electrical properties than CVD graphene. However, our comparison shows that both CVD graphene and rGO are very competitive materials for the fabrication of supercapacitor devices, showing comparable results in the Ragone plot shown in Fig. 11a. However, differences regarding the pseudocapacitive elements included in these devices are more clearly highlighted in that same graph. The recent literature has reported a large number of devices that use, in order to achieve the best performance, mostly bimetallic oxides and sulfides, as well as other materials including hydroxides, as pseudocapacitive elements. The use of metallic and bimetallic sulfides (green dots) has been showing the best results when compared to their oxide counterpart (orange triangles), and these devices show already a comparable, if not better, working point to that stated by Simon and Gogotsi for batteries in their recent review of the field.⁵ It is important to note that the red area of Fig. 11a and the blue area, taken from the Simon and Gogotsi article, only include commercial devices, and the dots are exclusively plotted from the data of research papers published during the last 3 years. This shows the strength of the supercapacitor technology, which can not only complement, but also compete directly with batteries regarding energy density and power density, showing much better cycling stabilities and being fabricated from materials less scarce than lithium.

This gravimetric classification includes graphene foam-based devices, which typically are reported using per-kg metrics. However, for thin film devices such as micro-supercapacitors, figures-of-merit are typically given in areal or volumetric terms, as they are fabricated with thicknesses in the range of a few μm . Its mass is not really the defining factor limiting its performance, but its area. Fig. 11b shows this classification in an areal Ragone plot, where every dot represents one energy-power working point of a fabricated micro-supercapacitor device as reported in the literature (references shown in Tables 2 and 3). There is growing interest in laser processing technology, due to its multiple advantages regarding flexibility, speed and low-cost. In the past two years, the number of papers published using laser writing to process supercapacitor materials has grown a lot, almost monopolizing all recent research in the field. Most of the papers found in the literature which used non-laser processes to fabricate micro-supercapacitors were mostly published in 2018 and before, using techniques such as mask-assisted filtration, screen printing or standard lithography to define the interdigitated geometries. However, from 2019, mostly laser-processed

micro-supercapacitors have been found. This classification of the results by year can be seen in Fig. 11c.

Regarding electrochemical performance, and despite all this recent interest, laser processed MSCs, shown as purple dots in Fig. 11b, show a saturation of the power density in the 10 mW cm^{-2} range, going even lower for the most energy dense performances, towards the 1 mW cm^{-2} limit. This could be due to the lower quality of the materials that can be achieved through laser processing, be it laser induced graphene from polymeric films or laser-reduced graphene oxide from graphene oxide films. However, the energy density of these devices has been steadily increasing since 2017, and some recent devices lay in a working range very close to that of thin film batteries, devices comparable to micro-supercapacitors in their metrics. By compositing these laser-processed graphene materials with pseudocapacitive elements, for example Co_3O_4 (ref. 174) or Fe_3O_4 (ref. 173), the energy density is being pushed towards the thin film battery region, which are the best performing devices in terms of areal energy density nowadays.¹⁸²

4 Conclusion and outlook

In summary, to move the supercapacitor and MSC field forward, DLW processing has recently emerged as a very promising technique. This technique allows quick, one-step processing to create a highly porous and conductive 3D network of graphene sheets, using commercial laser systems. Building up from technologies such as 3D printers and laser cutters, these systems demonstrate great scalability and increasing precision, as well as industrial availability. Furthermore, structuring these materials into IDEs helps electrolyte accessibility and could allow for better power performance. However, most of the recent work in the field has focused on increasing its energy density towards the thin-film battery range, and it will be a challenge for the following years to further increase the power density. On top of this, synergies are appearing and can be exploited, as some studies suggest the advantages of using laser processing for both the graphene material and the pseudocapacitive element. This combined process can, for example, add oxygen vacancies to metal oxides or transform through photochemistry conductive polymers, which could be used together with graphene materials fabricated using new laser 3D printer systems. However, there are still some challenges that need to be overcome for the widespread use of graphene-based supercapacitors. First, the large-scale fabrication of porous graphene materials with high quality and uniformity must be ensured. Optimization is still needed in several areas, such as the intimate contact and adherence of pseudocapacitive elements on the graphene network, or the choice of an ideal pseudocapacitive material and its possible synergies with the electrolyte for an optimized voltage window and cyclability. Minimizing the agglomeration and restacking of the graphene sheets is a problem present in all-carbon EDLCs that must be addressed, while in the case of pseudocapacitors, it can be done by intercalating the graphene material with active nanoparticles.

If the expectations are met, graphene supercapacitors and micro-supercapacitors are meant to become a competitive



complement, or even alternative, to traditional lithium ion batteries and thin film batteries, as well as an essential component in new wearable and portable electronic devices.

Conflicts of interest

There are no conflicts of interest to declare.

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