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Towards scalability: progress in metal oxide charge transport layers for large-area perovskite solar cells

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Metal oxides are emerging as prominent materials for the charge transport layer (CTL) because of their low fabrication cost, solution processability, robust material stability, and flexibility in material modification. In the construction of perovskite solar cells (PSCs), the CTL plays a pivotal role by facilitating the extraction of charge carriers photogenerated from the perovskite layer. For large-area PSCs, there is a need for scalable fabrication to achieve uniform and optimized CTLs. While PSCs have already demonstrated power conversion efficiencies (PCE) exceeding 26% at a laboratory scale, scaling up their production remains a significant challenge, thus impeding their commercial viability. To date, several researchers have made strides in developing scalable PSCs using both solution processes and vacuum deposition techniques. However, there is a pressing need for more intensive research and development. The primary goal is to fabricate CTLs that are not only efficient but also well-defined, uniform, homogeneous, and minimally defective over large areas. Therefore, in this review, we discuss the most promising metal oxide CTLs for large-area PSCs, emphasizing SnO₂, TiO₂, and NiO_x, and highlight their material processing methods. Additionally, we explore emerging strategies such as bilayer techniques, interfacial modification, and additive engineering, which aim to address and mitigate the macro/micro defects that invariably arise during the up-scaling process.

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1. Introduction

Perovskite solar cells (PSCs) have achieved power conversion efficiencies (PCE) exceeding 26% on a laboratory scale.^{1,2} Beyond their high efficiency, PSCs are gaining attention as

next-generation solar cells due to their cost-effective solution-processing methods and the potential for fabricating flexible devices.³ However, the commercialization of PSCs faces several challenges, including thermal,⁴ moisture,⁵ and light-induced instabilities,^{6,7} coupled with difficulties in the up-scaling process. There is an ongoing need to establish a reliable and scalable device fabrication process.

In general, PSCs are characterized by two primary device structures, commonly referred to as n-i-p and p-i-n. The n-i-

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p structure is typically constructed in the following sequence: transparent conductive oxide (TCO), electron transport layer (ETL), light absorber layer (perovskite layer), hole transport layer (HTL), and then the electrode. In contrast, the p-i-n structure is assembled in a reverse order, starting with the TCO, followed by the HTL, light absorber layer, ETL, and electrode. As highlighted by the n-i-p and p-i-n configurations, charge transport layers (CTLs, which include both ETL and HTL) are indispensable components, and these layers play a key role in the operation of PSCs. CTLs facilitate the extraction of photogenerated charge carriers from the perovskite layer,^{8,9} a process that aids in mitigating V_{OC} and fill factor (FF) losses. Furthermore, when a CTL is deposited onto a perovskite film, it acts as a barrier against moisture and oxygen, thereby enhancing device stability. In efforts to protect perovskite films, metal oxides, notably NiO_x , are often utilized to mitigate potential damage arising from deposition methods, especially the sputtering process for TCO.¹⁰ Therefore, the precise fabrication of CTLs is crucial for enhancing both the performance and stability of PSCs.

A variety of materials, including organic compounds, polymers, and metal oxides, have been explored as CTLs. Among these, metal oxides are emerging as especially promising candidates. Their suitability largely stems from their advantageous optoelectrical properties: robust material stability, transparency, a favorable band alignment with the perovskite for adept charge extraction, material modification flexibility, low fabrication cost, and solution processability. Owing to their compelling attributes, intensive research efforts have been dedicated towards developing high-quality metal oxide-based CTLs, emphasizing aspects such as morphology control, fabrication process refinement, and property optimization.

For ETL, n-type metal oxides including Nb_2O_5 , In_2O_3 , WO_x , ZnO , TiO_2 , and SnO_2 have been extensively studied.¹¹⁻¹⁷ Among these, in earlier studies, ZnO was often chosen due to its physical attributes that closely resemble those of TiO_2 , yet

with the added advantage of superior electron mobility. Furthermore, ZnO has potential in flexible PSCs,¹⁸ given its annealing temperature is below the deformation temperatures of many flexible substrates. However, challenges arise as the ZnO surface, being more basic than the perovskite, tends to extract H^+ from organic cations in the perovskite, leading to its decomposition.¹⁹ Moreover, the hydroxyl groups of the ZnO surface break the ionic interactions of the perovskite, subsequently leading to its degradation.¹⁷ Due to these significant drawbacks, the application of ZnO in PSCs has seen a decline in recent times. Regarding TiO_2 , since its introduction in the initial reports on PSCs, it has consistently been a preferred choice for ETL, having achieved an impressive certified PCE of 25.2%.²⁰ However, challenges associated with TiO_2 include its tendency for the photocatalytic decomposition of the perovskite under UV light and its requirement for sintering temperatures above 500 °C.²¹ Recently, SnO_2 has emerged as a promising alternative ETL, attributed to its advantageous properties such as a lower annealing temperature, high carrier mobility, and a band alignment that pairs well with the perovskite.²² Notably, PSCs utilizing SnO_2 have attained a record-breaking certified PCE of 25.7%.¹ N-type organic ETLs, such as fluorene (C_{60}) and phenyl- C_{61} -butyric acid methyl ester (PCBM), have been predominantly utilized in the p-i-n structure due to their high electron mobility and the advantage of low-temperature fabrication. Nevertheless, few studies have explored the use of C_{60} and PCBM as bottom CTLs in n-i-p configurations, and these efforts have often resulted in lower efficiencies compared with devices based on metal oxides. In contrast, organic HTLs, including self-assembly monolayers (SAM), small molecules, and polymers including Spiro-OMeTAD and PTAA, which exhibit moderate hole conductivity, have been investigated. These materials are advantageous because of their straightforward application methods, such as immersion coating, spin-coating, and drip-casting. However, their instability under light and heat remains a crucial issue that needs to be addressed. For HTL based on oxides applications, p-type metal oxides such as CuO and NiO_x have been explored. Notably, NiO_x has been extensively used owing to its appropriate band alignment for hole extraction, its deep valence band that contributes to a high open-circuit voltage (V_{OC}), and its wide bandgap (ranging from 3.4 to 4.0 eV) which ensures high transparency. Due to the inherently insulating properties of stoichiometric NiO , significant research has been invested in its modification to achieve higher conductivity. As a result, PSCs that employ modified NiO_x have achieved state-of-the-art performances, with PCEs surpassing 24%.^{23,24}

As abovementioned, much of the research to date has focused on enhancing the properties of metal oxides and optimizing their structures, and numerous reviews have extensively discussed and provided the advancements in this area. Recently, as interest in large-area perovskite fabrication has expanded, efforts to produce large-scale charge transport layers have significantly intensified. However, the preparation of large-area CTLs presents several challenges. For solution-processed CTLs, issues such as poor wetting properties



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between the substrates and the precursor solution, inhomogeneous evaporation rates over expansive areas, and sintering effects during heat treatment need to be addressed. Conversely, the vacuum process faces hindrances due to its high cost and energy consumption, both of which pose significant challenges for commercialization. Therefore, there is a pressing need for comprehensive review articles that address advancements and challenges in this domain. However, related literature on the topic remains sparse. In this work, as shown in Fig. 1, we aim to provide a concise overview focused on metal oxide CTLs for large-area PSCs, highlighting recent progress in deposition methods, modification strategies, and their adaptability to large-area device applications.

2. Recent progress in deposition methods

2.1. Solution processed method

Solution processing offers a cost-effective, low-energy-consuming, and straightforward approach to deposit metal oxides for CTLs compared with other techniques such as sputtering,^{25–27} electron beam evaporation,²⁸ atomic layer deposition (ALD),^{29,30} and pulsed laser deposition.^{31,32} Considering these advantages, numerous researchers have attempted to develop efficient solution-processed methods tailored for large-area CTL applications. Various solution-based deposition methods have been employed (Fig. 2), including spin coating, bar coating, spray coating, and chemical bath deposition (CBD). These solution processes can be broadly categorized into two approaches. The first involves coating a metal oxide thin film by spreading a solution containing either nanoparticles (NPs) or dissolved precursors, followed by drying and annealing. The second approach involves growing metal oxides in a chemical bath consisting of multiple reactants and precursors, requiring supplementary energy inputs, such as thermal or electrical energy. Among the methods, spin coating is the most frequently used for CTL fabrication due to its simplicity, especially for small-area cell production. However, its suitability for large-area CTL applications is often challenged,

mainly due to the non-uniform film thickness resulting from centrifugal forces, potentially hindering optimal charge transport. Furthermore, spin coating approaches can lead to various morphological defects, including pinholes, partial voids, and inconsistent thickness.³³ These defects can often be attributed to variations in the surface energy and the intrinsic roughness of TCOs, including fluorine-doped tin oxide (FTO) and indium tin oxide (ITO). Despite these limitations, spin-coated TiO₂-based perovskite mini-modules (n-i-p devices) achieved a world-record certified PCE of 22.87% in 2022 using single crystalline TiO₂ and remain in frequent use.³⁴ In the following parts, we reviewed various solution process for up-scaling and strategies for large-area spin coating. In the sections that follow, we explore a range of solution processes geared towards up-scaling, along with strategies specifically designed for large-area spin coating.

2.1.1. Solution processed ETL. To address the issues associated with spin coating for large-area ETL, Mandati *et al.* introduced bar coating for c-TiO₂ and mp-TiO₂, subsequently comparing their optical properties and photovoltaic performances with those of spin-coated TiO₂.³⁵ It is worth noting that c-TiO₂ is commonly deposited *via* spray pyrolysis using a precursor solution that dissolves titanium diisopropoxide bis(acetylacetonate) in an alcohol solvent. Conversely, mp-TiO₂ NPs are primarily synthesized through hydrothermal methods and applied using spin coating. Bar coating involves rolling a bar to spread the solution uniformly across the surface of the substrate. This method is straightforward, cost-effective, and scalable. For analysis, substrates sized 50 mm × 50 mm were divided into 9 sections, differentiating between the central and corner positions. Both bar-coated and spin-coated mp-TiO₂ displayed almost similar porous morphologies and particle sizes. However, in the 2D mapping of solar weighted average transmittance values, the bar-coated mp-TiO₂ exhibited a uniform transmission distribution, whereas the spin-coated samples displayed non-uniformity, particularly near the corners (Fig. 3a). Photoluminescence (PL) analyses also exhibi-



Fig. 1 Schematic PSC structure and illustration of requirements toward large-area CTL.

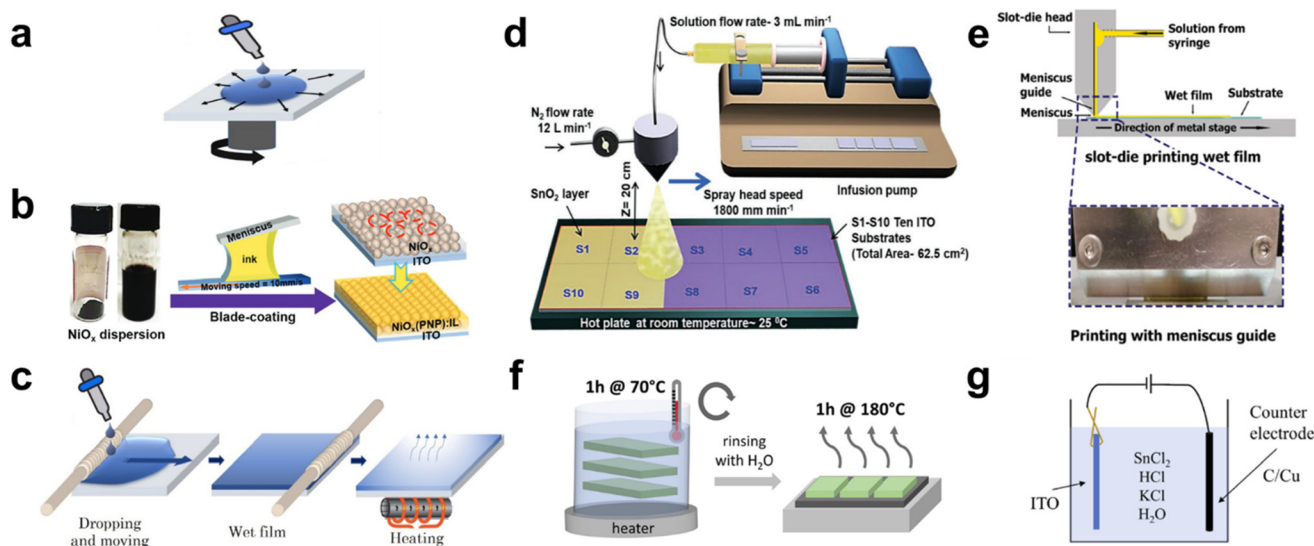


Fig. 2 Illustrations of scalable solution process for metal oxide CTL. (a) Image for spin coating. Reproduced with permission.³⁵ Copyright 2022, Elsevier. (b) Image for blade coating. Reproduced with permission.⁵³ Copyright 2021, Wiley-VCH. (c) Image for bar coating. Reproduced with permission.³⁵ Copyright 2022, Elsevier. (d) Image for spray coating. Reproduced with permission.³⁷ Copyright 2022, Wiley-VCH. (e) Image for slot-die coating. Reproduced with permission.⁵⁴ Copyright 2022, Wiley-VCH. (f) Image for chemical bath deposition (CBD). Reproduced with permission.⁴⁴ Copyright 2022, American Chemical Society. (g) Image for electron chemical deposition (ECD). Reproduced with permission.⁴⁶ Copyright 2023, Royal Society of Chemistry.



Fig. 3 (a) 2D spatial mapping of solar weighted transmittance values (T_{sw}) and PSC performance homogeneity of spin-coated and bar-coated $mp\text{-TiO}_2$, respectively. Reproduced with permission.³⁵ Copyright 2022, Elsevier. (b) Photograph of $\text{SnO}_2\text{-TiO}_2$ hybrid solution, schematic energy level diagram of ETLs, and J - V curve of flexible $7 \times 7 \text{ cm}^2$ mini-modules with its photograph. Reproduced with permission.³⁶ Copyright 2022, American Chemistry Society. (c) Atomic force microscope (AFM) images and schematic energy level diagram of spin-coated and spray-coated SnO_2 , respectively. Reproduced with permission.³⁷ Copyright 2021, Wiley-VCH. (d) Illustration of CBD deposition (left) and J - V curves of the optimized mini-modules (right). Reproduced with permission.⁴⁴ Copyright 2022, American Chemistry Society. (e) Illustration of the electrochemical regulation method (left) and J - V curves of thermal annealed and electrochemically regulated SnO_2 -based mini-modules (right). Reproduced with permission.⁴⁵ Copyright 2022, American Chemistry Society. (f) Schematic energy level and Sn^{4+} proportion of SnO_2 ETLs prepared in different pH. (The Sn^{4+} proportion was calculated based on the XPS results.) Reproduced with permission.⁴⁶ Copyright 2023, Royal Society of Chemistry.

ted greater spatial homogeneity in the bar-coated mp-TiO₂ relative to their spin-coated counterparts, regardless of their position on the substrate. When evaluating the performance homogeneity of PSCs, devices with spin-coated mp-TiO₂ showed a gradual decline in efficiency towards the corners, whereas those with bar-coated mp-TiO₂ maintained relatively consistent efficiency across the substrate. In summary, the comparison highlighted the non-uniform optical properties and photovoltaic efficiency of spin-coated samples, contrasting with the more consistent performance of bar-coated samples for large-area CTLs.

In a recent study on large-area ETLs implemented through spin coating, it was revealed that the SnO₂-TiO₂ hybrid ETL facilitates the development of efficient, flexible perovskite mini-modules using this coating technique.³⁶ This hybrid ETL was synthesized by incorporating a TiO₂ nanosol, which acts as an inorganic binder, into a SnO₂ colloidal solution. Notably, the TiO₂ nanosol comprises stably dispersed crystalline TiO₂ NPs with dimensions smaller than 10 nm. An optimal balance was found when 5 wt% of TiO₂ was added to a 3 wt% SnO₂ colloidal solution, resulting in a peak efficiency of 21.88% in small cells. UPS findings indicated that an elevation in the energy level of the SnO₂-TiO₂ hybrid ETL led to an advantageous energy level alignment, thus driving a higher V_{OC} value (Fig. 3b). Consequently, while the mini-module employing solely the SnO₂ ETL displayed markedly low V_{OC} and FF values, the mini-module based on the SnO₂-TiO₂ hybrid ETL achieved a PCE of 16.62% over an active area of 15 cm². The pronounced efficiency variation across large areas between the SnO₂ and SnO₂-TiO₂ ETLs indicates that the hybrid ETL offers a more consistent layer compared with the SnO₂ ETL. This consistency is likely attributed to the effective role of the TiO₂ nanosol as an inorganic binder. Additionally, in a bending resistance test with a bending radius of 5 mm, while SnO₂-based f-PSCs showed ~90% of the initial PCE after only 200 bending cycles, TiO₂-SnO₂-based f-PSCs maintained ~90% of the initial PCE after 500 bending cycles.

Ding *et al.* reported a study on single-crystalline TiO₂ (SC-TiO₂) rhombus-like NPs with exposed (001) facets.³⁴ These were synthesized through a one-step solvothermal method and employed as a large-area ETL. Impressively, the study reported a high certified efficiency of 22.72% over an active area of 24.63 cm². The authors suggested that SC-TiO₂ possesses superior conductivity and crystallinity, reduces interfacial charge recombination, and mitigates interfacial charge accumulation compared with commercial mp-TiO₂. Furthermore, SC-TiO₂ exhibited a decreased trap density and heightened electron mobility relative to mp-TiO₂. Based on these advantages, devices utilizing SC-TiO₂ achieved an enhanced fill factor exceeding 84%, ensuring efficient performance in both small-sized cells and mini-modules. Furthermore, they sustained roughly 90% of their initial efficiency over a span of 1400 hours. Consequently, TiO₂ NPs with high crystallinity enable the fabrication of efficient and stable perovskite mini-modules, even when employing spin coating.

Spray coating offers another avenue for enhancing the scalability of CTL. Kumar *et al.* reported on the potential of room-temperature spray deposition for the fabrication of large-area SnO₂ ETL-based PSCs.³⁷ They successfully demonstrated that a SnO₂ film measuring 62.5 cm² could be deposited using a rapid, multi-pass spray deposition technique at room temperature. The morphological, surface analytical, electrical, and device properties of SnO₂ were compared between the spin-coated and spray-coated methods, using commercial colloidal SnO₂ dispersions. The spray-coated SnO₂ exhibited a granular structure, indicative of a mesoporous morphology (Fig. 3c). This structure is advantageous for interfacial charge transfer, enhancing the contact area between the ETL and the perovskite.³⁸ Notably, such a granular structure was absent in the spin-coated SnO₂. From the XPS analysis, the spray-coated SnO₂ film exhibited fewer oxygen defects, reduced C-C containing compounds, and a higher concentration of K ions compared with the spin-coated SnO₂ film. The presence of K ions can reduce the defect density at the interface due to their passivation effect, promoting efficient interfacial charge transfer. To assess the performance of scaled-up SnO₂ films in PSCs, substrates measuring 2.5 × 2.5 cm² were sectioned into 10 pieces following the deposition of SnO₂ on 62.5 cm² ITO substrates. The highest efficiency for devices with spin-coated SnO₂ was 16.01% over an active area of 1.0 cm², whereas devices with spray-coated SnO₂ achieved an efficiency of 18.90%. Furthermore, the spray-coated SnO₂ devices across all 10 sections demonstrated remarkably consistent efficiencies, ranging narrowly between 19.11% and 20.08% in the active area of 0.096 cm². This study indicates that the spray-coating method not only produces high-quality SnO₂ with improved charge extraction and reduced recombination at the ETL/perovskite interface, yielding a lower trap density compared with spin-coated SnO₂, but also offers advantages in scalability.

Taheri *et al.* reported the performance of SnO₂-based perovskite solar modules, which were fabricated on flexible substrates utilizing the spray-coating method.³⁹ Using water-based SnO₂-NP dispersions in the spray coating method, they successfully deposited uniform and compact SnO₂ films over a 120 cm² area on flexible plastic substrates. These modules yielded power conversion efficiencies (PCE) of 12.0% and 11.7% for active areas of 16.8 cm² and 21.8 cm², respectively.

Zhang *et al.* explored the potential of a SnO₂/SnO_x bilayer ETL for large-area perovskite modules, employing ultrasonic spray coating at a low temperature (≤100 °C).⁴⁰ For a well-defined large-area SnO₂ layer using the spray coating method, they fine-tuned parameters such as substrate temperature, precursor flow rate, and co-solvent volume ratio. They further prepared a SnO₂/SnO_x bilayer by spraying a sol-gel of SnCl₂ onto SnO₂ to fill the voids and gaps in the SnO₂ NP film. Notably, the residual chloride in the spray-coated sol-gel SnCl₂ enhanced the Cl passivation effect, leading to improvements in both V_{OC} and J_{SC}. While this work did not specify the efficiency of the solar module, the high V_{OC} of 6.1 V in modules with a 2.1 cm² aperture area indicates that the SnO₂/SnO_x ETL exhibited good uniformity and homogeneity.

In the deposition methods employing colloidal solutions as described above, achieving a uniformly thick layer across large-area substrates is challenging due to the “coffee-ring effect”.⁴¹ This effect arises from varying solvent evaporation rates throughout the droplets,⁴² leading to capillary outflow. Optimizing coating conditions and refining solvent engineering to mitigate the unwanted coffee-ring effect requires significant effort. However, by directly growing a film on the substrate instead of using a colloidal solution, a more uniform film can be achieved, circumventing the challenges associated with the coffee-ring effect.

Recently, PSCs utilizing CBD SnO₂ have achieved a remarkable certified power conversion efficiency of 25.2% in a small area, utilizing a straightforward method that incorporates urea, tin(II) chloride dihydrate, and acidic materials.⁴³ Subsequently, the CBD method has emerged as a promising approach for depositing SnO₂, not only for highly efficient PSCs but also for achieving uniform films over large areas.

Zimmermann *et al.* attempted the fabrication of perovskite mini-modules using CBD SnO₂ as the ETL. CBD SnO₂ samples were prepared with 1, 2, and 3 cycles for 1 hour at 70 °C.⁴⁴ This was due to the finding that a single cycle of the CBD reaction did not reliably yield an efficient ETL, primarily due to incomplete coverage of the FTO surface. The optimal reaction was achieved in 2 cycles, resulting in an efficiency of 19.2% in small cells. For uniform mini-module fabrication, the perovskite and HTL layers were prepared using slot-die coating. The PCEs of the mini-modules reached 16.0% and 17.0% for aperture areas of 6.0 × 2.0 cm² and 16.0 × 2.5 cm², respectively (Fig. 3d).

Interestingly, Bai *et al.* developed an innovative and efficient annealing-free strategy for SnO₂ through simple electrochemical regulation.⁴⁵ This method achieved an impressive mini-module PCE of 21.3% over an active area of 10 cm² (Fig. 3e). The as-deposited CBD SnO₂ was subjected to both thermal annealing and electrochemical regulation, termed TA-SnO₂ and ER-SnO₂, respectively. For TA-SnO₂, the PCE decreased above 200 °C even though the conductivity increased with rising temperatures. The conduction band minimum (E_{CBM}) of TA-SnO₂, which shifted from -4.23 to -4.69 eV, did not align well with that of the perovskite. This indicates that the conductivity enhancement from heat treatment could not compensate for the energy loss due to obstructed electron transport. In contrast, through electrochemical regulation at room temperature, the E_{CBM} of the ER-SnO₂ can be modulated, leading to an enhanced band alignment in PSCs and subsequently improved the carrier extraction. Moreover, ER-SnO₂ exhibited a higher carrier concentration and increased conductivity, attributed to the K ion doping effect during electrochemical regulation. Time-resolved photoluminescence (TRPL) results revealed that electrochemical regulation facilitates electron extraction at the perovskite/ETL interface and reduces radiative recombination. Consequently, all photovoltaic parameters of the mini-module with ER-SnO₂ surpassed those of TA-SnO₂-based devices, suggesting that electrochemical regulation holds promise for efficient large-area PSCs.

Beyond the CBD method, Sun *et al.* were the first to report on the electrochemical deposition (ECD) of polycrystalline SnO₂ at room temperature for large-area perovskite solar cells.⁴⁶ By controlling the pH of the electrolyte, this method allowed for the modification of the SnO₂ work function, facilitating a better energy alignment with the perovskite layer. At the optimal pH value of 1, the device surpassed the performance of those made with commercial colloidal dispersion SnO₂, achieving a PCE of 19.21% in the active area of 0.99 cm². Regulating the presence of oxygen vacancies in SnO₂ is crucial as it influences the electron carrier concentration.⁴⁷ The proportion of Sn⁴⁺ increased with decreasing pH, suggesting that the ECD method could fine-tune the ratio of oxygen vacancies on the SnO₂ surface *via* electrolyte pH control (Fig. 3f). Lower pH values led to greater work function values due to the diminished oxygen vacancies, culminating in a superior energy level alignment. While ECD SnO₂-based mini-modules were not produced, smaller devices, created from 1.5 × 1.5 cm² sections cut from a 10 × 10 cm² SnO₂ substrate, demonstrated consistent PCEs.

2.1.2. Solution processed HTL. Among various p-type metal oxides, NiO_x is commonly selected for solution processed HTL. Similar to solution processed ETLs, one of the primary benefits of solution processed NiO_x is its ability to be realized without the need for costly setups like vacuum or laser deposition. This solution processed approach is not only cost-effective but also simplifies the study of NiO_x. The predominant strategies to date aim to boost electrical conductivity and adjust band alignment, ensuring efficient carrier extraction between the perovskite and NiO_x in small-scale cells.^{48–51} Among various solution processes, including spin coating, inkjet printing, slot die coating, blade coating, and spray coating, the spin-coating method has demonstrated effectiveness for fabricating PSCs in small-aperture areas with remarkable efficiency. However, the spin-coating-based NiO_x fabrication remains largely at the laboratory scale. For large-scale HTL fabrication, spray-coating, blade coating, and slot-die coating methods are considered as suitable candidates.

Chou *et al.* investigated a spray-coating technique for the large-scale deposition of NiO_x.⁵² They introduced a scalable ultrasonic spray-coating technique and examined the optical and chemical alterations in the spray-coated NiO_x following various post-annealing durations. In general, after NiO_x deposition, a post-annealing process at elevated temperatures (ranging from 300 to 600 °C) is employed to eliminate organic components and enhance NiO_x crystallinity. In this perspective, they presented a method that facilitates rapid, low-temperature annealing, resulting in a transparent and compact crystalline NiO_x layer. As a result, under optimal annealing conditions, the mini-modules achieved a PCE of 6.18% with an aperture area of 10.4 cm².

Moreover, Zhang *et al.* attempted to use the polymer network micro-precipitation method to address the agglomeration phenomenon observed in NiO_x NPs (Fig. 4a).⁵³ Through this method, they achieved enhanced uniformity in NiO_x films characterized by monodispersed and crystalline NPs, enabling



Fig. 4 (a) Schematic figure of the polymer network micro-precipitation method and introduction of a surfactant IL. Reproduced with permission.⁵³ Copyright 2021, Wiley-VCH. (b) Perovskite deposition method via slot-die coating with vacuum fast-assisted solution processing (VASP). Reproduced with permission.⁵⁴ Copyright 2021, Wiley-VCH. (c) Schematic illustration of well-dispersed Ni(OH)₂ nanoparticles prepared by emulsion and chemical precipitation processes. Reproduced with permission.⁵⁵ Copyright 2021, Elsevier. (d) Calculated crystal structure, charge density, partial density of states (DOS), and crystal structure including Ni vacancies. Typical J-V curves for the rigid and flexible devices with a large area. Reproduced with permission.⁵⁶ Copyright 2021, Wiley-VCH. (e) Schematic illustration of the NiO_x annealing process by a hot plate (HP) flash annealing (FIRA) process. Device structure, and J-V curve for FIRA and HP-O₂. Reproduced with permission.⁵⁸ Copyright 2023, Wiley-VCH. (f) Schematic figure of the NiO_x formation process by different chemically structured amino alcohol ligands. J-V curves of PSM with the target CBD-NiO_x. Device structure of PSM. Reproduced with permission.⁶² Copyright 2023 Wiley-VCH.

the use of the blade-coating technique for scalable NiO_x fabrication. The optimally dispersed NiO_x precursor yields a deposition that is both flatter and smoother than that derived from a non-optimized NiO_x colloidal solution. The polymer grid effectively limits the mobility and growth of NiO_x NPs during preparation. Concurrently, the polymer network method enhances charge extraction capabilities owing to the encompassing polymer grid. Furthermore, an ionic liquid (IL), 1-ethyl-3-methylimidazole diethyl phosphate, is integrated into the resultant NiO_x NPs as a surfactant. The surfactant reduces nanoparticle agglomeration and enhances its conductivity. Additionally, the functional group (–P=O) of the IL passivates the underlying perovskite film by coordinating with the divalent Pb²⁺, leading to a more crystalline perovskite layer. With this approach, PCEs of 20.91% (a control PCE of 18.05%) and 19.71% (a control PCE of 16.50%) were achieved in rigid and flexible large-area substrates with 1.01 cm², respectively.

The slot-die coating technique is also well-suited for the scalable fabrication of PSCs. PSCs fabricated entirely through slot-die coating have been demonstrated and executed under ambient conditions with a relative humidity (RH) of 20–40%

and temperatures ranging from 15–20 °C (Fig. 4b).⁵⁴ Critical to this process is a meticulous control over parameters such as the preheating temperature of the substrate and the film thickness, given the sensitive formation stages of NiO_x. Furthermore, the addition of A-cation additives based on chlorides, specifically MAcl and FAcl, proves crucial during the slot-die coating of the perovskite film. This incorporation facilitates the achievement of an average grain size exceeding 400 nm, indicative of high crystallinity. Using the comprehensive slot-die process, champion PSCs measuring 1 cm² and based on MAPbI_{3-x}Cl_x and CsFAPbI_{3-x}Cl_x achieved PCEs of 16.06% and 17.33%, respectively. Additionally, champion slot-die coated PSMs, having an active area of 2.1 cm², registered PCEs of 11.7% for MAPbI_{3-x}Cl_x and 14.9% for CsFAPbI_{3-x}Cl_x.

In addition to the selection of a deposition method, a low-temperature process for NiO_x is preferred for the fabrication of large-area devices. A recent study reports a low-temperature process (below 150 °C) for the synthesis of highly crystalline NiO_x NPs.⁵⁵ In this method, tetramethylammonium hydroxide (TMAOH) serves as a surfactant, facilitating the conversion of Ni(NO₃)₂ into Ni(OH)₂ NPs (Fig. 4c). The resultant NiO_x NPs,

which exhibit outstanding dispersibility and stability, are synthesized through an emulsion process. The uniform NiO_x films enhance the interfacial contact with the perovskite layer. As a result, impressive efficiencies of 18.85% and 13.93% were attained for devices with active areas of 0.09 cm² and 1 cm², respectively. This is in contrast to the control efficiencies of 16.68% and 10.06%. They also demonstrated a flexible PSC that achieved a PCE of 14.3%.

Recently, Wang *et al.* proposed a strategy to produce high-quality NiO_x NPs using a low-temperature process.⁵⁶ In the conventional low-temperature synthesis of NiO_x NPs, the NO₃⁻ ion, which arises from the reaction between Ni(NO₃)₂ and NaOH, is identified as the predominant impurity in the NiO_x film. This impurity cannot be completely removed at low temperatures. To address this issue, an ionic liquid-assisted synthesis approach (NiO_x-IL) has been proposed. This method utilizes 1-butyl-3-methylimidazolium in place of the traditional Ni(NO₃)₂ precursor. This approach effectively facilitates the removal of impurity ions, such as NO₃⁻, which tend to adsorb readily onto the surface of NiO_x NPs, subsequently enhancing the conductivity of NiO_x. The resultant NiO_x layer also facilitates hole transfer, attributed to its improved extraction capability. The resulting device exhibits a PCE of 22.62%, a notable improvement compared with the control PCE of 19.80%. Remarkably, even the large-area device measuring 1 cm² achieves a high PCE of 20.19%.

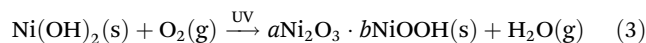
Beyond the typical monovalent and divalent ions, Bao *et al.* suggest that trivalent samarium-doped NiO_x (Sm:NiO_x) NPs, prepared using a chemical precipitation method, obviate the need for a post-annealing process.⁵⁷ This study reveals that the Sm³⁺ dopant effectively reduces the formation energy of the Ni vacancy, implying its role in enhancing the p-type property of NiO_x (Fig. 4d). As a result, the Fermi level is downshifted toward the valence band, enhancing electrical conductivity and facilitating carrier extraction at the perovskite/NiO_x interface, which in turn minimizes the charge recombination loss. The chemical reaction of Sm:NiO_x NP is illustrated using the following equations:



Owing to the enhanced optoelectronic properties, devices structured as an ITO/Sm:NiO_x/perovskite layer/PCBM/BCP/Ag achieve champion PCEs of 20.71% for an area of 0.09 cm² and 18.51% for 1 cm². Notably, since the deposition technique for Sm:NiO_x obviates the need for a post-annealing process, a flexible plastic PSC has been introduced, achieving a PCE of 17.95%. Additionally, a large module has been developed using the blade-coating method to deposit the perovskite film. The PSM (40 × 40 mm²) yields a high PCE of 15.27% with the active area of 8.85 cm² at the optimized Sm³⁺ concentration.

To ensure cost-effective mass production and expedited manufacturing, challenges related to the annealing temperature and the extended duration of the annealing process need to be addressed. Lian *et al.* reported a method for fabricating

NiO_x at temperatures below 82 °C using a UV irradiation strategy, which offers notable reductions in manufacturing costs.⁵⁸ This UV irradiation effectively converts Ni²⁺ to Ni³⁺, modulating a deeper valence band that is advantageous for charge extraction from the perovskite film. The relevant chemical mechanism is proposed as a following reaction.



A champion photovoltaic device fabricated using UV irradiation achieves a remarkable PCE of 22.45%, compared with a control PCE of 19.79%. Additionally, this approach has been applied to flexible PSCs, achieving a PCE of 19.7%.

Instead of the conventional annealing methods, such as those using a hot plate or furnace, an alternative approach is proposed. Ochoa-Martinez *et al.* introduce the flash infrared annealing (FIRA) process, which facilitates short annealing durations under ambient conditions (Fig. 4e).⁵⁹ The FIRA method promotes the generation of excess oxygen in NiO_x, leading to an elevated O/Ni ratio. This technique significantly enhances conductivity compared with the traditional hotplate annealing process. Consequently, impressive PCEs of 16.1% for a large area of 1 cm² and 15.9% for a submodule area of 17 cm² were recorded.

In regular structured PSCs that utilize SnO₂ as the electron transport layer, the chemical bath deposition (CBD) method offers a significant enhancement in both performance and stability.²⁰

However, when utilizing the CBD method for NiO_x deposition, the efficiency in inverted PSCs was found to be suboptimal. The primary challenges with the CBD approach for NiO_x include inconsistent grain sizes and low conductivity.⁶⁰ To address these issues, it is imperative to fine-tune the grain size through controlled growth adjustments and enhance conductivity. Recently, a CBD method has been developed that allows for *in situ* growth tunability through different ligands.^{61,62} This method also enables a low-temperature process, contrasting with the traditionally required high annealing temperatures for NiO_x deposition. Especially, it has been demonstrated that the controlled growth of NiO_x can be adjusted by the ligand structure.⁶³ The optimal ligand, tri-ethanolamine (T-EA), results in a uniform and compact deposition of NiO_x (Fig. 4f). This method utilizes a brief exposure to a modest temperature of 270 °C, making it compatible with substrates like ITO that can be processed at low temperatures. They emphasize the benefits of this method, which facilitates both the mass production and scalable fabrication of solar modules. Consequently, impressive PCEs of 22.03% and 19.03% were achieved for devices with active areas of 0.1 cm² and 18.10 cm², respectively.

In addition to this, a copper (Cu) doped and seeding-induced crystallization approach (SCBD) is also introduced.⁶⁰ By adopting the seed-assisted induction, a large grain size by controlled crystal growth is induced, resulting in a compact NiO_x layer with lowered defect sites. In conjunction with the SCBD approach, a Cu-doping strategy is employed to promote

the formation of shallow acceptor levels that function as defect states, enhancing both hole transport and conductivity. Utilizing an optimal Cu doping concentration, impressive PCEs of 22.51% and 19.29% were achieved for small (0.06 cm²) and large-area mini-modules (active area of 28 cm²), respectively.

2.2. Vacuum deposition

Considering the inherent limitations of general solution-processed deposition concerning surface morphology, uniformity, roughness, and coverage, the physical vapor deposition method offers a viable alternative. It ensures controllable thickness along with high conductivity. Furthermore, by adjusting doping concentration and modulating oxygen partial pressure, band structure tunability can be effectively facilitated, making it suitable for large-area manufacturing.^{64,65}

2.2.1. Vacuum deposited ETL. Erdenebileg *et al.* investigated the potential of low-temperature atomic layer deposition (ALD) SnO₂ for large-area perovskite solar modules.³⁰ Considering that the primary growth conditions involved the number of cycles and growth temperature, they tried to deter-

mine the optimal settings for thermal-based ALD (Fig. 5a and b). Their findings pointed to 120 °C and 150 cycles as the optimal conditions for efficient SnO₂ growth, leading to a peak PCE of 13.74% over an active area of 26.4 cm².

Zanoni *et al.* utilized the pulsed laser deposition (PLD) technique for SnO₂ fabrication.³² The PLD method is acknowledged for its scalability, pattern ability, and rapid processing capabilities. They optimized both the PLD chamber pressure and the concentration of oxygen in the background gas (Fig. 5c). A deposition pressure of 5 × 10⁻³ mbar was found ideal for producing a smooth, pinhole-free PLD SnO₂ layer. Additionally, an atmosphere saturated with 100% O₂ was identified as crucial to mitigate the formation of oxygen vacancies. As a result, PLD enabled consistent deposition across extensive areas, spanning over 615 cm², with a mere 1.5% variation in thickness.

2.2.2. Vacuum deposited HTL. Since most of the vacuum-based deposition still requires a high cost, in comparison with the solution-processed deposition, the research on the reforming NiO_x deposited by dry vacuum processes is rarely studied.

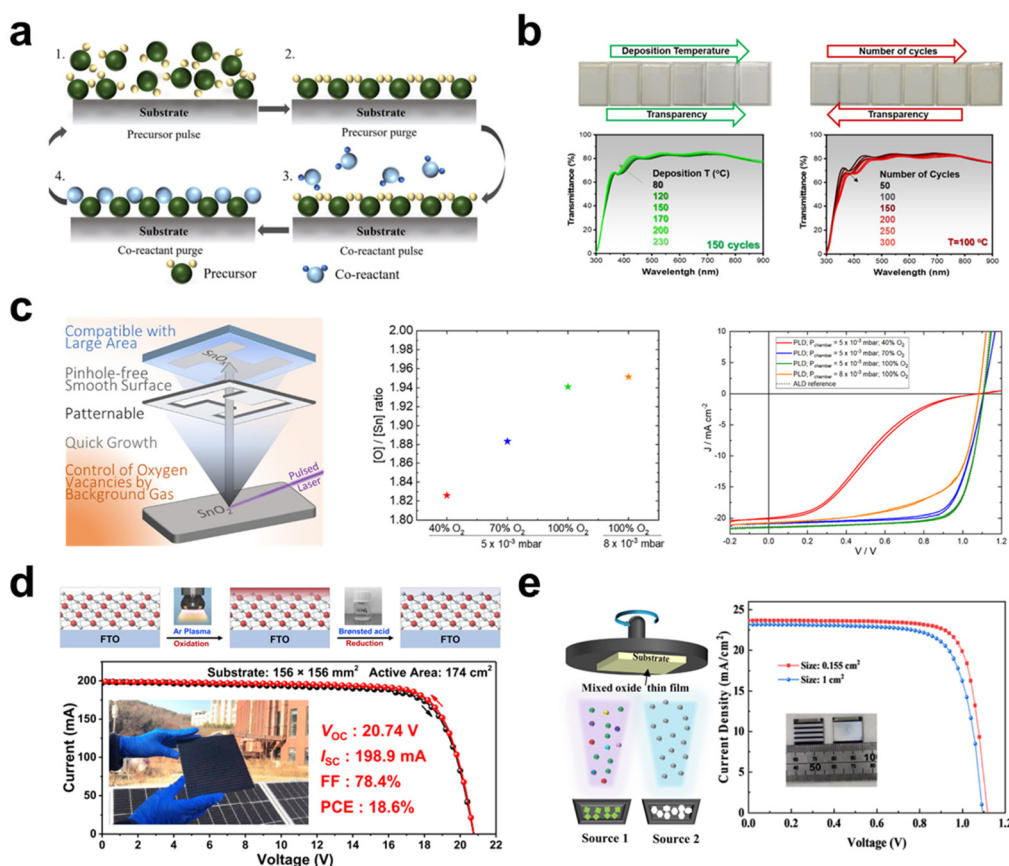
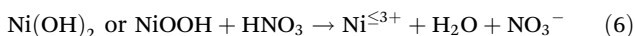
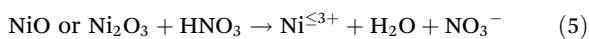
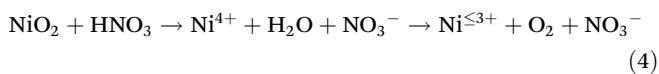


Fig. 5 (a) Illustration of atomic layered deposition (ALD). Reproduced with permission.⁶⁹ Copyright 2022, Wiley-VCH. (b) Transmittance of ALD SnO₂ prepared with different temperatures and cycles. Reproduced with permission.³⁰ Copyright 2021, Wiley-VCH. (c) Illustration of pulsed laser deposition (PLD) (left), O/Sn ratio (middle) and *J*-*V* curves (right) of SnO_x (or devices) under different pressures and oxygen concentrations. Reproduced with permission.³² Copyright 2023, American Chemistry Society. (d) Schematic illustration of the process of surface redox engineering (SRE). The *J*-*V* curves for the PSM with an active area of 174 cm². Reproduced with permission.⁶⁶ Copyright 2022, Elsevier. (e) Schematic figure showing the dual-source electron beam co-evaporation process. The *J*-*V* curves for the PSCs with an active area of 0.155 and 1 cm², respectively. Reproduced with permission.⁶⁷ Copyright 2022, Wiley-VCH.

Recently, a surface redox engineering (SRE) has been proposed (Fig. 5d).⁶⁶ In this approach, NiO_x is produced using electron beam evaporation, which is then followed by an Ar-plasma-initiated oxidation process. They highlighted challenges inherent in conventional wet-processed NiO_x. By refining their vacuum process and implementing a treatment involving HNO₃ solution, they induced a Brønsted-acid-mediated reduction process, successfully mitigating specific interfacial complications. Treatment of HNO₃ solution inducing a Brønsted-acid-mediated reduction process enables detrimental Ni⁴⁺ and -OH to be removed, suppressing the degradation reaction with a perovskite. Furthermore, because of the surface redox reaction, abundant Ni³⁺ leads to more p-type property with improved charge conductivity, thereby enhancing the carrier extraction. All chemical reactions seen with the HNO₃ acid environment are illustrated using the following reactions:



Besides the SRE method on NiO_x films, it is also imperative to achieve high-crystalline perovskite films. The slot die coating method is applied for the deposition of the perovskite, in which due to the increase of the surface-free energy of the SRE-treated NiO_x, the contact angle is significantly lowered to ~7°. The improved wettability facilitates the full coverage of the perovskite solution, which satisfies the difficult requirement of the slot-die coating. In this progress, an excellent PCE of 23.4% attributed to by an improved V_{OC} and FF is obtained. As a result, this yielded PCEs of 23.4% and 18.6% in small-area and mini-modules (156 × 156 cm²), respectively.

Jiang *et al.* have introduced a scalable electron-beam co-evaporation method applicable for all-inorganic charge transport layers, including NiO and niobium oxide (Nb₂O₅), serving as an HTL and ETL, respectively (Fig. 5e).⁶⁷ They propose mechanisms for enhancing electrical conductivity through the use of dopants such as Cu and tungsten (W). Through optimal dopant incorporation, copper-doped NiO_x (Cu-NiO_x) and tungsten-doped Nb₂O₅ (W-Nb₂O₅) exhibit improved charge transport abilities and effectively prevent halide segregation. These factors contribute to achieving high PCEs of 21.31% and 19.01% for active areas of 0.155 cm² and 1 cm², respectively.

2.3. Bilayer

2.3.1 Bilayer ETL. Lee *et al.* fabricated a SnO₂/TiO₂ bilayer using SnO₂ NPs and acetylacetone-modified TiO₂ (7-TiO₂) to establish a cascaded energy band structure.⁶⁸ This approach demonstrated that SnO₂ exhibits a high charge transport ability, while Acac-TiO₂ reduces the band offset and suppresses the interfacial charge recombination. The outcome was the attainment of a PCE of 18.39% in the best-performing mini-module, which had a 25 cm² aperture area and comprised 5 sub-cells. Additionally, this configuration retained 83.1% of

the initial PCE under thermal and moisture stability conditions (85 °C/RH 85%) for 1000 h.

He *et al.* proposed that a ZnO-SnO₂ cascaded ETL enables the efficient fabrication of large-area perovskite solar modules using a combination of spray-coated ZnO and blade-coated SnO₂.⁶⁹ The perovskite layer and HTL for the modules were also prepared through blade coating. The ZnO-SnO₂ ETL configuration effectively prevented the protonation of ZnO and established a well-matched conduction band level between the ETL and the perovskite layer, resulting in an improvement in the V_{OC}. The use of a spray pyrolysis-coated ZnO underlayer prevented unfavorable physical defects (such as pinholes and partial coverage shortages) that can arise due to the toughness of FTO, in comparison with the blade-coated SnO₂. The champion modules achieved efficiencies of 17.82% and 16.60% for active areas of 18.0 cm² and 47.0 cm², respectively.

Kim *et al.* reported a study on the utilization of polyacrylic acid (paa)-stabilized QD-SnO₂ deposited on a c-TiO₂ blocking layer to achieve a highly efficient ETL.⁷⁰ This was accomplished through spin coating for paa-QD-SnO₂ and spray pyrolysis for c-TiO₂. The QD-SnO₂ exhibited varying thicknesses, ranging from ~30 nm to ~70 nm, on the c-TiO₂ layer, while the paa-QD-SnO₂ demonstrated a uniform and conformal thickness of approximately ~30 nm. This suggests that the paa played an important role in ensuring uniformity in the paa-QD-SnO₂ ETL. The paa effectively interacted and bonded with the QD-SnO₂, contributing to the formation of a conformal ETL (Fig. 6a).

The best PCEs for mini-modules with active areas of 20 cm² and 64 cm² were found to be 21.7% and 20.6%, respectively. Moreover, an extended shelf-life test over 1000 h was conducted under ambient conditions with a relative humidity of 25% and a temperature of 25 °C. During this test, the paa-QD SnO₂@c-TiO₂-based perovskite solar cells retained approximately 80% of the initial PCE of 25.7%. The researchers not only addressed the limitations associated with spin coating through additive engineering for the SnO₂@TiO₂ bilayer fabrication but also achieved remarkably high efficiency for large-area mini-modules.

2.4. Interfacial modification

2.4.1. Interfacial modified ETL. To achieve highly uniform and well-defined metal oxides over large areas, surface engineering of the ETL using ionic compounds, acids, self-assembled molecules, and carbon derivatives remains crucial. Recently, as an enhancement to spin-coated mp-TiO₂, Castriotta *et al.* introduced an interlayer of potassium-doped graphene oxide (GO-K) between the perovskite and mp-TiO₂, applied using infrared annealing.⁷¹ Mini-modules incorporating GO-K demonstrated an impressive peak PCE of 16.10%, with an average PCE of 15.58% across six mini-modules with a 16 cm² active area, showing only a 0.43% deviation. They suggested that the GO-K interlayer not only augmented electron extraction to the ETL but also enhanced the crystallinity of the TiO₂. Additionally, the GO-K layer improved the physical



Fig. 6 (a) Schematic diagram of the formation of QD-SnO₂ and paa-QD-SnO₂. Reproduced with permission.⁷⁰ Copyright 2022, American Association for the Advancement of Science. (b) Structure of energy band diagrams (left), photographs and J - V curves of the champion mini-modules with different areas. Reproduced with permission.⁷¹ Copyright 2022, Elsevier. (c) HAADF-STEM images and the corresponding proportion of each exposed crystal plane of SnO₂ and S,N-CQD@SnO₂, respectively. Reproduced with permission.⁷² Copyright 2023, Wiley-VCH. (d) Illustration of ETL/PVK interface passivation with H₂KNO₃S in the perovskite solar cells and J - V curves of the 5 × 5 cm² champion mini-modules. Reproduced with permission.⁷⁶ Copyright 2023, Wiley-VCH. (e) XPS spectra of SnO₂ and SnO₂/K (left) and J - V curves of SnO₂/K-based mini-modules. Reproduced with permission.⁷⁸ Copyright 2021, Springer Nature. (f) Schematic images of the periodic acid post-treatment on the SnO₂ surface (left) and the J - V curves of the 3 × 3 cm² control and target mini-modules, respectively. Reproduced with permission.⁸⁰ Copyright 2023, Wiley-VCH.

properties of the perovskite, primarily by facilitating the removal of PbI₂, leading to superior perovskite crystal growth.

Pescetelli *et al.* employed 2D materials to modify interfaces within perovskite modules.⁷² Specifically, they utilized graphene ink for c-TiO₂ and mp-TiO₂ layers, MXenes for the perovskite layer, and fMoS₂ for the perovskite/PTAA interlayer (Fig. 6b). The inclusion of graphene in the ETL notably improved the electron mobility and electron injection at the mp-TiO₂/perovskite interface, and simultaneously reduced the charge trap sites within the mp-TiO₂. This led to an enhanced FF and an increased open-circuit voltage V_{oc} . Based on these improvements, they highlighted the potential of 2D materials in countering the inefficiencies typically introduced during scaling-up. In their optimized perovskite solar modules featuring 2D materials, the champion device achieved a 17.2% and 14.7% PCE for 121 cm² (the active area of 82.62 cm²) and 210 cm² (the active area of 137.2 cm²) modules, respectively (Fig. 6b). Moreover, the 121 cm² modules, encapsulated *via* a glass sealing technique, maintained 80% of their initial P_{MAX} values after approximately 530 h at 85 °C. Regarding the improved stability, they proposed that (1) the stable perovskite crystal structure within the mp-TiO₂ + graphene matrix contributed to the temperature stabilization of the charge carriers, and (2) the transformation of perovskite into PbI_x and PbO_x

species might be slowed down by the presence of graphene within the mp-TiO₂.

To further develop the CBD TiO₂, Guo *et al.* reported poly-L-lysine with modified sulfonic acid groups (PLLS) for the TiO₂/perovskite interface.⁷³ Not only did the sulfonic acid groups reduce the oxygen vacancies of the TiO₂ surface, leading to an increase in conductivity, but also the amine groups passivated deep-level defects of perovskite, resulting in a reduction of non-radiative recombination. Thus, a high PCE of 20.07% with a notable FF of 76.75% was obtained in the active area of 33 cm². Importantly, no voltage loss was observed during the scaling-up process. In terms of stability, the PLLS-modified mini-modules retained 80% of their initial PCE for 30 days under ambient conditions at room temperature and an RH of 5–10%. In contrast, the control mini-modules only managed to retain 60% of their initial PCE for a mere 6 days.

Among efforts to further improve spin-coated SnO₂, Yang *et al.* reported how altering the SnO₂ crystal facet with a novel carbon quantum dot (CQD) doping technique could yield highly efficient PSCs.⁷⁴ The CQD-doped SnO₂ predominantly featured the (200) and (111) facets, in contrast to the pristine SnO₂, which was characterized by the (110) and (101) facets (Fig. 6c). Considering that the adsorption energy of Pb²⁺ is higher on the more active (200) and (111) crystal planes, per-

ovskite crystal nucleation is enhanced on the CQD-SnO₂ surface. Besides, the buried perovskite/CQD-SnO₂ interface was observed with better crystallinity than that of pristine SnO₂. Flexible mini-modules based on CQD-SnO₂ attained a PCE of 17.79% over an aperture area of 24 cm². These modules also demonstrated superior stability, preserving approximately 95% of the initial PCE for 1200 h at 25 °C and an RH of around 40% under 1-sunlight exposure, and retained >90% of the initial PCE after bending cycles with a bending radius of 6 mm.

In a separate study, Hu *et al.* modified the SnO₂/perovskite interface with diphenyl phosphine oxide (DPO), a phosphorus-containing Lewis acid.⁷⁵ This resulted in an impressive PCE peak of 15.69% for mini-modules covering an aperture area of 22.56 cm². They reported that the Lewis acid sites of the DPO engaged with the SnO₂ surface, neutralizing OH⁻ groups. This interaction considerably enhanced electron transfer and diminished the energy barrier between the perovskite and SnO₂ layers.

Tong *et al.* introduced potassium sulfamate (H₂KNO₃S) as a modification to the SnO₂/perovskite interface (Fig. 6d).⁷⁶ Their aim was to mitigate the oxygen loss from SnO₂ encountered during the hybrid chemical vapor deposition (HCVD) process, a method intended for scalable perovskite fabrication. While the HCVD technique holds promise for large-scale perovskite deposition, they identified an issue: SnO₂ experienced oxygen loss during this deposition process, leading to interfacial defects and enhanced carrier recombination. By introducing H₂KNO₃S, they established a chemical bridge between the SnO₂ and perovskite layers. This not only compensated for the oxygen loss but also passivated the uncoordinated Pb²⁺ within the perovskite. The result was more than just a surface modification of SnO₂; it effectively addressed challenges inherent in large-area perovskite deposition. As a result, PCEs of 16.16% and 12.12% were achieved for active areas of 22.4 cm² and 91.8 cm², respectively.

While the CBD method is a promising method for SnO₂ deposition, challenges remain, such as the need to reduce the inherent defect density, eliminate interfacial defects, and ensure uniform coverage for scalable SnO₂ ETL. Addressing these, several strategies have been developed to introduce additives into the chemical bath solutions. For instance, Dong *et al.* incorporated potassium citrate into the CBD solution to optimize SnO₂ coverage.⁷⁷ Through this additive engineering, they successfully achieved a PCE of 24.84%, in small cells.

Tong *et al.* incorporated KMnO₄ into the chemical bath solution to enhance the quality of SnO₂.⁷⁸ This addition also facilitated the doping effects of K and Mn ions as they diffused into the upper perovskite grain boundaries. KMnO₄ reduced the trap densities and enhanced carrier mobilities of SnO₂ by promoting the oxidation of Sn²⁺ → Sn⁴⁺. Furthermore, they proposed that the formations of KCl and MnCl₂ on the SnO₂ surface served as effective passivators. Consequently, the K ions enabled the growth of larger perovskite grains, while the Mn ions enhanced perovskite crystallinity and phase stability due to their doping contributions. The resulting mini-

modules, with active areas of 22.4 and 91.8 cm², achieved PCEs of 17.26% and 13.72%, respectively (Fig. 6e).

Besides additive engineering, interfacial engineering presents another way for enhancement. Lv *et al.* employed hypervalent potassium xanthate (KiPX) to modify SnO₂, aiming to minimize the interfacial defects and improve charge extraction in perovskite solar modules.⁷⁹ The Lewis acid sites of the KiPX established robust interactions with both Pb²⁺ from the perovskite and Sn⁴⁺ from the SnO₂. Furthermore, the release and diffusion of K ions from the KiPX into the perovskite grain boundaries served to passivate defect sites. As a result, the KiPX-modified mini-modules exhibited a stabilized efficiency of 18.8% over a 48.0 cm² active area and retained 90% of their PCE after 600 h under ambient conditions.

Wu *et al.* demonstrated that periodic acid could interact with the hydroxyl groups present on the SnO₂ surface (Fig. 6f).⁸⁰ This interaction results in a more favorable energy level alignment between SnO₂ and perovskite, leading to decreased interfacial non-radiative recombination and improved charge transport. A strong oxidation state of IO₆⁻ enabled the oxidation of Sn²⁺ → Sn⁴⁺ of SnO₂, which led to reduced trap states and a higher V_{OC}. As a result, the resultant mini-modules with the aperture area of 2.29 cm² exhibited a high PCE of 18.10% (Fig. 6f). In a 3000 h shelf life test, the unencapsulated mini-modules retained 93.32% of their initial PCE under ambient conditions at 20 °C and a relative humidity of 20–25%. In contrast, the control mini-modules preserved only 29.79%.

2.4.2. Interfacial modified HTL. A pressing issue in the field is the poor interface quality between NiO_x and the perovskite, coupled with the undesired decomposition of perovskite. Li *et al.* employed the pyrolysis of urea, which led to a reduction in the valence band and consequently decreased the prevalence of high-valent Ni species, such as Ni³⁺. Such species are known to form hole barriers through their reactive hydroxyls (Fig. 7a).⁸¹ Furthermore, the pyrolysis process resulted in the formation of isocyanate (-NCO) species, which coordinate with Ni on the NiO_x surface, diminishing the interface defect sites and enhancing carrier extraction. Consequently, they achieved an impressive fill FF exceeding 86%, a PCE of 23.61%, and PCEs of 18.97% and 17.18% for active areas of 16 cm² and 196 cm², respectively.

Meanwhile, Wang *et al.* demonstrate the interface passivation strategy *via* HI soaking on NiO_x in process of the roll-to-roll (R2R) technology (Fig. 7b).⁸² After treatment with HI, Ni³⁺ is reduced to NiI₂ product by the following redox reactions.



The reduced Ni³⁺ forms a favorable band alignment with better charge transport ability. Simultaneously, the resultant NiI₂ product contributes to coordinating with Pb²⁺ in the perovskite film, which improves the perovskite quality and impedes the degradation reaction. Furthermore, it is found that the HI treatment is conducive for the adopted printing

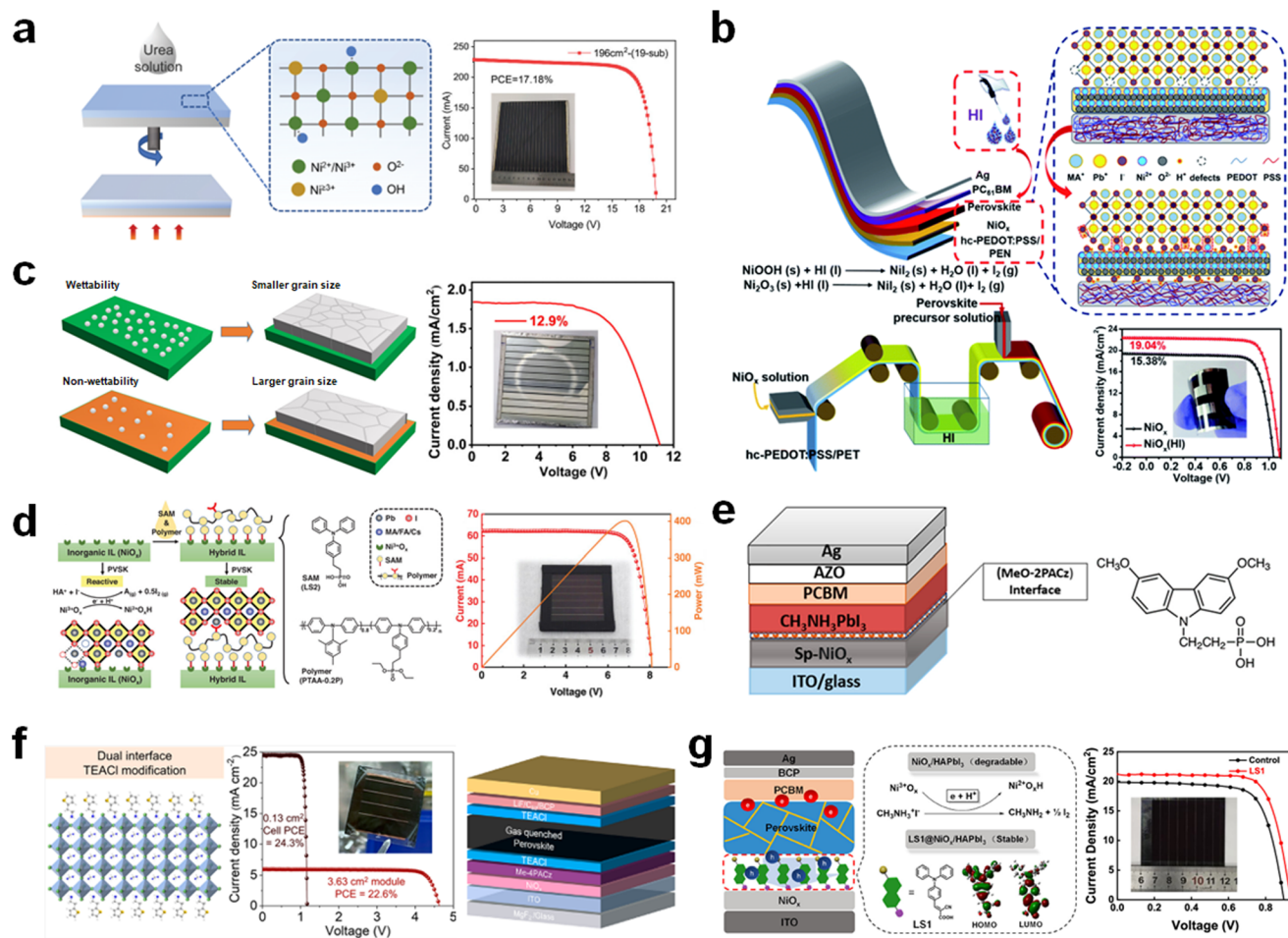


Fig. 7 (a) Schematic illustration of the post treatment of urea. J - V curves for the PSM (area of 196 cm²). Reproduced with permission.⁸¹ Copyright 2023, Wiley-VCH. (b) Fabrication of the PSCs by an R2R process. J - V curves for the HI-treated and control devices. Reproduced with permission.⁸² Copyright 2021, The Royal Society of Chemistry. (c) Perovskite crystallization with a large grain size by TeDA-treated NiO_x. J - V curves for the PSM (area of 53.64 cm²). Reproduced with permission.⁸³ Copyright 2022, Wiley-VCH. (d) Hybrid interfacial strategy optimized by bilateral surfaces of CTL and a perovskite. A P - J - V curve of PSM on a hybrid IL (aperture area of 19.4 cm²). Reproduced with permission.⁸⁴ Copyright 2023, Wiley-VCH. (e) Schematic illustration of the introduced MeO-2PACz as an interface in the device architecture. MeO-2PACz molecule structure. Reproduced with permission.⁸⁹ Copyright 2022, American Chemical Society. (f) Schematic figure of the TEACl modification. Device architecture. Reproduced with permission.⁹¹ Copyright 2023, American Chemical Society. (g) Schematic illustration of suppressing perovskite decomposition by LS1. J - V curves for the LS1-treated and control perovskite solar modules (aperture area of 19.16 cm²). Reproduced with permission.⁹⁶ Copyright 2022, American Chemical Society.

method due to the smoothed surface of the NiO_x film. Eventually, the PCEs of the flexible PSCs show 19.04% (1 cm²) and 16.15% (15 cm²), respectively, which retain their initial PCE of 80% with a bending radius of 3 mm after 500 bending cycles.

Liu *et al.* discovered that TeDA modulates the wettability of the NiO_x surface, impeding the crystallization process of the perovskite film (Fig. 7c).⁸³ This leads to the deposition of a film characterized by larger grains and fewer defect sites. The TeDA treatment increases the contact angle to 87.1°, which manipulates the surface energy, resulting in the non-wetting surface leading to slow crystallization. As a result, a PCE of 12.9% is achieved for the module with an aperture area of 53.64 cm² on spray-coated NiO_x by surface engineering with the organic amine of TeDA.

Recently, a synergistic strategy has been introduced that both enhances the interfacial charge transport ability and improves the chemical stability.⁸⁴ This approach involves a multifunctional thin layer that incorporates LS2 and PTAA-0.2P. This layer passivates NiO_x by interacting with the dangling bonds on the metal oxide and coordinates with Pb²⁺ in the perovskite film (Fig. 7d). This hybrid interfacial strategy refines the chemical composition, boosts the carrier extraction ability, and results in optimal energy band alignment. Consequently, a solar module with an aperture area of 19.4 cm² achieved a PCE of 20.7%.

For inverted flexible PSCs, low-temperature processed NiO is commonly utilized due to the robust chemical stability, high transmittance, and low manufacturing cost. However, the processed NiO intrinsically possesses high defect sites and energy

band mismatch with the perovskite film, thereby hindering the efficiency of the flexible PSCs. Beyond the intrinsic limit of single-junction PSCs, a tandem PSC with mitigated major interfacial problems such as non-radiative recombination and energy level mismatch is reported.⁸⁵ The mixed molecules for bridging with 2PACz and MeO-2PACz tune the energy-level alignment between the NiO_x and the perovskite and improve the charge extraction with mitigated interfacial recombination. Consequently, a large flexible device with an aperture area of 1.05 cm² shows a high PCE of 23.5%, which retains the initial PCE after 10 000 bending cycles with a maximum bending radius of 15 mm.

Recently, efforts to surpass the Shockley–Queisser limit for single-junction solar cells have intensified, with perovskite-silicon tandem solar cells emerging as a focal point of research. The CTLs and their associated interfacial layers are pivotal for

tandem device operation, particularly in terms of charge transport and transmittance.⁸⁶ In the context of CTL and TCO selection, SAMs have been introduced to minimize the Voc deficit and ensure optimal charge transfer.⁸⁷ In recent advancements, IZO has been adopted as a TCO and 2PACz as an HTL. It has been demonstrated that a homogeneously anchored SAM on TCO achieves compact coverage, subsequently boosting the Voc and FF. Additionally, PTAA has been chosen to regulate the charge extraction of NiO_x through π - π stacking.⁸⁸

The MeO-2PACz interlayer is also utilized solely for the surface modification of NiO_x.⁸⁹ The modified interface enhances the quality of the perovskite film, with its well-structured band facilitating efficient charge transfer (Fig. 7e). This reduces the charge recombination due to the interfacial passivation effect on the NiO_x. Consequently, a PCE of 16.25% is achieved over an active area of 1 cm².

Table 1 Reported deposition methods, PCE, and area of PSMs based on metal oxides using SnO₂, TiO₂, and NiO_x, coupled with their counterparts

Device type	Metal oxide CTLs	Deposition method	PCE (%)	Area (cm ²)	Reported kinds of area	Ref.
n-i-p	SnO ₂ -TiO ₂	Spin coating	16.62	15	Active	36
n-i-p	c-TiO ₂ /SC-TiO ₂	Spin coating	22.87	24.63	Active	34
n-i-p	SnO ₂	Spray coating	12.0/11.7	16.8/21.8	Active	39
n-i-p	SnO ₂	Chemical bath deposition	16.0/17.0	6.0 × 2.0/16.0 × 2.5	Aperture	44
n-i-p	SnO ₂	Chemical bath deposition	21.3	10	Active	45
n-i-p	SnO ₂	Atomic layer deposition	13.74	26.4	Active	30
n-i-p	SnO ₂ /Acac-TiO ₂	Spin coating	18.39	20	Aperture	68
n-i-p	ZnO ₂ /SnO ₂	Spray/blade coating	17.8/16.6	18/47	Active	69
n-i-p	c-TiO ₂ /SnO ₂	Spray/spin coating	21.7/20.6	20/64	Active	70
n-i-p	c-TiO ₂ /mp-TiO ₂	Spray/spin coating	16.1	16	Active	71
n-i-p	SnO ₂	Spin coating	17.79	24	Aperture	74
n-i-p	SnO ₂	Chemical bath deposition	15.69	22.56	Aperture	75
n-i-p	SnO ₂	Spin coating	16.16/12.12	22.4/91.8	Active	76
n-i-p	SnO ₂	Chemical bath deposition	17.26/13.72	22.4/91.8	Active	78
n-i-p	SnO ₂	Chemical bath deposition	18.8	48	Active	79
n-i-p	SnO ₂	Chemical bath deposition	18.1	2.29	Aperture	80
p-i-n	NiO _x	Spray coating	6.18	10.4	Aperture	52
p-i-n	NiO _x	Blade coating	20.19/19.7	1.01 (rigid/flexible)	—	53
p-i-n	NiO _x	Slot die coating	14.9	2.1	Active	54
p-i-n	NiO _x NPs	Spin coating	13.93	1	Active	55
p-i-n	NiO _x NPs	Spin coating	20.19	1	—	56
p-i-n	Sm:NiO _x NPs	Spin coating	18.51/15.2	1/8.85	Active	57
p-i-n	NiO _x	Spin coating	16.1/15.9	1/17	—	59
p-i-n	NiO _x	Chemical bath deposition	19.03	18.1	Active	63
p-i-n	Cu:NiO _x	Chemical bath deposition	19.29	28	Active	60
p-i-n	NiO _x	Electron beam evaporation	18.6	156 × 156	Substrate	66
p-i-n	Cu:NiO _x	Electron beam co-evaporation	19.01	1	Active	67
p-i-n	NiO _x	Spray pyrolysis	18.97/17.18	16/196	Active	81
p-i-n	NiO _x	Roll-to-roll fabrication	19.04/16.15	1/15	Active	82
p-i-n	NiO _x	Spray coating	12.9	53.64	Aperture	83
p-i-n	NiO _x	Spin coating	20.7	19.4	Aperture	84
p-i-n	NiO _x	Spin coating	23.5	1.05	Aperture	85
p-i-n	NiO _x	Sputtering	16.25	1	Active	89
p-i-n	NiO _x	Spin coating/sputtering	22.6/21.7	3.63/3.78	Active	91
p-i-n	NiO _x	Spin coating	14.90	19.16	Aperture	96
Device type	Organic CTLs	Deposition method	PCE (%)	Area (cm ²)	Reported kinds of area	Ref.
n-i-p	Spiro-Naph	Spin coating	21.83	25	Aperture	98
n-i-p	Spiro-OMeTAD	Spin coating	19.16	16.38	Active	99
n-i-p	Spiro-OMeTAD (PPO-TEMPO)	Spin coating	21.4	17.1	Aperture	100
n-i-p	PTAA (OATFSI)	Spin coating	19.91	209.39	Active	101
p-i-n	MPA-CPA	Spin coating	22.0	9.66	Active	102
p-i-n	TPT-P6	Spin coating	20.09	1	Active	103
p-i-n	Enantiomer-pure	Spin coating	20.1	36.4	Active	104



Fig. 8 The efficiency of (a) n-i-p and (b) p-i-n structured metal oxide-based PSMs over 1 cm² of the active or aperture area reported recently.

Ammonium salts which generally form 2D perovskite materials have been used for passivation within buried perovskite and NiO_x.⁹⁰ 2-Thiopheneethylammonium chloride (TEACl) is incorporated in both sides of the perovskite layer (*i.e.*, buried, and top interfaces) (Fig. 7f).⁹¹ The TEACl-based dual interfaces contribute to the optimal band alignment and reduced defect sites, and improve interfacial contact, resulting in a high photovoltaic performance. The TEACl dual-treated PSC exhibits a PCE of 22.6% for the large submodule with an active area of 3.63 cm².

It is crucial to mitigate the adverse interfacial reactions between NiO_x and the perovskite film to prevent detrimental changes.^{92,93} Redox reactions, which lead to perovskite decomposition, and UV-induced degradation are both strongly associated with the degradation of the NiO_x-perovskite interface.⁹⁴ Interfacial treatment using SAM molecules is also an effective strategy.⁹⁵ Liu *et al.* utilizes a self-assembled dyad molecule to reduce the redox reaction between Ni³⁺ and ammonium iodide composited in a perovskite film (Fig. 7g).⁹⁶ A new self-assembled dyad molecule effectively suppresses the interfacial degradation and assists in modulation of the perovskite growth and charge extraction. Consequently, a PCE of 14.90% is obtained for the large module with an aperture area of 19.16%. Furthermore, as an integrated system to improve interfacial properties such as contact problems and carrier extraction, A-site cation-size mismatch and interface stabilization (CM-IS) strategies are introduced to achieve highly effective and stable PSCs.⁹⁷ The integrated strategy facilitates the charge transport and improves interface contact in inverted PSCs. The surface modification of PTAA onto NiO_x enhances the hole transport and suppresses the trap states of the NiO_x. The further modification of mesoporous alumina (mp-Al₂O₃) on PTAA not only facilitates effective interfacial contact but also enhances charge extraction. As a result, in large area PSM (11.2 cm²), a PCE of 16.9% is achieved.

3. Conclusions

This review provides a comprehensive overview of metal oxide CTLs tailored for large-area PSMs in recent years. With impressive progress in PCE for both regular and inverted structured PSCs, the urgency for commercialization to support mass and large-scale production has grown. Considering this trend, there is a pressing need to focus on metal oxide CTLs for the development of large-area PSCs as shown in Table 1 and Fig. 8.

This review highlights several critical aspects that must be addressed to enhance the efficiency, stability, and scalability of PSCs. Foremost among these is the need for spatial uniformity and reproducibility to ensure scalability. Moving beyond spin-coating, techniques such as bar, spray, blade, and slot die-coating emerge as potential solutions to issues of non-uniformity. However, the current volume of research on this topic is limited, and significant challenges persist, particularly when scaling up to larger module areas. Vacuum-based deposition entails higher costs and demands specialized facilities, but it remains a viable option for large-scale CTL fabrication due to its consistent uniformity and reproducibility. However, to date, only a limited number of studies have explored modifications to this deposition technique.

Strategies to regularly arrange metal oxide NPs can be beneficial for the up-scaling process. Introducing molecules acting as binders can promote the formation of a well-defined CTL with consistent thickness and fewer defects. Single crystalline or facet-controlled metal oxides also provide significant effects on large-area PSMs. The single crystalline NPs possess high conductivity as well as effectively suppress interfacial charge recombination and accumulation, leading to the narrow gap between the efficiencies of PSMs and small cells. Certain facet-controlled metal oxides tailor the activity of the metal oxide surface, which modulates the perovskite nucleation.

CBD is gaining prominence as a method for the large-scale production of SnO₂ ETL. Through this approach, a uniform and homogeneous ETL is directly formed on the substrates within the chemical bath. The development of CBD can be readily tailored through adjustments in pH, the addition of chemicals, and other treatments such as thermal annealing. These manipulations offer potential for further refining of the morphological, optical, physical, and chemical properties of SnO₂.

Considering that flexible polymer substrates necessitate low-temperature processes, there is a compelling need for research and development focused on low-temperature methodologies and associated materials to harness the full potential of PSMs. Unlike the high-temperature calcination required for TiO₂, SnO₂ and NiO_x have recently emerged as promising CTL alternatives for large-area flexible PSMs due to their low-temperature processing capabilities. Additionally, while hybrid ETLs enable low-temperature annealing below 150 °C, methods employing electrochemical treatments also present the advantage of bypassing thermal annealing altogether.

The modulation of various properties through the use of additives and interfacial engineering is crucial for crafting CTLs suitable for scalable devices. These modifications play a pivotal role in influencing the optoelectrical, optical, and morphological attributes of the CTLs. In tandem with the passivation effects, the optimized energy band alignment significantly enhances carrier extraction, leading to reproducible device performance and enhanced stability. The enhanced electrical conductivity, stemming from an increased carrier concentration and mobility, is predominantly fine-tuned through this engineering approach, with a particular emphasis on NiO_x refinement. Additionally, well-modified CTLs influence the perovskite grain growth rate and nucleation dynamics based on factors such as wettability and surface roughness, which, in turn, favor the formation of larger perovskite grains. A highly crystalline active layer significantly contributes to enhanced performance and stability, importantly influencing the scalability of PSMs. Introducing alkaline, transition, and halogen ions within CTL offers various benefits. These additives not only infiltrate the bulk perovskite layer, facilitating grain boundary passivation and modulating crystal growth, but they also chemically interact with interfacial defect sites, thereby diminishing trap densities.

The robust stability, which ensures reproducibility, is also important for solar modules exposed to external conditions, including thermal, humid, and light-illuminated environments. Approaches to inhibit the detrimental reactions at the perovskite/CTL interface through interfacial modifications are highly desired.

In summary, for the realization of mass-produced, large-area PSMs, continuous research and development efforts are essential to develop metal oxides that are both scalable and capable of achieving high performance. In this perspective, the target technology of metal oxides optimal for the scalable device is to secure low-temperature processes and low-cost facilities, which are necessary to achieve mass production.

Even capabilities to ensure uniformity and reproducibility by connecting NPs with binders are expected to narrow the gap between large- and small-scale devices. To achieve this, there is still an urgent need to use various modification integrating additive, interfacial engineering, and deposition methods.

Conflicts of interest

There are no conflicts to declare.

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