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A co-doping strategy for p- to n-type transition and performance boost in SnSe-based flexible thermoelectric generators

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Flexible thermoelectric generators (FTEGs) have garnered considerable interest for their potential in energy harvesting applications. This study investigates the synthesis of SnSe and Bi/Te co-doped SnSe polycrystals using the solid-state reaction method, followed by the fabrication of FTEGs using a low-cost, scalable screen-printing technique. Hall effect measurements confirm successful doping, resulting in a transition from p-type to n-type conduction in SnSe. The Seebeck coefficient of the 2% Bi-doped SnSe/SnSe (p–n type) FTEG reaches $-1146 \mu\text{V K}^{-1}$, enhancing the thermoelectric performance. A maximum power output of 6.8 nW was obtained for a p–n-type FTEG consisting of SnSe and $\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$ at a temperature difference of 120 °C. Thermal conductivity measurements indicate that doping reduces phonon transport due to increased microstrain and dislocation density, which enhance phonon scattering. Furthermore, the FTEGs exhibited excellent mechanical stability, with less than 0.5% change in electrical resistance at bending angles up to 120° and after 500 cycles. These results suggest that Bi/Te co-doped SnSe is a potential candidate for scalable, flexible thermoelectric applications.

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1 Introduction

The rapid expansion of wearable and portable electronic devices has created a growing need for flexible, lightweight, and efficient energy-harvesting systems.¹ Among the diverse range of energy harvesting technologies, thermoelectric generators (TEGs) stand out due to their intrinsic capability to directly convert thermal gradients into electrical energy *via* the Seebeck effect.² This attribute is particularly advantageous when power autonomy is crucial, such as in wearable electronics, biomedical sensors, and flexible electronic devices. TEGs are solid-state devices known for their high reliability, minimal maintenance, and ability to recover electrical energy from waste heat without needing mechanical parts, liquids, or gases.³ This makes them a potentially environmentally friendly alternative to traditional batteries, often requiring frequent replacement and posing environmental hazards.⁴

Traditionally, thermoelectric research has concentrated on chalcogen compounds such as Bi_2Te_3 ⁵ and PbTe ,⁶ which are used for low- and medium-temperature applications, respectively. However, the scarcity of tellurium (Te) in the Earth's crust has constrained the commercial viability of these generators, limiting their application in niche areas such as space exploration, watches, and Peltier coolers.⁷ Despite investigations into

alternative materials like oxides,⁸ silicides, and half-Heuslers,⁹ these materials generally exhibit lower thermoelectric performance (ZT) compared to their Te-based counterparts, thus curtailing their broader adoption. Conventional thermoelectric materials, however, are predominantly characterized by their rigidity and brittleness, which significantly hinders their deployment in flexible and stretchable applications. To address these limitations, recent research has increasingly focused on developing flexible thermoelectric materials that maintain high thermoelectric performance while offering mechanical compliance.¹⁰ Among these, tin selenide (SnSe) stands out due to its exceptional thermoelectric properties, including a notably high Seebeck coefficient and intrinsically low thermal conductivity; both are critical for achieving high-efficiency energy conversion.^{11,12} SnSe, a tellurium-free compound, has garnered significant interest within the thermoelectric community, having demonstrated a ZT of 2.6 in single crystal ingots along a preferential crystallographic plane.¹²

In the present study, SnSe samples were prepared through the solid-state reaction method, and their thermoelectric properties were further optimized through the strategic co-doping of bismuth (Bi) and tellurium (Te). The pristine SnSe exhibits p-type semiconducting behavior, while the Bi/Te-doped SnSe transitions to an n-type semiconductor. We initially fabricated and characterized separate n-type and p-type FTEGs. Subsequently, these n-type and p-type materials were combined to create a new flexible thermoelectric generator (FTEG) utilizing the screen-printing technique, which was then characterized to evaluate its performance. The screen-printing

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process offers several key advantages, including cost-effectiveness, simplicity, scalability, mass-production capability, and the ability to deposit thermoelectric materials onto flexible substrates. Additionally, unlike many traditional fabrication techniques for SnSe, such as solvothermal synthesis,¹³ melting followed by spark plasma sintering (SPS),¹⁴ and thermal evaporation,¹⁵ which require high temperatures, high pressures, and lengthy fabrication times, screen printing can be performed at room temperature and atmospheric pressure. By addressing the challenges associated with merging mechanical flexibility with high thermoelectric efficiency, this study contributes to the advancement of flexible energy harvesting technologies. This work also reports the fabrication and optimization of FTEG devices using low-cost and sustainable materials and methods.

2 Materials and methods

2.1 Materials

The materials employed in this study included tin, selenium, and tellurium, all with 99.9% purity, purchased from Molychem, Mumbai, India, and bismuth with 99.999% purity from Otto Kemi, Maharashtra, India. The process also involved the use of diacetone alcohol from Loba Chemicals and cellulose acetate propionate from Sigma-Aldrich. The flexible thermoelectric generator (FTEG) was fabricated on a transparent, flexible polyester terephthalate (PET) film, 100 μm thick, provided by Venlon Polyester Film Ltd, India. Screen-printable silver ink, specifically Loctite ECI 1010 E&C, was obtained from Henkel, India.

2.2 Synthesis and characterization of SnSe and Bi/Te co-doped SnSe samples

A solid-state reaction method is employed to synthesize $\text{Sn}_{1-x}\text{Bi}_x\text{Se}_{0.97}\text{Te}_{0.03}$ ($x = 0, 0.02, 0.04$) polycrystals. Initially, high-purity precursor materials, including tin, selenium, bismuth, and tellurium, were combined in precise stoichiometric ratios and mixed thoroughly for two hours in an agate mortar using intense grinding. The homogeneous fine powder was then pelletized under a compression pressure of 5 tons, resulting in pellets with dimensions of $10 \times 2 \times 5 \text{ mm}^3$. These pellets were sintered at 400 $^\circ\text{C}$ for 24 hours in a quartz tube with a diameter

of 12 mm, which was evacuated to a vacuum of 10^{-4} torr. After sintering, the furnace was permitted to cool down naturally to room temperature. The resulting $\text{Sn}_{1-x}\text{Bi}_x\text{Se}_{0.97}\text{Te}_{0.03}$ ($x = 0, 0.02, 0.04$) materials were formed into rectangular bulk samples, which were ground for further characterization. Fig. 1 provides a detailed illustration of the sample preparation process.¹⁶ The structural characterization of SnSe and co-doped SnSe was performed using X-ray diffraction (XRD) using a Rigaku Ultima IV instrument, with a scan range of 10° to 90° and a step size of 0.02° per minute. The surface morphology of the prepared pellets was examined using a scanning electron microscope (SEM, Carl Zeiss Sigma).

2.3 Screen printing workflow

The screen-printing technique consists of a sequence of key procedures. Initially, the ink formulation is prepared. This ink is then applied to a screen, consisting of a fabric mesh stencil. A coating blade evenly distributes the ink across the screen surface, ensuring that it fills the mesh openings. A squeegee is then employed to apply pressure against the screen, forcing the ink through the mesh and onto the substrate. As the squeegee is drawn across the screen, it deposits a uniform layer of ink onto the substrate in the desired pattern. Upon removal of the screen, the inked pattern is transferred onto the substrate. Fig. 2 shows the schematic workflow of the screen-printing technique for the fabrication of the FTEG.

2.4 Ink formulation of SnSe and Bi/Te co-doped SnSe and characterization

Following the preparation of the bulk materials, these SnSe and Bi/Te co-doped SnSe samples were further processed to formulate inks suitable for screen printing. To achieve this, 20% cellulose acetate propionate (CAP) was dissolved in 80% diacetone alcohol, serving as the ink vehicle. Table 1 presents the composition of the three distinct Bi/Te co-doped $\text{Sn}_{1-x}\text{Bi}_x\text{Se}_{0.97}\text{Te}_{0.03}$ ($x = 0, 0.02, 0.04$) inks. The ink mixtures were homogenized using a magnetic stirrer for one hour to ensure uniform distribution of the components. Ink viscosity was controlled within the ideal range of 1750–2000 cP to ensure compatibility with screen printing.

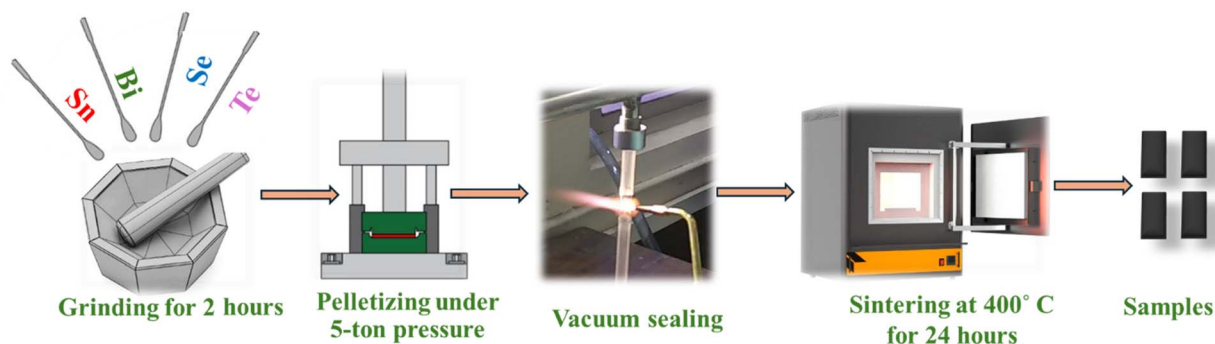


Fig. 1 The sequential process of the solid-state reaction method for synthesizing SnSe and co-doped SnSe materials.



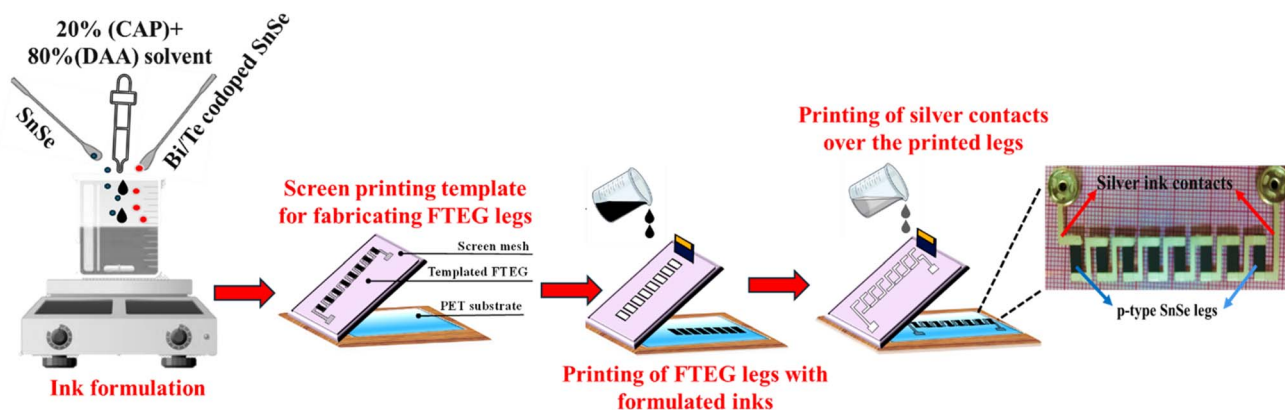


Fig. 2 Schematic workflow of the screen-printing technique for the fabrication of the FTEG.

Table 1 Composition of SnSe and codoped SnSe inks

Sample	Sn (g)	Se (g)	Bi (g)	Te (g)	Ink vehicle (g)
SnSe	3.002	1.997	—	—	1.75
$\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$	2.895	1.906	0.103	0.096	1.75
$\text{Sn}_{0.96}\text{Bi}_{0.04}\text{Se}_{0.97}\text{Te}_{0.03}$	2.810	1.889	0.206	0.095	1.75

The crystallinity of the inks was characterized using a Rigaku Ultima IV X-ray diffraction (XRD) instrument, with a scanning range of 10° to 90° and a step size of $0.02^\circ \text{ min}^{-1}$. The morphology of the prepared inks was examined using a scanning electron microscope (SEM, Carl Zeiss Sigma). The ink samples were pelletized using a hydraulic press at 5 tons of pressure for a duration of 5 minutes. These pellets were then analyzed for their electrical properties through Hall effect

measurement using a Keithley Hall measurement system, employing the Van der Pauw method at room temperature.

2.5 Screen printing of FTEGs

To develop flexible thermoelectric generators (FTEGs) using specially formulated inks, separate p-type and n-type FTEGs were initially fabricated, each consisting of eight legs measuring $3 \text{ mm} \times 10 \text{ mm}$ (see Fig. 3(a and b)). A mesh screen with a mesh count of 120 cm^{-1} (Shebro, INDIA) was employed, and the stencil was created using a combination of direct and indirect techniques.¹⁷ A flat, wedge-shaped rubber squeegee with a hardness of 75 Shore A was used to apply multiple overprints (12 layers) of the respective p-type and n-type inks (refer to Table 1). Additionally, commercially available silver ink was screen printed as electrodes on both the p-type and n-type devices. Subsequently, a combined p–n type FTEG was fabricated using

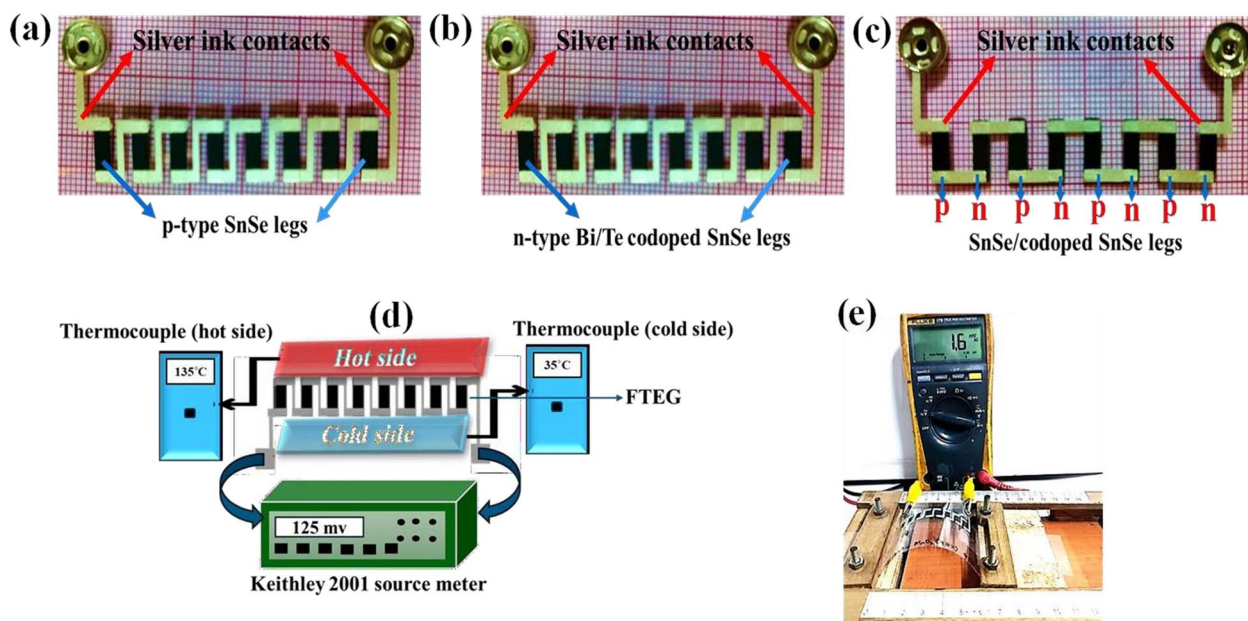


Fig. 3 (a) FTEG with p-type legs, (b) FTEG with n-type legs, (c) FTEG with alternative p–n type legs, (d) characterization arrangement for the FTEG, and (e) setup for the FTEG's flexibility analysis.



the same inks (Fig. 3c), interconnecting the p-type and n-type legs with four layers of silver ink screen printed onto a 100 μm thick flexible PET substrate. All FTEGs were fabricated to have the same dimensions of 15 mm \times 55 mm.

2.6 Characterization of the FTEG

A customized experimental setup consisting of two thermocouples and a Keithley 2001 source meter (illustrated in Fig. 3d) was utilized to determine the Seebeck coefficient and power output of the FTEGs based on SnSe and Bi/Te codoped SnSe

inks. One side of the FTEG was heated *via* a hot plate to create a temperature gradient, whereas the opposite remained at room temperature. The temperatures at both ends of the FTEG were recorded using a K-type digital thermocouple (Lutron TM-902C). As previously documented, the Keithley 2001 source meter records both the voltage generated across the FTEG and its internal electrical resistance.^{18–20} A Mitutoyo 547–301 Digimatic thickness gauge was employed to measure the thickness of the ink film. The flexibility of the FTEGs was tested at ambient temperature using a two-probe multimeter (Fluke 179)

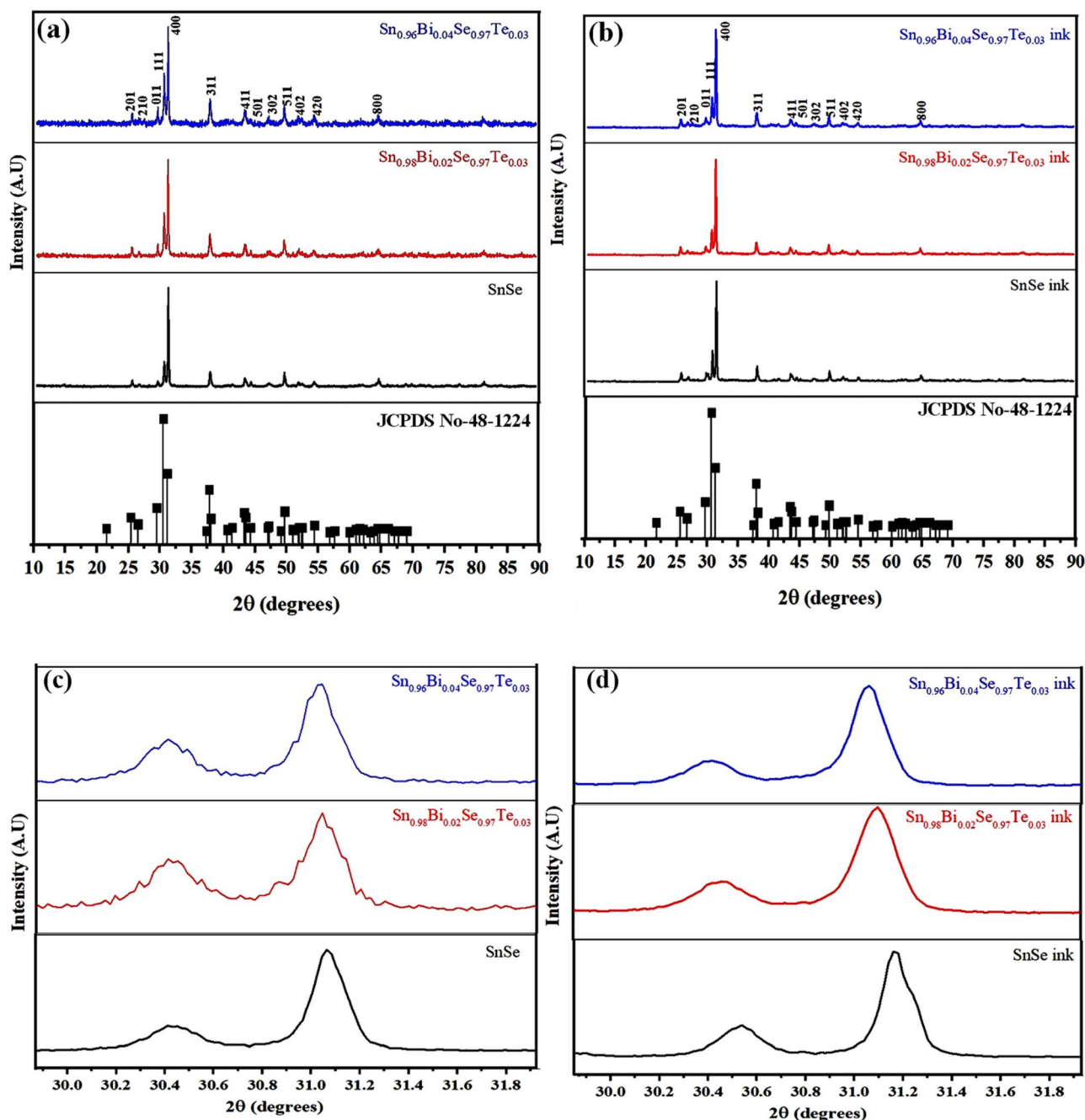


Fig. 4 (a) XRD peak pattern of powder, (b) XRD peak pattern of ink-based samples, (c) peak shift graph of powder samples, and (d) peak shift graph of ink-based samples.



in conjunction with a specially designed bending device (Fig. 3e).²¹ Additionally, an evaluation of the thermoelectric performance was conducted after subjecting the FTEGs to various single-fold bending cycles (0, 100, 200, 300, 400, and 500).

3 Results and discussion

3.1 Structural evaluation via X-ray diffraction analysis

The X-ray diffraction (XRD) analysis of pellet powder and ink-based SnSe/Bi-Te co-doped SnSe powder (prepared by adding 20% cellulose acetate propionate (CAP) and 80% diacetone alcohol as the ink vehicle) reveals similar peak patterns, indicating that the addition of the ink vehicle did not affect the crystal structure of the material. As seen in Fig. 4a and b, all the diffraction peaks matched with the JCPDS data (48-1224) without any secondary phases within the detectable range of the analysis,²² confirming that all prepared samples are single-phase and correspond to the intended composition. The (400) plane exhibited the most intense diffraction peak across all samples, denoting the sample's preferred growth orientation. All samples crystallize in the low-temperature orthorhombic phase of SnSe, associated with the *Pnma* space group.²³ The orthorhombic crystal structure of SnSe is retained across both preparation methods.

Fig. 4c and d show a noticeable shift towards the lower angle side in the high-intensity peak pattern at (400), which is observed upon doping SnSe with Bi/Te. This shift suggests lattice expansion due to the substitution of Sn atoms by the larger Bi and Te atoms, leading to increased interplanar spacing.²⁴ The observed peak shifts in the XRD spectrum arise from the slight variation in ionic radii between Bi (230 pm) and Sn (225 pm), causing strain in the matrix.¹⁶ The difference of 5 pm in ionic radii results in tensile stress within the compounds. The variation in lattice parameters tabulated in Table 2 confirms the correlation between increasing Bi content and the observed peak shifts. These results indicate successful incorporation of dopant atoms into the SnSe lattice. We adopted the Scherrer method to determine the crystallite size (*D*) and strain (ε). The Scherrer equation is given by eqn (1),²⁵

$$D = \frac{0.9 \lambda}{\beta \cos \theta} \quad (1)$$

considering the X-ray wavelength (λ), full width at half maximum (β), and peak position (θ) in radians. Dislocation

refers to flaws or irregularities in the crystal structure. However, the addition of Bi/Te has increased dislocation density and microstrain. The dislocation density (δ) can be determined using eqn (2),²⁵

$$\delta = \frac{1}{D^2} \quad (2)$$

Similarly, eqn (3) was utilized to calculate the average microstrain (ε) present in the samples,²⁵

$$\varepsilon = \frac{\beta}{4 \tan \theta} \quad (3)$$

The changes in crystallite size, dislocation density, and microstrain, as demonstrated in Table 2, are well aligned with earlier reports.²⁶ Materials with smaller crystallite size, elevated dislocation density, and higher microstrain typically show reduced thermal conductivity.²⁷ Dislocation plays a crucial role in thermoelectric properties, as it can impact both charge carrier flow and heat transfer within the material. These factors are interrelated: as crystallite size decreases, dislocation density and microstrain tend to rise due to an increase in grain boundaries, which introduce more defects.

3.2 SEM and EDAX studies of screen-printed ink films

Scanning electron microscopy (SEM) was employed to study the microstructural properties of the samples, as depicted in Fig. 5a–f. The SEM images of the pellet samples with a magnification scale of approximately 1 μm made from SnSe and Bi/Te co-doped SnSe (Fig. 5a–c) using the solid-state reaction method exhibit a relatively smooth and uniform surface texture characterized by fine granulation. This uniform morphology indicates effective particle interconnection, essential for optimizing thermoelectric performance.²⁸ The observed layered structure suggests a well-organized arrangement of the material that can enhance carrier mobility. Distinct crystal boundaries may influence the scattering of charge carriers and phonons, thereby affecting both electrical and thermal conductivity. The compactness of the structure also implies good mechanical integrity, crucial for the stability of thermoelectric devices during operation.

After grinding the sintered pellets into fine powder, the material was used to fabricate screen-printed FTEGs. The corresponding SEM images of the printed films after thermal annealing are shown in Fig. 5d–f, captured at a magnification

Table 2 Powder XRD analysis of SnSe and Bi/Te co-doped SnSe: structural characterization of powder and ink-based samples

Sample	Average crystallite size (<i>D</i>) (nm)	<i>a</i> (Å)	<i>b</i> (Å)	<i>c</i> (Å)	Dislocation density (δ) 10^{15} m^{-2}	Microstrain (ε) 10^{-3}
SnSe	30 \pm 3	11.490	4.151	4.432	1.1	3.7
Sn _{0.98} Bi _{0.02} Se _{0.97} Te _{0.03}	28 \pm 2	11.505	4.153	4.434	1.3	4.3
Sn _{0.96} Bi _{0.04} Se _{0.97} Te _{0.03}	27 \pm 2	11.520	4.158	4.438	1.4	4.7
SnSe ink	29 \pm 3	11.495	4.150	4.432	1.2	4.0
Sn _{0.98} Bi _{0.02} Se _{0.97} Te _{0.03} ink	24 \pm 2	11.510	4.154	4.433	1.7	5.6
Sn _{0.96} Bi _{0.04} Se _{0.97} Te _{0.03} ink	23 \pm 2	11.515	4.157	4.439	1.9	6.3



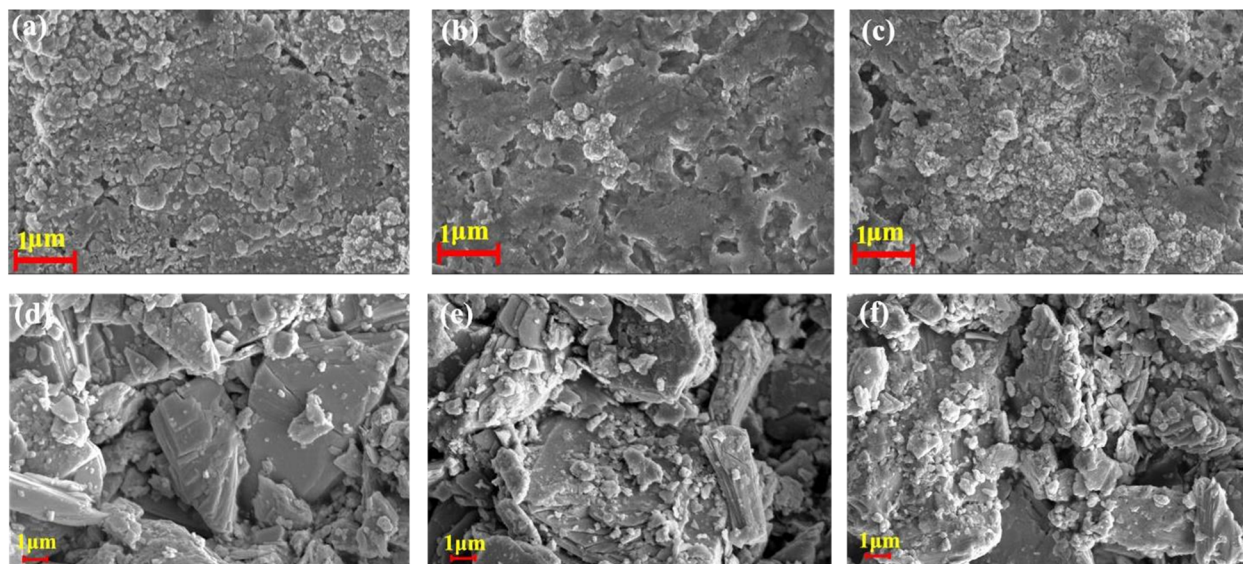


Fig. 5 SEM images of (a) SnSe, (b) $\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$, and (c) $\text{Sn}_{0.96}\text{Bi}_{0.04}\text{Se}_{0.97}\text{Te}_{0.03}$ pellet samples, and SEM images of (d) SnSe, (e) $\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$, and (f) $\text{Sn}_{0.96}\text{Bi}_{0.04}\text{Se}_{0.97}\text{Te}_{0.03}$ ink-film samples.

corresponding to a 1 μm scale to reveal surface morphological features. The presence of a layered structure and different particle morphologies may facilitate phonon scattering, which is advantageous for achieving low thermal conductivity while

maintaining adequate electrical conductivity.²⁹ The rougher texture observed in the printed samples can enhance interfacial contact between the thermoelectric material and the substrate, thus improving overall device performance. Additionally, any

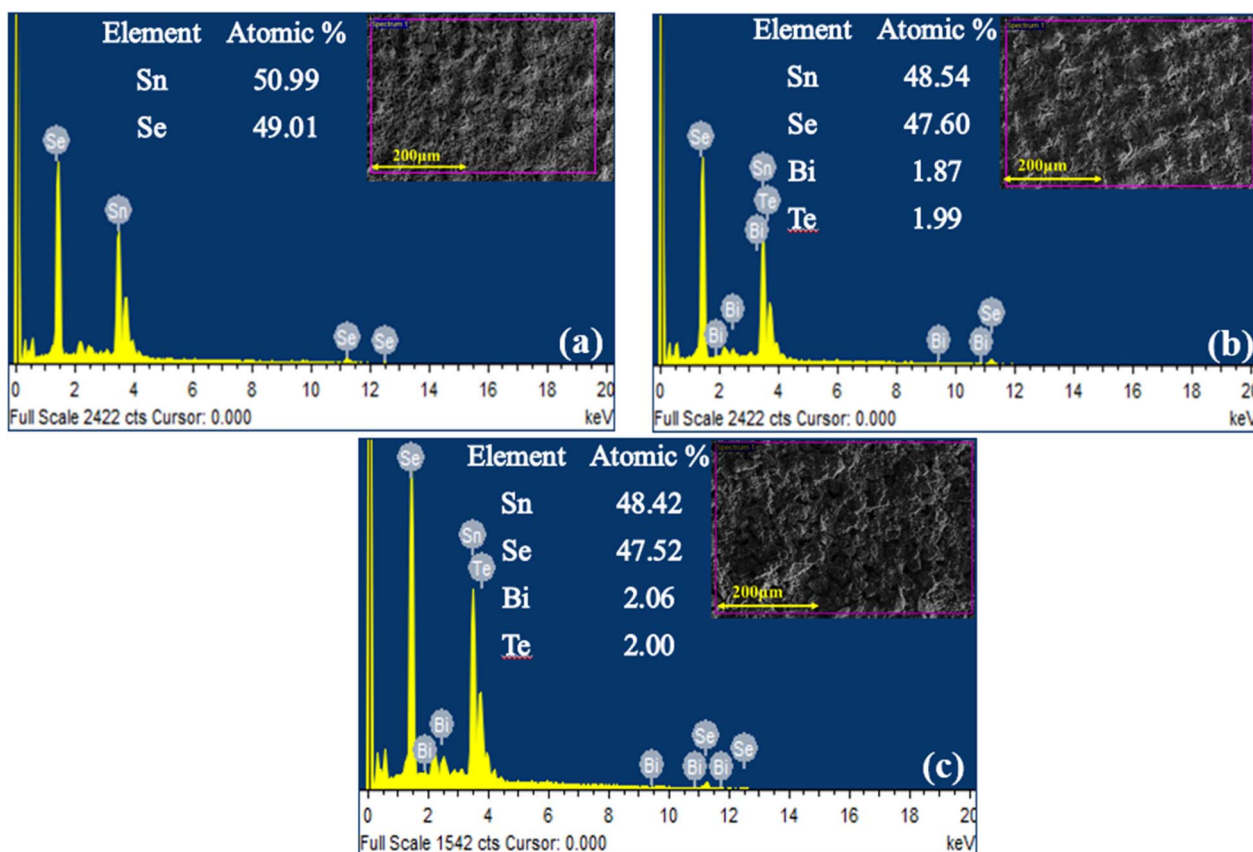


Fig. 6 EDAX images with precise locations of spectrum acquisition of (a) SnSe, (b) $\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$ and (c) $\text{Sn}_{0.96}\text{Bi}_{0.04}\text{Se}_{0.97}\text{Te}_{0.03}$ ink-film samples.



visible cracks or voids may serve as potential points for phonon scattering, further contributing to thermoelectric efficiency. The bulk samples exhibit densely packed grains with relatively uniform morphology, while the printed films appear less compact and structurally more porous, likely due to the nature of the screen-printing process and post-deposition drying limitations. Overall, the SEM analysis highlights distinct microstructural characteristics between the pellet samples and the printed FTEGs. The uniform structure of the pellets shows high material quality, while the varied structure of the printed devices enables tunable thermoelectric properties in flexible applications.

The EDAX spectra obtained from the ink films reveal their elemental compositions, accompanied by inset images indicating the precise spectrum acquisition locations with a magnification scale of 200 μm . The SnSe sample displays a nearly stoichiometric ratio of Sn and Se (Fig. 6a), while the Bi/Te co-doped SnSe ink films confirm the successful integration of Bi and Te alongside substantial amounts of Sn and Se (Fig. 6(b and c)). This elemental distribution aligns with the intended doping concentrations, thereby facilitating the anticipated enhancement in the thermoelectric properties of the materials.

Fig. 7a illustrates the thickness measurement of the SnSe and Bi/Te co-doped SnSe ink films. The data reveal that the maximum observed variation in thickness does not exceed 2%, underscoring the consistency and reliability of the film deposition process.

3.3 Hall effect study

Hall effect measurements were conducted at an ambient temperature of 300 K to assess the impact of Bi and Te doping on the carrier concentration, type, and mobility in SnSe samples. The result shown in Fig. 7b reveals that pure SnSe behaves as a p-type semiconductor,³⁰ as evidenced by its positive carrier concentration. However, with the introduction of dopants, the samples display n-type behavior, represented by

the negative sign of the carrier concentration.¹² These findings are consistent with Seebeck coefficient measurements. Fig. 7b also illustrates the effect of dopant concentration on both mobility and carrier concentration. As the concentration of Bi increases, mobility decreases, likely due to the enhanced scattering of carriers. It is also evident that mobility and carrier concentration follow opposing trends, as reported in previous studies.³¹ Upon introducing an n-type dopant like Bi to the p-type SnSe matrix, electrons introduced by the dopant shift the majority carrier from holes to electrons. This transition reduces the carrier concentration from $+4.28 \times 10^{16} \text{ cm}^{-3}$ (in undoped SnSe) to $-0.708 \times 10^{16} \text{ cm}^{-3}$ in a sample doped with 2% Bi ($\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$). With further increase in Bi concentration, the material maintains its n-type behavior, and the carrier concentration rises to $-1.78 \times 10^{16} \text{ cm}^{-3}$ in the 4% Bi-doped sample. Te, which partially substitutes for Se, plays a supportive role by slightly modifying the band structure and defect chemistry, thereby facilitating n-type conduction and influencing carrier scattering behavior.³² Initially, the neutralization of p-type defects by n-type dopants reduces defect-induced scattering and thus increases mobility. However, at higher doping levels, the increased carrier concentration contributes to enhanced electron–electron scattering, resulting in a subsequent decline in mobility.³³ These changes in carrier concentration and mobility are crucial for tuning the Seebeck coefficient and optimizing the thermoelectric performance of the material.

3.4 Transient thermal conductivity studies

Fig. 8a and b depict the variation in the specific resistance and transient thermal conductivity of the p-type, n-type, and p–n-type FTEGs across five different temperature gradients. The plots reveal that as the temperature increased, the specific resistance of the fabricated FTEGs decreased, which can be attributed to the increase in thermally excited charge carriers.³⁴ As the temperature rises, more electrons or holes are activated, leading to a higher carrier concentration. Although phonon

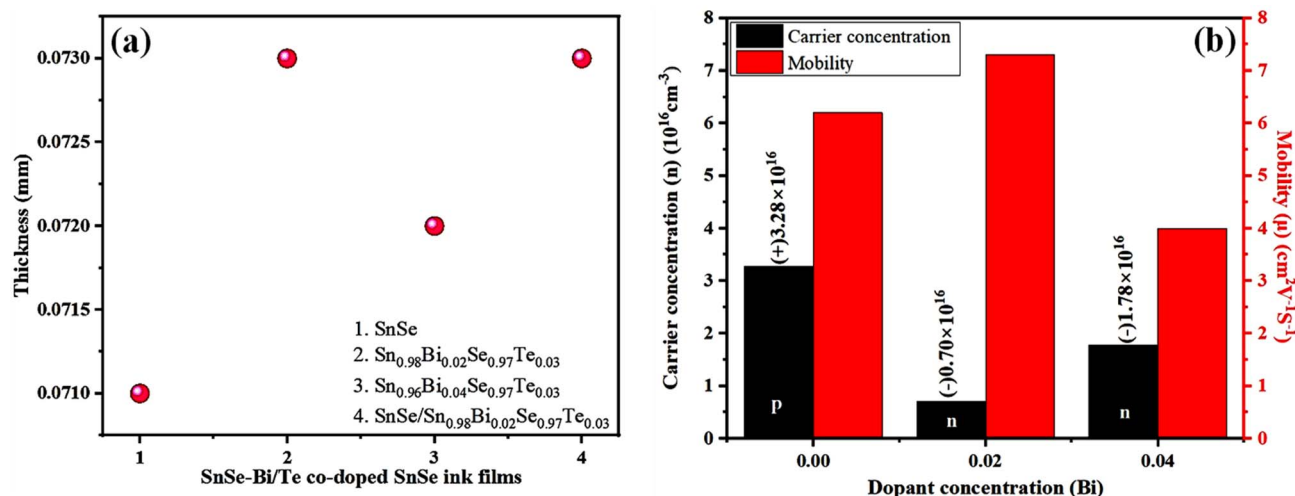
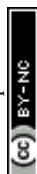


Fig. 7 (a) Thickness values and (b) carrier concentration and mobility graph of ink samples.



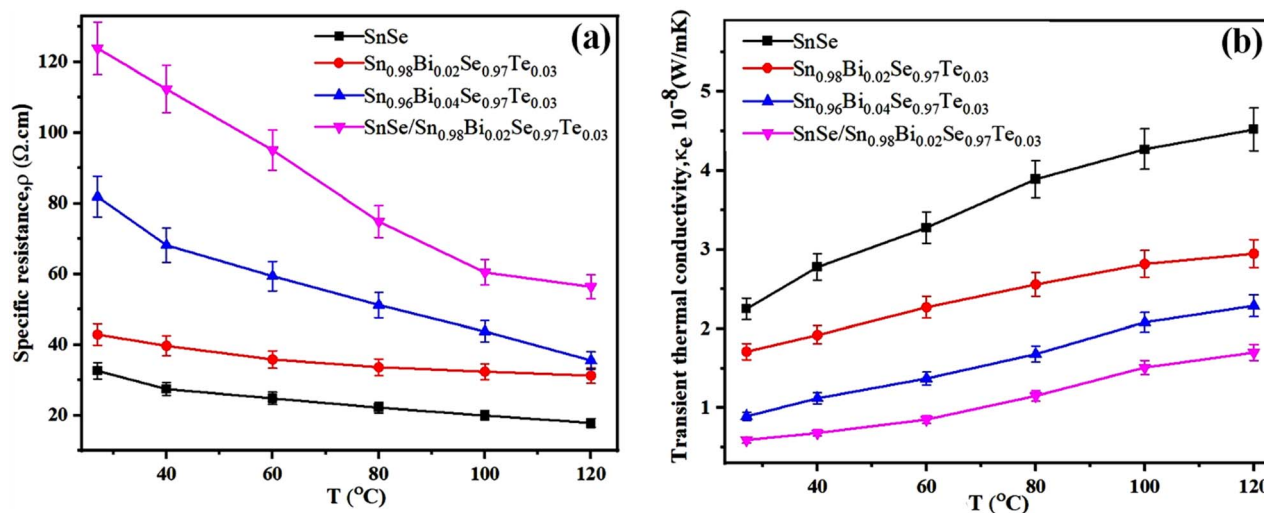


Fig. 8 (a) Specific resistance and (b) transient thermal conductivity (κ_e) graph of the fabricated FTEGs.

scattering generally intensifies at elevated temperatures, the increase in charge carriers compensates for this effect, resulting in an overall reduction in specific resistance. This behavior is typical in semiconductor-based thermoelectric materials.³⁵ Among the four FTEGs, the SnSe (p-type) FTEG exhibited the highest transient thermal conductivity of $4.52 \times 10^{-8} \text{ W m}^{-1} \text{ K}^{-1}$, while the $\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$, $\text{Sn}_{0.96}\text{Bi}_{0.04}\text{Se}_{0.97}\text{Te}_{0.03}$, and $\text{SnSe}/\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$ (p-n type) FTEGs demonstrated the lowest values. The FTEGs showed the lowest thermal conductivity, attributed to the increased microstrain and dislocation density in the Bi/Te-doped SnSe compared to undoped SnSe. These defects promote phonon scattering, reducing thermal transport.

This result correlates well with the XRD findings, where the introduction of Bi and Te dopants caused an increase in microstrain and dislocation density with significant variation in lattice parameters. The atomic size mismatch between the dopants and SnSe contributed to these changes, leading to greater defect formation. These microstructural defects, particularly dislocations, serve as effective phonon scattering centers, hindering thermal conductivity.

3.5 Electrical resistance, Seebeck coefficient, and power output studies

Fig. 9a illustrates the variation of electrical resistance as a function of the temperature difference (ΔT) in the fabricated FTEGs. It is observed that as ΔT increases, the electrical resistance of all FTEGs decreases, demonstrating semiconductor behavior, which is advantageous for thermoelectric applications.³⁶ Compared to the pristine SnSe FTEG with the Bi and Te doped FTEGs, the pristine SnSe FTEG displays lower electrical resistance. In contrast, increasing the concentration of dopants leads to a rise in electrical resistance. This increase can be attributed to changes in mobility and carrier concentration caused by the doping process. Specifically, higher doping levels can disrupt the transport of charge carriers, resulting in increased resistance. For this reason, the doping level has been

limited to 4%, and based on this result, we opted to fabricate a p-n device using SnSe and a 2% Bi-doped sample. Additionally, introducing dopants can create defects within the crystal lattice, which can impede the flow of charge carriers.³⁷ Fig. 9b illustrates the internal voltage (ΔV) generated by the FTEGs at various temperature differences (ΔT). The results indicate a consistent increase in voltage output as temperature rises, which is characteristic of thermoelectric materials. This behavior occurs due to the enhanced thermal excitation of charge carriers that leads to greater mobility, resulting in increased voltage generation. Among the tested devices, the p-n $\text{SnSe}-\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$ FTEG shows the highest voltage output across all ΔT values, likely due to the effective integration of p-type and n-type semiconductors that promote efficient charge carrier transport.

The Seebeck coefficient of the FTEGs (Fig. 9c) was determined by applying a linear fit to the slope of the ΔV versus ΔT data. The pristine SnSe exhibited p-type behavior, characterized by positive thermopower, while the doped SnSe FTEGs displayed n-type behavior due to the additional charge carriers provided by Bi doping.²³ Te co-doping supported this transition by complementing the electronic effects of Bi and aiding in the stabilization of n-type conduction.³² Notably, the p-n $\text{SnSe}/\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$ FTEG achieved the highest Seebeck coefficient at $-1146 \mu\text{V K}^{-1}$, reflecting enhanced thermoelectric performance due to the synergistic effect of p-type and n-type materials. In contrast, the 4% Bi-doped FTEG recorded the lowest Seebeck coefficient ($-207 \mu\text{V K}^{-1}$), which can be attributed to the increased carrier concentration resulting from higher doping levels. According to the Pisarenko relation,³⁸ the Seebeck coefficient decreases with increasing carrier concentration, consistent with our observations. Furthermore, the enhanced carrier-carrier and impurity scattering at high doping levels likely contribute to reduced mobility, collectively leading to the observed decline in thermopower. The power output (P) of the printed flexible thermoelectric generator can be determined using eqn (4):¹⁷



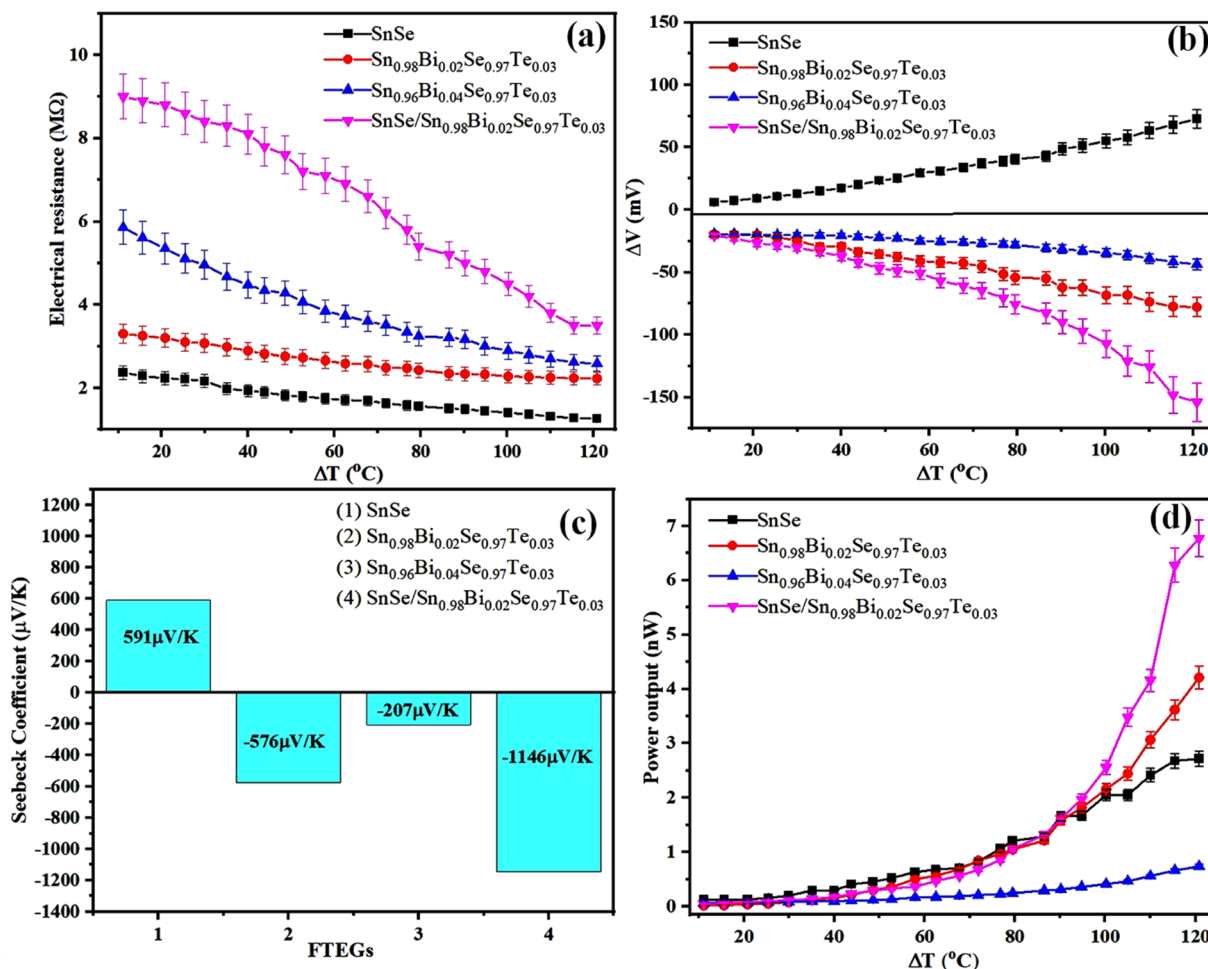


Fig. 9 (a) Electrical resistance, (b) ΔV versus ΔT , (c) Seebeck coefficient, and (d) power output plot of the fabricated FTEGs.

$$P = \frac{V^2}{R} \quad (4)$$

Here, V is the open-circuit voltage across the FTEG measured in volts at a specific temperature gradient, while R is the internal resistance of the FTEG in ohms. The estimated power outputs for the screen-printed p-type, n-type, and p-n-type FTEGs are depicted in Fig. 9d. The p-n type FTEG achieves the maximum power output of approximately 6.8 nW at a temperature difference (ΔT) of 120 °C. This optimal power output is primarily due to the increased internal voltage (Fig. 9b), Seebeck coefficient (Fig. 9c), and decreased transient thermal conductivity (Fig. 8b) in the p-n type FTEG. The combination of p-type and n-type materials considerably affects the thermoelectric properties, as their interaction enhances overall performance. Various factors account for the observed results. First, the presence of the p-n junction facilitates a higher voltage output, as the thermal gradient promotes charge separation, resulting in a greater potential difference across the junction.³⁹ Secondly, the synergistic effect of the p-type and n-type materials optimizes charge carrier dynamics, which improves energy conversion efficiency.³⁹ Lastly, the interplay between thermal and electrical properties in the combined materials enhances the

overall power output of the p-n type FTEG, which is reflected in the significant performance observed at elevated temperature gradients.

Table 3 compares the power output of the p-n type FTEG with some of the standard reported results.

3.6 Flexibility analysis of FTEGs

The flexibility and durability of the screen-printed FTEGs fabricated using SnSe and Bi/Te co-doped SnSe were rigorously evaluated using a customized bending apparatus (Fig. 3e). To assess the impact of mechanical stress on electrical performance, electrical resistance was measured at bending angles of 0°, 30°, 60°, 90°, and 120°. The results showed minimal variation in resistance across these angles, with a maximum deviation of less than 0.5% (Fig. 10a). This minimal change indicates that the FTEGs maintain their electrical integrity even when subjected to significant mechanical deformation. Furthermore, the stability of the devices was tested over multiple bending cycles, specifically after 0, 100, 300, 400, and 500 cycles, revealing only slight variation in internal resistance (Fig. 10b). These findings underscore the exceptional mechanical stability and flexibility of the SnSe and Bi/Te co-doped FTEGs,



Table 3 Reported power output of FTEGs: present work and literature comparison

FTEG material	Fabrication technique	Substrate	Number of leg couples	Seebeck coefficient ($\mu\text{V K}^{-1}$)	Power output (nW)	ΔT (K)	Ref.
PEDOT:PSS	Solution coating	Polyester fabric	5	18.5	12.29	75	40
PEDOT-Tos/carbon	Inkjet printing	Silicon	54	−48.0	1.13	308	41
PEDOT:PSS/Ag	Inkjet printing	Polyimide	8	—	0.24×10^{-3}	5	42
$\text{Sb}_2\text{Te}_3/\text{Bi}_2\text{Te}_3$	Deposition	Silk fabric	12	—	15.00	35	43
$\text{Ag}_2\text{Se}/\text{PVDF}$	Physical mixing + cold pressing	PI	5	95.9	5.00	30	44
Graphene	Printing	Paper	5	−21.5	1.70	60	45
Silver/nickel	Thermal evaporation	Fiber	7	—	2.00	6.6	46
AuCl_3 -doped P3HT	Spin coating	Flip film	20	163.0	1.90	10	47
Cu_2Se	Magnetron sputtering + thermal evaporation	Kapton	10	55.0	3.30	38	48
MWCNT- NH_2/NiO	Screen printing	PET film	15	55.2	1.44	373	21
MWCNT- NH_2/CuO	Screen printing	PET film	15	47.7	1.06	373	21
MWCNT- $\text{NH}_2/\text{Fe}_2\text{O}_3$	Screen printing	PET film	15	43.3	0.32	373	21
$\text{SnSe}/\text{PEDOT:PSS}/\text{MWCNT/DMSO}$	Vacuum filtration	PI	5	22.3	14.70	39	49
$\text{SnSe}-\text{Bi}/\text{Te}$ co-doped SnSe	Screen printing	PET film	8	1146.0	6.80	120	^a

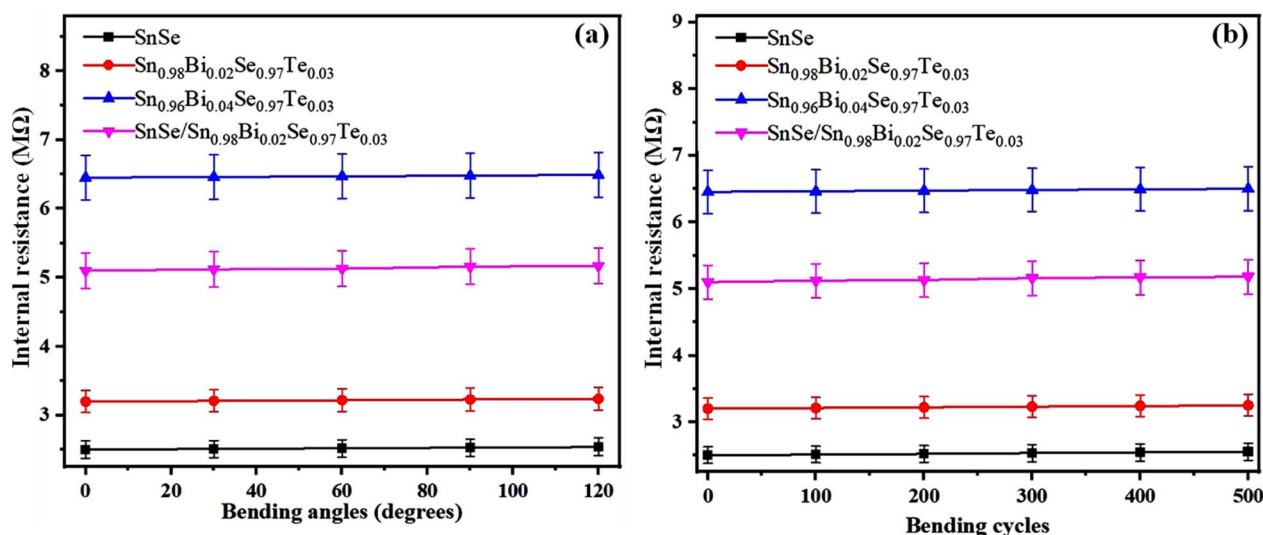
^a Present work.

Fig. 10 (a) Internal resistance vs. bending angle and (b) internal resistance vs. bending cycles of the fabricated FTEGs.

confirming their robustness under repeated stress. As a result, these properties make them highly suitable for practical applications in wearable devices and flexible sensors, where reliability and durability are crucial for optimal performance in dynamic environments.

4 Conclusion

This study explores the thermoelectric performance of SnSe and Bi/Te-doped SnSe flexible thermoelectric generators (FTEGs), fabricated using a low-cost, scalable screen-printing technique. The SnSe and doped SnSe materials were synthesized using the solid-state reaction method, ensuring high purity and phase stability. Characterization techniques, including XRD, SEM,

and EDAX, were employed to assess the structural integrity, morphology, and elemental composition. XRD analysis confirmed the successful incorporation of Bi and Te dopants into the SnSe matrix, leading to modified lattice parameters. SEM and EDAX results demonstrated uniform film morphology and consistent distribution of dopants. The Bi/Te co-doping facilitated a transition from p-type to n-type behavior, optimizing the carrier concentration and significantly enhancing the thermoelectric performance. The p–n-type $\text{SnSe}/\text{Sn}_{0.98}\text{Bi}_{0.02}\text{Se}_{0.97}\text{Te}_{0.03}$ FTEG achieved a peak Seebeck coefficient of $-1146 \mu\text{V K}^{-1}$ and a power output of 6.8 nW at a 120 °C temperature gradient. Transient thermal conductivity measurements indicated a reduction in thermal conductivity, attributed to phonon scattering resulting from defects



introduced by Bi/Te doping. The FTEGs also demonstrated remarkable mechanical flexibility, retaining electrical stability even after 500 bending cycles, with less than 0.5% variation in resistance across different bending angles. These findings highlight the potential of screen-printed Bi/Te co-doped SnSe FTEGs for flexible thermoelectric energy harvesting, offering promising applications in wearable devices and flexible sensors. Future work will focus on optimizing the material composition and improving device efficiency for large-scale commercial usage.

Author contributions

Manasa R. Shankar: conceptualization, analysis, methodology, and original manuscript drafting. A. N. Prabhu: supervision, validation, manuscript review and editing, and resource allocation. Ramakrishna Nayak: resources, technical support, writing review & editing, and supervision.

Conflicts of interest

The authors state that there are no conflicts of interest to disclose.

Data availability

The corresponding author can provide the data used in this study upon request.

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