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On the V_{OC} loss in NiO-based inverted metal halide perovskite solar cells†

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Recent reports have shown that nickel oxide (NiO) when adopted as a hole transport layer (HTL) in combination with organic layers, such as PTAA or self-assembled monolayers (SAMs), leads to a higher device yield for both single junction as well as tandem devices. Nevertheless, implementing NiO in devices without PTAA or SAM is seldom reported to lead to high-performance devices. In this work, we assess the effect of key NiO properties deemed relevant in literature, namely- resistivity and surface energy, on the device performance and systematically compare the NiO-based devices with those based on PTAA. To this purpose, (thermal) atomic layer deposited (ALD) NiO (NiO_{Bu-MeAMD}), Al-doped NiO (Al:NiO_{Bu-MeAMD}), and plasma-assisted ALD NiO (NiO_{MeCp}) films, characterized by a wide range of resistivity, are investigated. Although Al:NiO_{Bu-MeAMD} (~400 Ω cm) and NiO_{MeCp} (~80 Ω cm) films have a lower resistivity than NiO_{Bu-MeAMD} (~10 k Ω cm), the Al:NiO_{Bu-MeAMD} and NiO_{MeCp}-based devices are found to have a modest open circuit voltage (V_{OC}) gain of ~30 mV compared to NiO_{Bu-MeAMD}-based devices. Overall, the best-performing NiO-based devices (~14.8% power conversion efficiency (PCE)) still lag behind the PTAA-based devices (~17.5%), primarily due to a V_{OC} loss of ~100 mV. Further investigation based on light intensity analysis of the V_{OC} and FF and the decrease in V_{OC} compared to the quasi-Fermi level splitting (QFLS) indicates that the V_{OC} is limited by trap-assisted recombination at the NiO/perovskite interface. Additionally, SCAPS simulations show that the presence of a high interfacial trap density leads to a V_{OC} loss in NiO-based devices. Upon passivation of the NiO/perovskite interface with Me-4PACz, the V_{OC} increases by 170–200 mV and is similar for NiO_{Bu-MeAMD} and Al:NiO_{Bu-MeAMD}, leading to the conclusion that there is no influence of the NiO resistivity on the V_{OC} once interface passivation is realized. Finally, our work highlights the necessity of comparing NiO-based devices with state-of-the-art HTL-based devices to draw conclusion about the influence of specific material properties on device performance.

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1. Introduction

The transition toward the production of electricity from renewable sources such as photovoltaics (PV) and wind energy is essential to solve challenges related to climate change, decrease the dependence on non-renewable sources, and strive for a sustainable future. The progress in the field of perovskite solar cells (PSCs) has been enormous with the power conversion efficiency (PCE) rising from 3.8% to 26.7% in the last 15 years.¹ This improvement can be attributed to the engineering of the

optoelectronic properties and stability of the perovskite absorber, as well as the whole device architecture, including the development of efficient and selective charge transport layers.

The inverted planar (p–i–n) architecture is particularly interesting due to lower temperature processing, enabling the application of flexible substrates in combination with a scalable fabrication process and better compatibility in monolithic tandem architecture.² Various hole transport layers (HTLs) have been employed in inverted planar PSCs, ranging from organic polymers such as poly[bis(4-phenyl)(2,4,6-trimethylphenyl)amine] (PTAA),³ poly(3,4-ethylenedioxythiophene)–poly(styrene sulfonate) (PEDOT:PSS)⁴ and in recent years, carbazole-based self-assembling monolayers (SAMs),^{5,6} to metal oxides such as NiO^{7,8} and Cu₂O.⁹ Devices with organic HTLs often have high PCEs but they suffer from poor environmental stability which affects the long-term stability and performance of the PSCs. Another downside is the inhomogeneous surface coverage *via* wet chemistry processing when the HTLs are processed on textured surfaces.¹⁰ NiO has been widely investigated in recent years. Devices with NiO have

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been shown to exhibit higher operational stability as compared to organic HTLs like MeO-2PACz SAM,¹⁰ PTAA,¹¹ PEDOT:PSS,^{4,12} poly-TPD,¹³ etc.

NiO has a wide band gap (3.4–4 eV)^{8,14} resulting in good transmittance (> 80% in 400–800 nm wavelength range)¹⁵ and a deep valence band (5.2–5.4 eV)¹⁴ which aligns well with most of the metal halide perovskite absorbers. It can be deposited by solution processing,^{16,17} as well as scalable vapor deposition techniques such as sputtering¹⁸ and atomic layer deposition (ALD).^{8,10,19} Notably, ALD distinguishes itself from sputtering and solution processing due to its ability to grow conformal and uniform pinhole-free thin films (as low as 6 nm) even on rough, textured surfaces.²⁰ It has often been adopted along with other organic HTLs in p–i–n single junction PSCs¹⁰ as well as in the tunnel recombination junction (TRJ) of perovskite-silicon²¹ and perovskite-CIGS tandem devices,²² to prevent the above-mentioned inhomogeneous surface coverage of solution-processed organic HTLs on the substrate underneath. It has also been shown to enable the homogeneous formation of SAM due to the presence of a higher concentration of hydroxyl groups than ITO, which is essential for the chemisorption of SAM.¹⁰ Additionally, it reduces differences in surface potential arising due to distinct microstructures of the underneath ITO film.^{20,23}

Despite these advantages, champion devices using only NiO as HTL are rarely reported, mostly due to a loss in the open circuit voltage (V_{OC}) and fill factor (FF) affecting the PCE.^{8,10,22,24} This has often been attributed to the high electrical resistivity of pristine NiO (10^4 – 10^7 Ω cm)^{17,25,26} which can affect hole transport. Decreasing the resistivity has been shown to result in efficient charge extraction and lower recombination due to reduced hole accumulation at the NiO/perovskite interface, thereby increasing the V_{OC} .^{7,27–29} Moreover, the series resistance reduces, thereby improving the FF. For instance, Koushik *et al.* achieved a ~ 40 mV V_{OC} and $\sim 9\%$ FF gain by lowering the lateral resistivity of NiO from 870 Ω cm to 170 Ω cm by post-annealing.⁸ Doping NiO with cations such as Li,³⁰ Cu,³¹ Al,^{17,32,33} Co,³⁴ Ag,³⁵ Zn,³⁶ etc., also lowers NiO resistivity, improves the V_{OC} and modifies the band alignment of NiO HTL with respect to perovskite.

Other approaches involving physical and chemical post-treatments have also been shown to improve the bulk properties of NiO or the NiO/perovskite interface helping in improving the device performance.^{8,27,37} Modifying the surface energy of NiO has been shown to influence the growth of perovskite which can affect the perovskite morphology and the charge transport properties.^{38,39} Finally, modifying the NiO/perovskite interface by introducing an organic interlayer such as PTAA,²² or SAM such as [4-(3,6-dimethyl-9H-carbazol-9-yl)butyl]phosphonic acid (Me-4PACz),^{18,40} etc. has also been reported to lead to a gain in V_{OC} and FF as compared to the NiO-only based devices.^{8,10,18,22,24,41}

Very often, the above-described experimental efforts do not include a comparison between NiO-only-based devices and state-of-the-art organic HTL-based devices. The lack of comparison makes it challenging to draw conclusions about the efficacy of the chosen approach to improve the bulk and/or surface

properties of NiO towards high-efficiency devices. Therefore, in this study, we have investigated three different ALD NiO HTLs with variable lateral resistivity and surface energy and have compared the NiO-based device's performance to PTAA-based devices. For this purpose, a novel ALD process to dope NiO with aluminium using the sequential precursor approach is reported in this work. Al is chosen to dope the most resistive NiO as it leads to a decrease in NiO resistivity without affecting its transmittance, unlike other dopants such as Co,⁴² Li,^{43,44} etc. We observe that lowering the resistivity of NiO results in a slight V_{OC} gain of ~ 30 mV, with respect to the most resistive NiO-based device. However, all NiO-based devices have an average PCE of $\sim 14.8\%$ and they lag behind the PTAA-based devices due to a major V_{OC} loss of ~ 100 mV. This V_{OC} loss is recovered by surface modification of NiO with Me-4PACz, thereby attributing the lower performance to the quality of the NiO/perovskite interface. Light intensity dependence of photovoltaic parameters, time-resolved photoluminescence (TR-PL) and absolute photoluminescence point out that interface recombination phenomena limit hole extraction from the perovskite to NiO, thereby leading to the V_{OC} loss. Furthermore, SCAPS (solar cell capacitance simulator) simulation is carried out to validate our experimental results and show the effect of interfacial defect states on the device and the V_{OC} . We also argue that the conclusions drawn so far in literature on the beneficial role of the NiO conductivity on the device performance^{17,33,45–47} can be shadowed by the interface loss phenomena between NiO and perovskite.

2. Methods

2.1. Atomic layer deposition of NiO

The undoped and aluminium-doped thermal ALD NiO processes (hereafter, referred to as NiO_{Bu-MeAMD} and Al:NiO_{Bu-MeAMD} respectively) are carried out in the commercial FlexALTM MK1 (Oxford Instruments) ALD reactor and the plasma-assisted ALD NiO process (hereafter, referred to as NiO_{MeCp}) is developed in the home-built ALD reactor previously introduced.^{8,20} Both ALD reactors are high vacuum systems equipped with a rotary and turbomolecular pumping unit that can reach a base pressure of 10^{-6} Torr. All the processes are carried out at a deposition temperature of 150 °C. The detailed process description of the NiO_{Bu-MeAMD} and NiO_{MeCp} and the ALD process development of the Al:NiO_{Bu-MeAMD} are mentioned in Section S1 (ESI†) and summarised in Table 1.

2.2. NiO film characterization

The thickness of the NiO films is determined by spectroscopic ellipsometry (SE) (NIR Ellipsometer M2000, J.A. Woollam Co.). For this purpose, NiO film is deposited on c-Si (100) substrates, and the growth is monitored by *in situ* SE (VIS Ellipsometer M200, J.A. Woollam Co.). The ellipsometry spectra of the NiO are modelled by a Cauchy dispersion model in the wavelength range of 1.25–3.5 eV. The SE data of the c-Si and the native SiO₂ are modelled according to the model reported by Herzinger *et al.*⁴⁹ The elemental composition of the NiO films is analyzed



Table 1 Overview of the different ALD processes used in this work. GPC refers to the growth per cycle of the ALD process

ALD process	ALD precursor and co-reactant	Deposition temperature (°C)	GPC (Å/cycle)	Ref.
NiO _{Bu-MeAMD}	(<i>N,N'</i> -di- <i>tert</i> -butylacetamidinato)Nickel(II) + water	150	0.43 ± 0.01	20
Al:NiO _{Bu-MeAMD}	NiO: (<i>N,N'</i> -di- <i>tert</i> -butylacetamidinato)Nickel(II) + water Al-dopant: dimethyl aluminium isopropoxide + water	150	0.44 ± 0.02	Developed in this work
NiO _{MeCp}	bis-methylcyclopentadienyl nickel + oxygen plasma	150	0.29 ± 0.01	8 and 48

by Rutherford backscattering spectroscopy (RBS) and elastic recoil detection (ERD) using a 3.5 MV Singletron with a 1000 keV He⁺ beam at an angle almost perpendicular to the sample (170°) and also a glancing angle. For this purpose, the Al:NiO_{Bu-MeAMD} film is deposited on glassy carbon substrates with 50 nm Ti oxide on top instead of c-Si substrates to prevent overlapping of the RBS spectrum of Al with Si substrate. Furthermore, X-ray photoelectron spectroscopy (XPS) is carried out in the Thermo Scientific KA1066 spectrometer using monochromatic Al K α X-rays with an energy of 1486.6 eV and without any pre-sputtering. The data is analyzed by the Advantage software with a Smart background (Shirley background with offset correction) subtraction. The results are corrected for sample charging using the C1s orbital as a reference with a binding energy of 284.8 eV. The transmittance of the NiO films is characterized using ultraviolet-visible (UV-Vis) spectrophotometry using a PerkinElmer LAMBDA 1050 spectrophotometer. The measurement is carried out inside an integrating sphere by exciting it from the glass/ITO side. The crystal structure of the NiO films on c-Si substrates is analyzed by X-ray diffraction (XRD) in a Bruker D8 discover using a Cu K α (λ = 1.54 Å) radiation in the range of 30°–80° with a step size of 0.04° and a time per step of 10 s in the grazing incidence (GI) mode with an incidence of 0.4°. The contact angle of different liquids on the NiO surface is measured using a contact angle goniometer (Ramé-Hart Incorporation). The surface energy of the NiO is calculated by the Owens–Wendt–Rabel and Kaelble Model using 3 liquids with different ratios of the polar and dispersive components: ethanol, water, and ethylene glycol. The lateral resistivity of the NiO films is measured at room temperature using a Signatone four-point probe in combination with a Keithley 2400 source meter, right after deposition. For this purpose, the films are deposited on c-Si/450 nm SiO₂ substrates.

2.3. Solar cell fabrication

Patterned soda-lime glass/indium tin oxide (ITO) (BBBB configuration, 3 × 3 cm²) from Naranjo is used for the single junction PSCs. These substrates are first cleaned in soap water, de-ionized water, and isopropanol (IPA) in subsequential steps for 15 minutes in an ultrasonic bath at room temperature. They are left in IPA overnight followed by an additional cleaning of 15 minutes in an ultrasonic bath. Then, they are dried with an N₂ gun. Next, the hole transport layer (HTL) – PTAA or NiO, is deposited. For the PSCs with poly(triarylamine)(PTAA) as the HTL, a 2 mg mL^{−1} solution of PTAA (Solaris Chem) in toluene (Sigma-Aldrich) is spin-coated on the ITO at 5000 rpm for 35 s followed by annealing at 100 °C for 10 minutes. The ALD processes for NiO_{MeCp}, Al:NiO_{Bu-MeAMD} and NiO_{Bu-MeAMD} are

described above. The resultant film thickness for the Al:NiO_{Bu-MeAMD} with a cycle ratio '2m:1' = 135 is ~11 nm and the film thicknesses of the undoped NiO_{Bu-MeAMD} and NiO_{MeCp} are also kept at ~11 nm for a fair comparison. For the NiO-based PSCs with the [4-(3,6-dimethyl-9H-carbazol-9-yl)butyl]-phosphonic acid (Me-4PACz) SAM, a 1 mM Me-4PACz (Tokyo Chemical Industry, >99.0%) solution is prepared by dissolving it in anhydrous ethanol (Sigma-Aldrich), followed by sonication in an ultrasonic bath for 30 minutes for full dispersion. The Me-4PACz SAM is deposited on NiO_{Bu-MeAMD} and Al:NiO_{Bu-MeAMD} via static spin-coating by dropping 120 μ L of the solution and waiting for 90 s for the solution to spread, followed by 30 s of spin-coating at 3000 rpm. Then, the layers are annealed at 100 °C for 10 minutes.

In this study, a dual-cation perovskite Cs_{0.15}FA_{0.85}Pb-(I_{0.92}Br_{0.08})₃ solution is used. The procedure followed is reported by Bracesco *et al.*⁵⁰ In short, it is prepared by mixing a stoichiometric amount of the following precursors: PbI₂ (Alfa Aesar, 99.999%) and PbBr₂ (Tokyo Chemical Industry, 99.9%), FAI (Greatcell (Dyesol), 99.9%), and CsI (Alfa Aesar, 99.9%), in anhydrous DMF: DMSO (Sigma-Aldrich, >99.9%) (9:1 volume ratio), to achieve a concentration of 1.33 M. Then, the perovskite solution is stirred overnight at room temperature. It is spin-coated with a two-step procedure: at 2000 rpm for 10 s and 5000 rpm for 30 s. During the first step, 110 μ L precursor solution is dynamically spin-coated on the substrate 5 s after the start of the program. Next, 250 μ L chlorobenzene (Sigma-Aldrich) anti-solvent is dropped on the spinning substrate 21 s after the start of the program to quench the film and form a smooth and compact layer. The film is subsequently annealed on a hotplate at 100 °C for 10 min.

Phenylethylammonium iodide (PEAI, Sigma Aldrich, 98%) is used for passivating the ETL- perovskite interface, in some devices (mentioned in the text), by spincoating 100 μ L of 1.5 mg mL^{−1} of PEA solution in isopropanol, at 3000 rpm for 45 s followed by annealing at 100 °C for 10 min. For the electron transport layer (ETL), a 20 mg mL^{−1} PCBM (Solenne B.V., 99%) solution in chlorobenzene is spin-coated onto the perovskite layer at 1500 rpm for 50 s. This is followed by the spin-coating of 1 mg mL^{−1} bathocuprine (Sigma-Aldrich, 96%), in short BCP, solution in ethanol (SigmaAldrich) at 5000 rpm for 30 s. Finally, the 100 nm Cu electrode is deposited by thermal evaporation of Cu (Kurt J. Lesker Co.) at a pressure of 10^{−6} mbar using a shadow mask, defining an active device pixel area of 0.09 cm².

2.4. Device characterization

The current density–voltage (*J*–*V*) measurements are carried out in a WACOM solar simulator which is calibrated with a Si



reference cell from Fraunhofer ISE to simulate the AM1.5 spectrum. The cell area of 0.09 cm^2 is defined by a stainless-steel shadow mask. The scan speed is 200 mV s^{-1} with a step size of 20 mV controlled by a Keithley 2400 source-measure unit. The maximum power point tracking (MPPT) is performed for 3 minutes. The light intensity-dependent J - V measurements are carried out in a nitrogen-filled glovebox under a white light halogen lamp. The light intensity is calibrated to 100 mW cm^{-2} using a silicon reference cell to simulate the AM 1.5 spectrum. A set of neutral filters are used to obtain different intensities of 1, 0.83, 0.53, 0.33, 0.1, 0.01, and 0.001 suns. A Keithley 2400 is used to measure the J - V curves with a scanning rate of 200 mV s^{-1} with a 20 mV step. The EQE measurements are performed using a home-built modulated monochromatic probe light to illuminate the solar cell. Absolute photoluminescence (PL) is measured using a 455 nm fibre-coupled LED (Thorlabs) source with an intensity of 1 sun to excite the perovskite film through an optical fibre placed in an integrating sphere (Avantes, AvaSphere30-REFL) fitted with a 550 nm short-pass filter (Edmund Optics). The spectrum is measured through an optical fibre connected to a calibrated spectrometer (Avantes, AvaSpec-HERO) using a 550 nm long-pass filter.

The time-integrated photoluminescence (PL) and time-resolved photoluminescence (TRPL) measurements of the perovskite deposited on the ITO/NiO (or PTAA) are carried out in a home-built setup. The samples are excited using a laser with an excitation wavelength of 420 nm , a repetition frequency of 5 MHz , and an excitation power of around $35 \text{ }\mu\text{W}$. The spot size is around $83 \pm 8 \text{ }\mu\text{m}$ and the fluence is around $128 \pm 13 \text{ nJ cm}^{-2}$. The structural properties of the perovskite film are analyzed by XRD in the Bragg-Brentano mode in the full range, *i.e.* 11.4° to 55° with a step size of 0.01° and a time per step of 0.2 s . GI-XRD measurements are carried out in the range of 11.4° to 16° with a step size of 0.05° and a time per step of 10 s with an incidence angle of 1° to check the presence of PbI_2 . The morphology is checked by scanning electron microscopy (SEM) imaging carried out using a JEOL JSM-7500FA. The images are made using an accelerating voltage of $1.5\text{--}2 \text{ kV}$ and a probe current of $7 \text{ }\mu\text{A}$.

3. Results and discussions

All three NiO films – $\text{NiO}_{\text{Bu-MeAMD}}$, $\text{Al:NiO}_{\text{Bu-MeAMD}}$ and NiO_{MeCp} , are slightly oxygen-rich with a Ni/O ratio in the range of 0.88--

0.96 ± 0.03 . This is consistent with what has been reported in the literature so far.^{13,51} The concentration of Al, as quantified from RBS (Table S1, ESI†), in the $\text{Al:NiO}_{\text{Bu-MeAMD}}$ film is $0.9 \text{ at}\%$. A detailed characterization of these NiO films can be found in Section S2 (ESI†). The lateral resistivity of the NiO films along with other key material properties discussed later in the study, are reported in Table 2. The resistivity of the $\text{NiO}_{\text{Bu-MeAMD}}$ is nearly a factor of 200 higher than NiO_{MeCp} . It decreases to $\sim 400 \text{ }\Omega \text{ cm}$ upon doping with Al, as it creates shallow acceptor states at low Al at% ($< 2.5\%$).^{52,53} The work function, reported in Table 2, is however not affected by Al-doping because the Al-rich region in $\text{Al:NiO}_{\text{Bu-MeAMD}}$, is buried under a NiO layer according to the supercycle approach and the UPS, with a penetration depth of $\sim 2 \text{ nm}$, is sensitive to the NiO top layer. The energy difference between the VBM and W.F. decreases from $0.87 \pm 0.07 \text{ eV}$ for the $\text{NiO}_{\text{Bu-MeAMD}}$ to $0.75 \pm 0.01 \text{ eV}$ upon doping it with Al.

In this study, we have adopted a p-i-n architecture consisting of glass/ITO/NiO/ $\text{Cs}_{0.15}\text{FA}_{0.85}\text{Pb}(\text{I}_{0.92}\text{Br}_{0.08})_3/\text{PCBM}/\text{BCP}/\text{Cu}$ and compared it to a reference device with the conventional PTAA as HTL. Fig. 1 and Table 3 report the J - V parameters of the devices. The devices with undoped $\text{NiO}_{\text{Bu-MeAMD}}$ yield a PCE of $\sim 14.8\%$, with a J_{SC} of $\sim 20.6 \text{ mA cm}^{-2}$, an FF of $\sim 77\%$, and a V_{OC} of $\sim 940 \text{ mV}$. There is a gain of $\sim 30 \text{ mV}$ in V_{OC} for $\text{Al:NiO}_{\text{Bu-MeAMD}}$ and NiO_{MeCp} -based devices, likely due to the decrease in resistivity when compared to the $\text{NiO}_{\text{Bu-MeAMD}}$. The improvement in V_{OC} is in line with what has been observed in literature.^{8,17,27,33} There is also a slight gain in J_{SC} for $\text{Al:NiO}_{\text{Bu-MeAMD}}$ whereas the J_{SC} decreases to $\sim 20.3 \text{ mA cm}^{-2}$ for NiO_{MeCp} -based devices. The difference in J_{SC} is corroborated by the integrated current ($J_{\text{SC,EQE}}$) calculated from the EQE spectra obtained for the different HTL-based devices, as shown in Fig. S7 (ESI†) and reported in Table S2 (ESI†). The slight loss in the J_{SC} observed for the NiO_{MeCp} -based devices, as compared to those based on $\text{NiO}_{\text{Bu-MeAMD}}$ and $\text{Al:NiO}_{\text{Bu-MeAMD}}$, is confirmed by the lower $J_{\text{SC,EQE}}$. This also corresponds to the slightly lower transmittance of NiO_{MeCp} , as shown in Fig. S4 (ESI†).

Despite the lower resistivity of the $\text{Al:NiO}_{\text{Bu-MeAMD}}$ and NiO_{MeCp} , their corresponding devices suffer from a FF loss, specifically up to 3% absolute value with respect to the $\text{NiO}_{\text{Bu-MeAMD}}$ -based devices. Thus, in the case of $\text{Al:NiO}_{\text{Bu-MeAMD}}$, the overall gain in J_{SC} and V_{OC} balances the loss in FF, resulting in a PCE of $\sim 14.8\%$. The NiO_{MeCp} -based devices also exhibit PCE similar to the $\text{Al:NiO}_{\text{Bu-MeAMD}}$ and $\text{NiO}_{\text{Bu-MeAMD}}$. The FF loss

Table 2 Bulk and surface material properties of $\text{NiO}_{\text{Bu-MeAMD}}$, $\text{Al:NiO}_{\text{Bu-MeAMD}}$ and NiO_{MeCp} . The resistivity data was determined from 5 samples each of $\text{NiO}_{\text{Bu-MeAMD}}$ and $\text{Al:NiO}_{\text{Bu-MeAMD}}$ and 15 samples of NiO_{MeCp} . The mass density of the NiO is calculated from RBS measurements and the thickness derived from spectroscopic ellipsometry analysis

	$\text{NiO}_{\text{Bu-MeAMD}}$	$\text{Al:NiO}_{\text{Bu-MeAMD}}$	NiO_{MeCp}
Resistivity ($\Omega \text{ cm}$)	$(1.7 \pm 0.3) \times 10^4$	430 ± 97	70 ± 20
Preferred crystal orientation	(111)	(111)	(200)
Surface energy (mN m^{-1})	91 ± 1	85 ± 1	50 ± 1
Transmittance (%) (400–800 nm)	84	85	82
Density (g cm^{-3})	5.1 ± 0.3	4.5 ± 0.3	7.0 ± 0.7
Work function (eV)	4.47 ± 0.05	4.47 ± 0.05	4.70 ± 0.05
Valence band maximum (eV)	5.34 ± 0.05	5.21 ± 0.05	5.46 ± 0.05



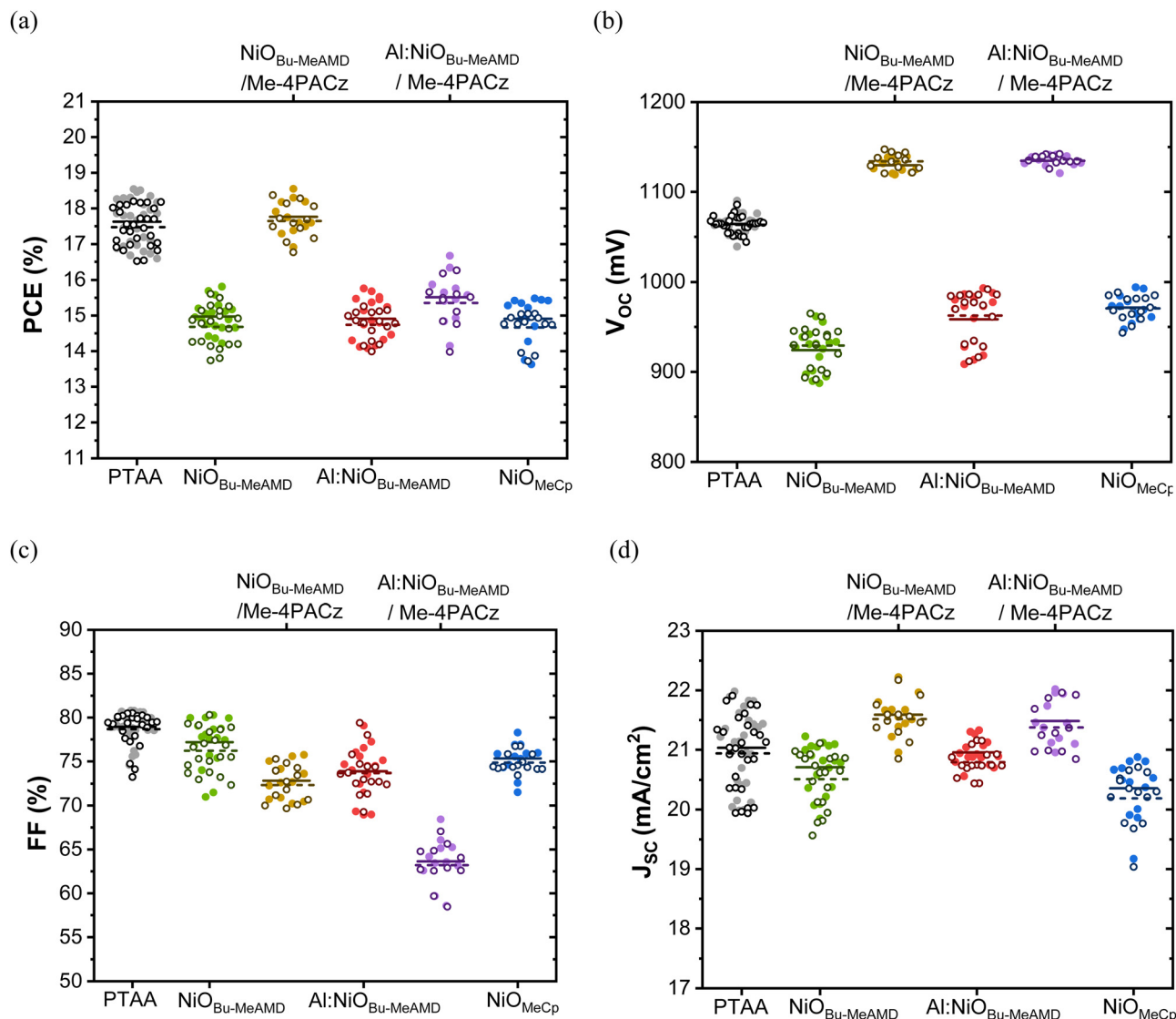


Fig. 1 PV parameters of the different HTL-based devices (a) PCE, (b) V_{OC} , (c) FF, and (d) J_{SC} . The filled circles and the solid line represent the reverse $J-V$ scan whereas the empty circles and the dashed line represent the forward $J-V$ scan. The solid and dashed lines represent the mean value of the PV parameter during the reverse and forward scans respectively. These data are collected from several batches on 30 devices with PTAA, 20 devices with NiO and 15 devices with NiO/SAM.

Table 3 Average values of PV parameters, shunt (R_{shunt}) and series resistance (R_{series}) along with their standard deviation of the different HTL-based PSCs. The R_{shunt} and R_{series} are calculated from the slope of the $J-V$ curve at the J_{SC} and V_{OC} respectively. This data is collected from several batches on 30 devices with PTAA and 20 devices with NiO

	V_{OC} (mV)	J_{SC} (mA cm ⁻²)	FF (%)	PCE (%)	R_{series} (Ω cm ²)	R_{shunt} (k Ω cm ²)
NiO _{Bu-MeAMD}	937 ± 33	20.6 ± 0.4	77 ± 3	14.8 ± 0.6	4.0 ± 0.5	2.7 ± 1.2
Al:NiO _{Bu-MeAMD}	961 ± 28	20.9 ± 0.2	74 ± 3	14.8 ± 0.5	4.7 ± 0.7	2.9 ± 0.7
NiO _{MeCp}	971 ± 14	20.3 ± 0.5	75 ± 1	14.8 ± 0.5	3.9 ± 0.4	2.4 ± 0.4
PTAA	1064 ± 10	20.9 ± 0.6	79 ± 2	17.5 ± 0.7	3.7 ± 0.3	3.8 ± 1.2

observed in the Al:NiO_{Bu-MeAMD} and NiO_{MeCp}-based devices can be due to the higher R_{series} and lower R_{shunt} (see. Table 3), respectively when compared to the NiO_{Bu-MeAMD}-based devices. Studies have shown that especially at a low R_{shunt} ($< 10^5$ k Ω cm²), a small increase in R_{series} or a decrease in R_{shunt} can lead to a loss

of 1–3% in FF.^{54,55} Here, it should be noted that R_{series} depends on the resistance of the individual layers as well as the interfacial contact resistance between the layers. The presence of hydroxyl groups has been shown to increase the interface contact resistance between NiO and perovskite.⁵⁶ We have detected hydroxyl



groups on NiO surface based on ERD measurements (see, Table S1, ESI†) and the O1s spectra (Fig. S3(b), ESI†). The H present in metal oxides is in the form of hydroxyl groups and thus, the H content in the films provides information on the hydroxyl content.^{57,58} The Al:NiO_{Bu-MeAMD} layer has a higher concentration of hydroxyl groups (~ 13 at. nm⁻³ as compared to ~ 10 at. nm⁻³ for NiO_{Bu-MeAMD}) and in parallel, the OH feature contribution at 531 eV in the O1s spectra confirms such a difference between the 2 layers. Since the Al:NiO_{Bu-MeAMD} layer has a lower resistivity than NiO_{Bu-MeAMD} and we observe no influence of the NiO properties on the perovskite bulk properties, specifically crystallinity and morphology (discussed later), we argue that the larger (relative) concentration of hydroxyl groups on the Al:NiO_{Bu-MeAMD} surface leads to an increase in interface contact resistance with the perovskite.

When compared with NiO-based PSCs, PTAA-based PSCs exhibit a higher PCE of $\sim 17.5\%$. The J_{SC} of the PTAA-based PSCs is similar to the NiO-based PSCs but the FF is slightly higher by 2–5% in absolute value. The R_{shunt} of the PTAA-based devices is slightly higher and its R_{series} is slightly lower than the NiO-based devices, resulting in the increase in FF. From this, we conclude that the performance of the NiO-based devices lags behind those based on PTAA, despite decreasing NiO film resistivity, due to a significant V_{OC} loss in the range of 90–130 mV.

The ideality factor is calculated from the light intensity dependence of the V_{OC} ⁵⁹ to gain insight into the dominant recombination mechanism affecting the V_{OC} . The PTAA-based devices have an average ideality factor of ~ 1.6 (Fig. 2) indicating that Shockley–Read–Hall (SRH) recombination within the perovskite bulk is the dominant recombination mechanism. For all the NiO-only-based devices, the ideality factor is in the range of 1.1–1.3. It has been previously noted that the ideality factor is not always a superposition of the SRH and bimolecular

recombination, but it is also affected by interface recombination, especially in devices with low PCE and V_{OC} .^{60,61} Moreover, when we take care of passivating the other interface *i.e.* the ETL (PCBM)/perovskite interface by PEAI, the ideality factor still remains around 1.1 (Fig. S8, ESI†). This suggests that the NiO–perovskite interface is non-ideal and limits the device performance.

At the same time, we observe that the V_{OC} loss in the NiO-based devices is recovered after modifying the NiO/perovskite interface by the Me-4PACz SAM interlayer (see, Fig. 1(b)). The increase in the V_{OC} hints at better charge extraction induced by Me-4PACz. Similar large improvement in V_{OC} (100–200 mV) has also been observed in the literature when using a carbazole-based SAM interlayer between NiO and perovskite, irrespective of the deposition method of NiO.^{10,62–66} The ideality factor of these devices also increases to ~ 1.8 indicating that interface recombination is mitigated and that SRH charge recombination primarily occurs in the bulk perovskite. Therefore, we confirm that the reason behind the loss in NiO-only-based device performance is interface recombination and any beneficial effect from the decrease in NiO resistivity may be shadowed by phenomena at interfaces. The role of the SAM will be discussed in detail later in the manuscript.

Next, we analyze the potential causes behind the poor quality of the NiO/perovskite interface. We first investigate the crystallographic properties of NiO and its surface chemistry and their effect on the perovskite morphology and bulk properties. The crystallographic properties of NiO films, as determined by GI-XRD, are shown in Fig. S5 (ESI†). The preferential crystal orientation of undoped and Al:NiO_{Bu-MeAMD} is (111) while it is (200) for NiO_{MeCp}. The crystal orientation of the NiO has been shown to influence the surface energy of the film with the (111) orientation being polar in nature.⁶⁷ This results in a higher surface energy (and higher hydrophilicity) of the Al:NiO_{Bu-MeAMD} and NiO_{Bu-MeAMD} than NiO_{MeCp} (see, Table 2). However, no visible difference in the wettability of the perovskite solution during spin-coating as well as of the film coverage is observed on the different NiO films. Top-view SEM images in Fig. S9 (ESI†), show that there is only a small difference in the average grain size of the perovskite grown on hydrophobic PTAA and NiO_{MeCp} (grain in the diameter range of ~ 160 – 170 nm) and Al:NiO_{Bu-MeAMD} and NiO_{Bu-MeAMD} (both in the range of ~ 190 – 200 nm). All perovskite films are without voids and pinholes, therefore the presence of local shunt pathways is excluded. Furthermore, no bright needle-shaped grains signifying the presence of lead iodide (PbI₂) are observed on top of the perovskite layers which could have been from unreacted precursor material or due to storage and exposure.⁶⁸

XRD measurements are carried out on the ITO/NiO (or PTAA)/perovskite/tri-octyl phosphine oxide (TOPO) stack to seek any difference in the bulk crystallographic properties of the perovskite processed on top of the different HTLs. The thin layer of TOPO is spin-coated on top of the stack to limit exposure to ambient air. As shown in Fig. 3, the main diffraction peaks of the perovskite films are at 14.0° , 19.9° , 28.3° , 31.7° , 34.8° and 40.5° corresponding to the (100), (110), (200),

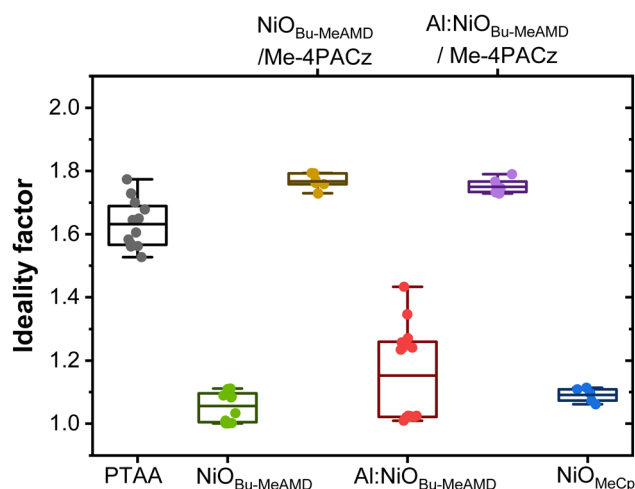


Fig. 2 Ideality factor of different HTL-based devices (without PEAI passivating the ETL/perovskite interface), inferred from the slope of the V_{OC} vs. light intensity graph. The box represents 25–75% percentile of the distribution with the mean as the middle line. The whiskers are 5 and 95% percentile of the distribution and the rest are outliers.

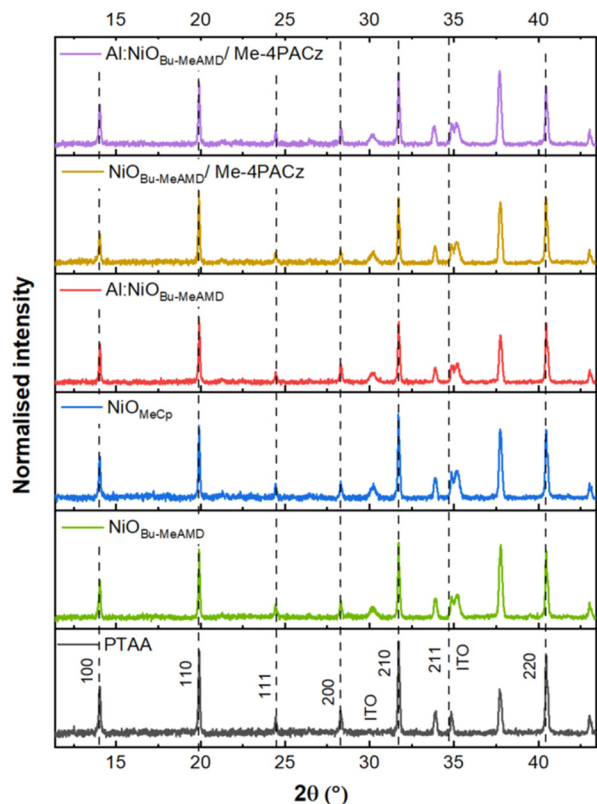


Fig. 3 XRD pattern of perovskite layers deposited on different HTLs.

(210), (211) and (220) crystal planes, respectively.⁶⁹ The peaks at 30° and 35° can be assigned to the ITO⁷⁰ and the peak at 37.5° is a contribution of the XRD sample holder. There is a negligible difference in the XRD peak positions and intensity for the different HTL-based stacks, indicating that the perovskite crystallographic quality is not affected by the surface properties of the HTLs underneath. In conclusion, the surface energy of NiO is not expected to be the cause of the V_{OC} loss addressed earlier because it has no effect on the perovskite solution wettability and its bulk properties.

The presence of PbI_2 in NiO-based devices has been reported in literature to lead to a V_{OC} loss in the devices. This has often been attributed to a redox reaction at the NiO/perovskite interface between the Ni^{3+} sites and the organic A-site cation of perovskite.^{13,71} Also, it has been shown that PbI_2 is formed due to the decomposition of perovskite when it is in direct contact with the hydroxyl groups on the NiO surface.^{72–74} Boyd *et al.* suggested that the formation of the $PbI_{(2-x)}Br_x$ compound can impede charge extraction by NiO and result in V_{OC} loss.¹³ Interestingly, in our study, we do not consistently observe the PbI_2 peak at $\sim 12.6^\circ$ across our several batches. XRD measurement carried out after a month to evaluate whether the NiO/perovskite stack undergoes a pronounced degradation upon ageing in an inert atmosphere,⁷⁵ shows that PbI_2 is not consistently present in all the different NiO-based stacks, as shown in Fig. S10 (ESI[†]), *i.e.* it is absent in the NiO_{MeCp} -based half stack, whereas it is present in the other NiO-based stacks. Moreover, we detect PbI_2 also in the case of $NiO_{Bu-MeAMD}/Me-$

4PACz and PTAA-based stack. GIXRD measurements (Fig. S10(b), ESI[†]) carried out on the same stacks at an incidence angle of 1° to solely probe the perovskite bulk, indicate that this PbI_2 is present in the bulk of the perovskite. From this, we would like to point out that Bragg-Brentano XRD measurements, generally reported in literature studies,^{13,71} is not sufficient to conclude whether PbI_2 is present in the bulk or at the interface. Instead, varying the incidence angle of X-rays in GIXRD measurements can be useful to probe different penetration depths in perovskite.

We further look into absolute photoluminescence (PL) measurements to quantify the non-radiative recombination loss calculated from the quasi-Fermi level splitting (QFLS) of the devices and elucidate the reason behind this loss. For this purpose, only the $NiO_{Bu-MeAMD}$ and PTAA-based devices are compared. The QFLS of the NiO-based partial stack has a 40 mV loss with respect to the PTAA-based stack indicating that the latter forms a better interface with the perovskite (Fig. 4). Addition of the ETL and the top electrode to the partial stack results in a ~ 60 mV loss in the implied V_{OC} in both NiO and PTAA-based devices which can be due to non-radiative recombination at the perovskite/PCBM interface. The implied V_{OC} from the QFLS for the PTAA-based devices coincides with the externally measured V_{OC} . This shows that the QFLS and externally measured V_{OC} are governed by the same recombination mechanism. On the other hand, the devices with $NiO_{Bu-MeAMD}$ suffer from ~ 200 mV loss in V_{OC} with respect to the implied V_{OC} . This can be due to a band misalignment between the valence band levels and/or fast interface recombination.^{60,76} There is no indication of a band misalignment for the NiO/perovskite as the valence band maximum (VBM) of the $NiO_{Bu-MeAMD}$ is 5.3 eV (see, Table 2 and Fig. S6, ESI[†]) and is similar to that of PTAA (5.20 eV⁷⁷). On the other hand, a high interface recombination rate at the NiO/perovskite interface can deplete hole density near this interface resulting in asymmetric band bending of the hole quasi-Fermi level ($E_{F,hole}$) in the perovskite absorber and in turn, leading to a decrease in the V_{OC} of the NiO-based

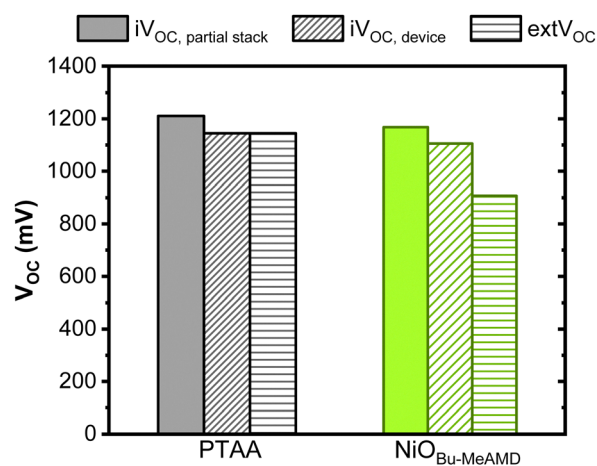


Fig. 4 QFLS (implied V_{OC}) of the PTAA and $NiO_{Bu-MeAMD}$ -based partial stacks and devices calculated from absolute PL and compared with the externally measured V_{OC} .

devices.^{60,76} The decrease in implied V_{OC} indicates that a high rate of interface recombination affects the NiO-based devices.

The light intensity dependence of the FF is analysed to provide further insight into the charge carrier transport and recombination mechanisms. Fig. 5 shows the changes in FF at high (1 sun) and low (10^{-4} sun) intensities compared to the maximum FF obtained at 10^{-2} sun intensity. The FF decreases drastically at low illumination for all the devices indicating a case of low shunt resistance. This is also supported by the fact that both NiO and PTAA-based devices have low shunt resistance ($< 10^5 \Omega \text{ cm}^2$).⁵⁵ The maximum FF at 10^{-2} sun of the NiO, as well as of the PTAA-based devices, is lower than the Shockley Queisser limit ($\sim 90\%$)⁷⁸ suggesting that trap-assisted recombination in the bulk perovskite absorber can affect the device performance. The FF decrease in the NiO_{Bu-MeAMD}-based devices at both low and high intensities compared to the maximum value at 10^{-2} sun indicates that they suffer from a combination of ohmic and trap-assisted interfacial recombination losses.⁷⁹ At a high light intensity, a higher concentration of charge carriers is generated and there is an asymmetry in the non-radiative recombination at the interface with higher trap sites resulting in the FF drop at high intensities.^{41,79} Previously, Glowienka *et al.* have also reported a similar trend in the FF with varying light intensities and showed *via* simulation based on the drift-diffusion model that there is an asymmetric band-bending at the NiO/perovskite interface.⁴¹ Hence, based on the above discussions, we conclude that NiO-based devices have a higher recombination rate at the NiO/perovskite interface due to the presence of traps as compared to the PTAA-based devices. This can result in the V_{OC} loss observed in the NiO-based devices. We believe that the presence of Ni^{3+} states in our NiO films, shown in the Ni2p spectra in Fig. S3 (ESI[†]), can act as recombination centres at the NiO-perovskite interface, as has also been reported by Wang *et al.*⁸⁰ Ni^{3+} states are electron acceptors (see, Section S2, ESI[†]) and therefore, it can also prevent hole extraction when present on the NiO surface.²⁴

Next to the experimental results, we have carried out simulations *via* the solar cell capacitance simulator software (SCAPS)⁸¹

to correlate the loss in V_{OC} with the presence of NiO/perovskite interface defect states. The parameters used for the simulation are mentioned in Section S4 of ESI[†]. The experimental J - V parameters of the NiO-based devices at varying light intensities are used to validate our simulation model (see Fig. S13, ESI[†]). Fig. S14 (ESI[†]) shows that the V_{OC} starts decreasing significantly with the increase of interface trap states of concentration around 10^7 cm^{-2} . This further substantiates our experimental results as the simulated V_{OC} of NiO-based devices without any interfacial trap states is 1.08 V, while a decrease by more than 100 mV is simulated at an interfacial trap density of 10^{10} cm^{-2} , matching the experimental V_{OC} of the NiO-based devices. The presence of traps and high recombination rate at the NiO/perovskite interface also affects the photocurrent as shown in Fig. 6. The total photocurrent is zero as this simulation is at open circuit condition. The electron and hole photocurrent without trap states are similar to the generation profile. On the other hand, a high electron and hole photocurrent with an inversion of its distribution is present at the NiO/perovskite interface with traps due to the high recombination leading to a decrease in hole concentration in that interface.⁴¹

This leads us to conclude that high interface recombination accompanied by asymmetric band bending of the quasi-Fermi level impedes charge extraction at the NiO/perovskite interface and limits the V_{OC} . Moreover, time-resolved PL (TRPL) in Fig. 7(a) supports this conclusion, as shown by the faster TRPL decay in the initial period of ~ 50 ns for the PTAA-based stack indicating an enhanced hole extraction by PTAA as compared to NiO. Additionally, we observe a large quenching of the PL signal for PTAA (Fig. 7(b)) consistent with the TRPL results and the observed difference in V_{OC} . Therefore, passivating the NiO/perovskite interface is crucial in suppressing the interfacial recombination.

Next, we investigate the passivation of NiO surface by Me-4PACz SAM. As concluded earlier, this approach may serve to disclose whether NiO resistivity affects the device performance, once the NiO/perovskite interface is passivated. The J - V parameters of the NiO-based devices, with and without Me-4PACz

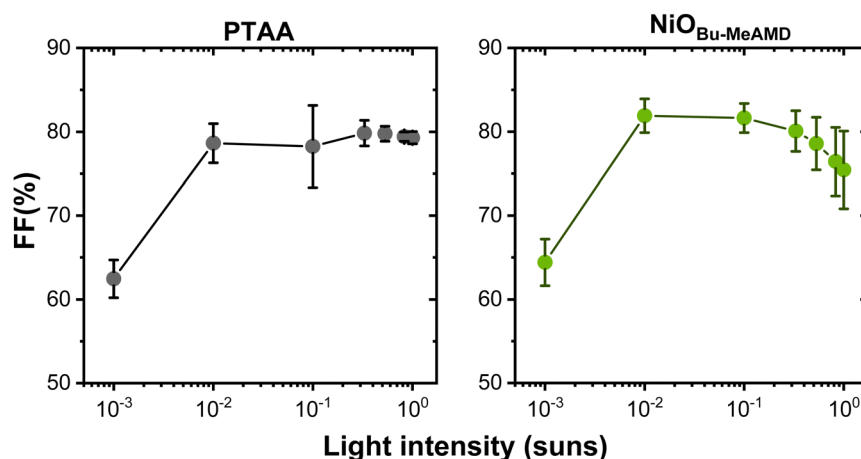


Fig. 5 FF vs. light intensity of PTAA and NiO_{Bu-MeAMD}-based devices. The average and the standard deviation are based on 8 devices.

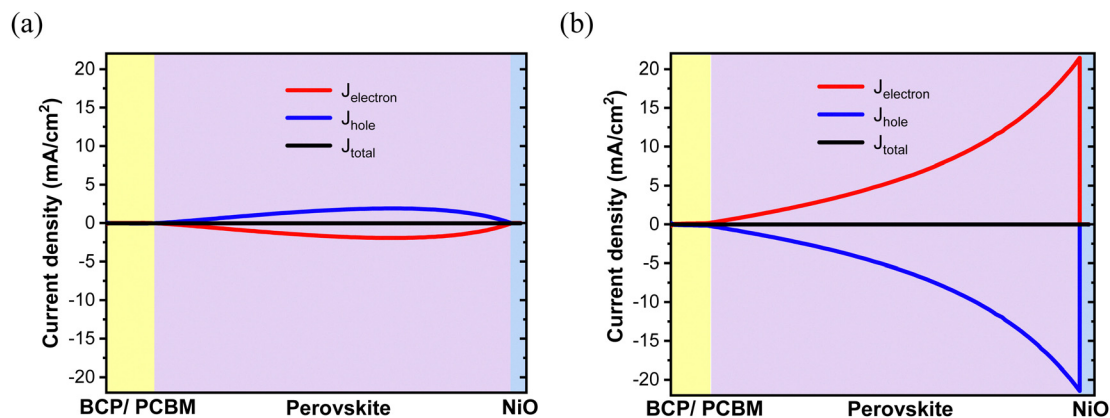


Fig. 6 Simulated electron current density (J_{electron}), hole current density (J_{hole}) and total current density (J_{total}) of (a) NiO-based devices with no interface defect states, and (b) NiO-based devices with 10^{10} cm^{-2} defect states at the NiO/perovskite interface, at open circuit condition.

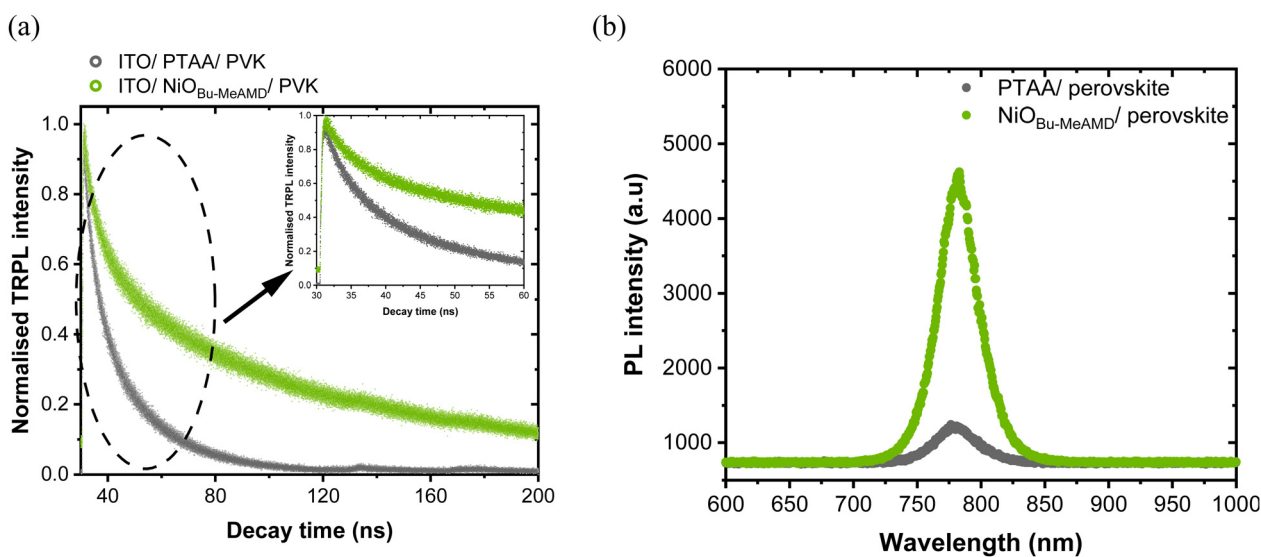


Fig. 7 (a) TRPL and (b) PL of the partial stacks of glass/ITO/NiO_{Bu-MeAMD} or PTAA/perovskite. The inset in (a) shows the initial decay region to focus on the charge extraction by the HTL.

passivation are reported in Fig. 1. The J - V curves of the NiO/Me-4PACz-based devices after light soaking are presented in Fig. 8 and J - V parameters are summarised in Table 4. Light soaking for nearly 3 minutes is carried out because Al:NiO_{Bu-MeAMD}/Me-4PACz-based devices exhibited a S-shaped J - V curve, likely due to charge transport barrier or band misalignment,⁸² which disappeared upon light soaking. Passivating the NiO_{Bu-MeAMD} and Al:NiO_{Bu-MeAMD} layers with Me-4PACz leads to the improvement of the V_{OC} by 170–200 mV compared to the unpassivated devices (Table 3). The NiO_{Bu-MeAMD}/Me-4PACz-based devices also have a J_{SC} gain of $\sim 1 \text{ mA cm}^{-2}$ leading to a PCE of $\sim 17.5\%$. However, there is a fill factor loss of $\sim 4\%$, compared to the unpassivated device (Table 3), due to an increase in the R_{series} as shown in Table 4. We hypothesize that the increase in R_{series} is due to the formation of a carbazole-multilayer on NiO, instead of a monolayer.⁸³ When comparing the Al:NiO_{Bu-MeAMD} with the highly resistive NiO_{Bu-MeAMD}-based devices, after interface passivation, we observe

that the V_{OC} and the J_{SC} are similar. So, in conclusion, the V_{OC} gain with respect to the unpassivated device is due to the trap passivation and better charge extraction by the Me-4PACz and is not further influenced by the NiO film resistivity. However, the FF of the Al:NiO_{Bu-MeAMD}/Me-4PACz-based device is affected by the S-kink which could be potentially due to an increase in the valence band-offset to $\sim 0.5 \text{ eV}$ between Me-4PACz ($\sim 5.7 \text{ eV VBM}^{84}$) and Al:NiO_{Bu-MeAMD} compared to the NiO_{Bu-MeAMD}.^{85,86} The FF increases from $\sim 54\%$ to $\sim 64\%$ due to the mitigation of the S-shape upon light soaking (see, Fig. S11, ESI†).⁸⁶ Nevertheless, the Al:NiO_{Bu-MeAMD}/Me-4PACz-based devices have a lower PCE compared to the NiO_{Bu-MeAMD}/Me-4PACz-based devices due to the lower FF of the former devices as a consequence of a higher series resistance. Lastly, the processing of Me-4PACz and the perovskite on the NiO_{MeCP} layer did not lead to a full film coverage (image attached, see Fig. S12, ESI†) due to which further J - V measurements could not be carried out and hence it is not included here.

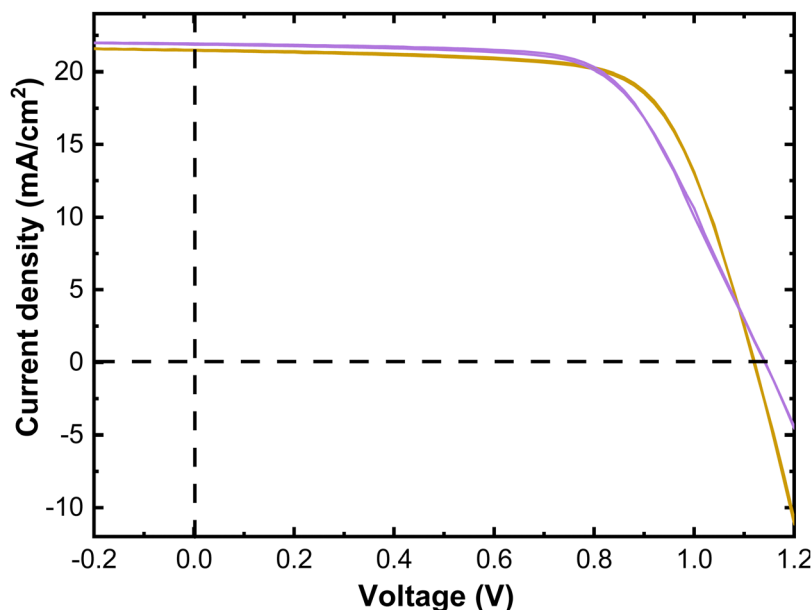


Fig. 8 The forward and reverse scanned J - V curves of $\text{NiO}_{\text{Bu-MeAMD}}/\text{Me-4PACz}$ and $\text{Al:NiO}_{\text{Bu-MeAMD}}/\text{Me-4PACz}$ -based devices after 3 minutes of light soaking.

Table 4 Average values of PV parameters, shunt (R_{shunt}) and series resistance (R_{series}) along with their standard deviation of the $\text{NiO}/\text{Me-4PACz}$ -based PSCs. The R_{shunt} and R_{series} are calculated from the slope of the J - V curve at the J_{SC} and V_{OC} respectively. This data is collected from several batches on 30 devices with PTAA and 15 devices with $\text{NiO}/\text{Me-4PACz}$

	V_{OC} (mV)	J_{SC} (mA cm^{-2})	FF (%)	PCE (%)	R_{series} ($\Omega \text{ cm}^2$)	R_{shunt} ($\text{k}\Omega \text{ cm}^2$)
PTAA	1064 ± 10	20.9 ± 0.6	79 ± 2	17.5 ± 0.7	3.7 ± 0.3	3.8 ± 1.2
$\text{NiO}_{\text{Bu-MeAMD}}/\text{Me-4PACz}$	1131 ± 8	21.6 ± 0.3	72 ± 2	17.5 ± 0.7	7.7 ± 0.9	3.2 ± 1.2
$\text{Al:NiO}_{\text{Bu-MeAMD}}/\text{Me-4PACz}$	1135 ± 6	21.4 ± 0.4	64 ± 2	15.7 ± 0.6	14.6 ± 1.9	1.8 ± 0.3

4. Conclusion

In summary, we evaluate the performance of $\text{NiO}_{\text{Bu-MeAMD}}$, $\text{Al:NiO}_{\text{Bu-MeAMD}}$ and NiO_{MeCp} -based devices, compared to PTAA-based devices. The $\text{NiO}_{\text{Bu-MeAMD}}$ is doped with aluminium using a novel ALD process developed in this work. Decreasing the resistivity of NiO , in case of $\text{Al:NiO}_{\text{Bu-MeAMD}}$ and NiO_{MeCp} , increases the V_{OC} by ~ 30 mV with respect to the $\text{NiO}_{\text{Bu-MeAMD}}$ -based device. However, no improvement in device performance occurs because of a lower FF in the former devices. We also conclude that the performance of PTAA-based devices surpasses that of NiO -based devices primarily due to a 90–130 mV V_{OC} gain. Ideality factor of the NiO -based devices, which is in the range of 1–1.3, reveals that the $\text{NiO}/\text{perovskite}$ interface is defective as compared to the PTAA/ perovskite interface, resulting in V_{OC} loss. We do not consistently observe the PbI_2 peak in the NiO -based partial stacks which is indicative of the interfacial redox reaction leading to a V_{OC} loss.

On the other hand, analysis of absolute photoluminescence measurements and the variation of FF with light intensity demonstrate that trap-assisted interfacial recombination is the cause behind the V_{OC} loss in NiO -based devices. A high rate of trap-assisted recombination at the $\text{NiO}/\text{perovskite}$

interface can lead to band bending of the quasi-Fermi level for holes at the interface thereby decreasing the V_{OC} . SCAPS simulation results further confirm that the presence of a high trap density at the $\text{NiO}/\text{perovskite}$ interface results in a decrease in the V_{OC} . Our work also shows that whilst lowering the NiO resistivity has a limited positive effect on the V_{OC} of the devices built only on NiO as HTL, modifying the $\text{NiO}/\text{perovskite}$ interface with Me-4PACz SAM enabled a V_{OC} increase of 170–200 mV, showing that passivating this interface is key to the increase in device performance. Also, once the interface is passivated, the NiO resistivity has no further influence on the V_{OC} within the explored range of resistivity of around 70 to $1.7 \times 10^4 \Omega \text{ cm}$. Moreover, we would like to highlight that it is necessary to compare the performance of NiO -based devices with a state-of-the-art HTL to draw conclusion on the efficacy of tuning a specific material property on device performance.

Data availability

The data supporting this article have been included as part of the ESI.†

Conflicts of interest

There are no conflicts to declare.

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