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ALD-grown two-dimensional TiS_x metal contacts for MoS_2 field-effect transistors†

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Metal contacts to MoS₂ field-effect transistors (FETs) play a determinant role in the device electrical characteristics and need to be chosen carefully. Because of the Schottky barrier (SB) and the Fermi level pinning (FLP) effects that occur at the contact/MoS₂ interface, MoS₂ FETs often suffer from high contact resistance (R_c) . One way to overcome this issue is to replace the conventional 3D bulk metal contacts with 2D counterparts. Herein, we investigate 2D metallic TiS_x ($x \sim 1.8$) as top contacts for MoS₂ FETs. We employ atomic layer deposition (ALD) for the synthesis of both the MoS_2 channels as well as the TiS_x contacts and assess the electrical performance of the fabricated devices. Various thicknesses of TiS, are grown on MoS₂, and the resultant devices are electrically compared to the ones with the conventional Ti metal contacts. Our findings show that the replacement of 5 nm Ti bulk contacts with only ~1.2 nm of 2D TiS_x is beneficial in improving the overall device metrics. With such ultrathin TiS_x contacts, the ONstate current (I_{ON}) triples and increases to \sim 35 μ A μ m⁻¹. R_c also reduces by a factor of four and reaches \sim 5 M Ω μm . Such performance enhancements were observed despite the SB formed at the TiS $_x/MoS_2$ interface is believed to be higher than the SB formed at the Ti/MoS₂ interface. These device metric improvements could therefore be mainly associated with an increased level of electrostatic doping in MoS₂, as a result of using 2D TiS_x for contacting the 2D MoS₂. Our findings are also well supported by TCAD device simulations.

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Introduction

Transition metal di-chalcogenides (TMDCs) are a family of twodimensional (2D)-layered materials with a chemical formula of MX_2 (M = Mo, W, Ti, Nb, V, etc. and X = S, Se, Te). 1,2 2D TMDCs constitute a wide library of compounds ranging from semiconductors and (semi)metals to superconductors.3 Among semiconducting 2D TMDCs, MoS2 is the most widely explored material because of its abundance in nature and its outstanding electronic properties. 4,5 Field-effect transistors (FETs) based on MoS_2 demonstrate high current densities of 700-1135 μA μm^{-1} , 6-8 high ON/OFF current ratios in the range of 10^7 – 10^9 , 5,9,10 low subthreshold swing (SS) values close to the thermionic limit $(\sim 60 \text{ mV dec}^{-1})$, 11,12 reasonably good mobility, 5,13,14 decent reliability,9 relatively low variability15 and compatibility with conventional Si processing technologies.16-18 These fascinating features of MoS₂ FETs may open new horizons for ultra-scaled nanoelectronic devices and circuits. 19,20

Implementation of MoS2 or any other 2D TMDC into mainstream technology platforms is not without hurdles. 2D FETs generally suffer from high contact resistance (R_c) , ²¹ which is still above the requirements specified by the International Roadmap for Devices and Systems (IRDS).²² High R_c originates from the unavoidable Schottky barrier (SB) formation and Fermi level pinning (FLP) effect at the metal-semiconductor (M-S) junctions.23,24 In recent years, several attempts have been made to tackle high R_c in 2D FETs, namely substitutional/chemical doping of the 2D layer, 25,26 2D phase engineering at the contact regions,27 the insertion of oxide buffer layers (Ta₂O₃, Al₂O₃) below the contacts, ^{28,29} electrostatically doping the 2D channel by high-κ dielectrics, 6,10,30 switching to edge contact device geometry (rather than using the conventional top contact device geometry)31-34 and utilization of semi-metal,8 graphene32,35-38 or metallic 2D TDMC contacts. 34,39,40 To date, the lowest R_c records are in the range of 123-520 Ω μm, being held for semi-metal⁸ or graphene contacts,37 phase engineering the 2D layer at the contact areas,27 dielectric mediated charge transfer doping of the 2D channel6 and metallic 2D TMDC employment in edge contact device geometry.34

Among the above-mentioned methods for reducing R_c , the usage of 2D metallic contacts, such as VS_2 ,³⁴ VSe_2 ,³⁹ or NbS_2 ,⁴⁰ in 2D FETs has lately gained a surging interest.

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Conventional bulk metallic contacts are known to form covalent bonds with the 2D semiconducting layer,41 leading to charge redistribution at the M-S junction, work function (WF) modulations and metal-induced gap state (MIGS)42 formation as well as 2D electronic band-structure perturbation that altogether result in high SB/strong FLP14,41-43 and therefore an overall high $R_{\rm c}$. 2D metallic contacts, on the other hand, offer several advantages over the 3D bulk counterparts. First and foremost, they only weakly bind/react with 2D semiconductors. This is mainly because of the overall weak van der Waals (vdW) interactions that leads to an almost clean and flat vdW interface at the 2D-2D M-S junctions, wherein lattice matching becomes less important.44 Such vdW interactions are also shown to suppress MIGS and allow for an almost unperturbed 2D semiconductor electronic band-structure, weaker FLP and lower R_c. 45 Second, the WF in 2D metals can be modulated by the application of an external electric field, enabling the control of Schottky barrier height (SBH) formed at the 2D M-S junctions.45,46

The other challenge ahead of integrating metallic and/or semiconducting 2D TMDCs into nanoelectronic devices and circuits is their high quality and large-scale synthesis. Among the different synthesis methods, chemical vapor deposition (CVD) is shown to be one of the most promising techniques for the growth of 2D TMDCs, as it ensures the delivery of premium quality films over large areas. 13,47-51 However, the high thermal budget that is often used in CVD may be a concern for the semiconductor industry. In addition, realization of vdW heterostructures made from 2D metals and semiconductors by using CVD, in both edge and top contact device geometries, typically involves complex procedures. 34,39,40

In recent years, atomic layer deposition (ALD) has drawn attention for the growth of not only single layer 2D TMDCs52-55 but also their heterostructures (both in lateral56 and horizontal directions⁵⁷). ALD is a low-temperature thin-film cyclic synthesis technique which is highly compatible with conventional Si technologies and excels in large area uniformity, thickness control down to sub-monolayer regime as well as conformality for high aspect ratio features. 58,59

In this work, we employ ALD for the growth of both 2D metallic and 2D semiconducting layers and introduce a straightforward approach for the fabrication of 2D-based FETs. We chose TiS_x ($x \sim 1.8$) as the contacts and MoS_2 as the semiconducting channel material. TiS₂ is one member of the 2D TMDC family with (semi)metallic60 properties. Theoretically, it has been shown that if employed as the contact electrodes, TiS₂ forms Schottky (Ohmic) contacts with n-type (p-type) MoS₂,61 due to its high WF (\sim 5.7 eV). 45,62,63 In addition, it preserves the MoS₂ intrinsic properties, meanwhile delivering high electrical conductivities. 61 During our study, we compare ALD grown TiSx contacts of various thicknesses with evaporated conventional Ti counterparts. We demonstrate that the fabricated MoS₂ FETs with ~ 1.2 nm thick TiS_x contacts outperform the ones with Ti contacts, as the overall MoS₂ FET device figures of merit (e.g. the maximum current density (I_{ON}) , field-effect mobility (μ_{FE}) and $R_{\rm c}$) improve when such ultrathin layers of TiS_x contacts are utilized.

Experimental

MoS₂ film synthesis

A two-step approach was followed for the synthesis of MoS₂, whereby ~ 1.5 nm MoO_x was initially grown using plasmaenhanced (PE-)ALD at 50 °C,64 in an Oxford Instruments Plasma Technology (FlexAL) ALD reactor, on degenerately doped (p^{++}) Si substrates that were covered with \sim 87 nm SiO₂. The as-deposited MoO_x films were then sulfurized in a homebuilt tube furnace, where a gas mixture of H₂S/Ar (10%/90%) was introduced at 900 °C for 45 min, resulting in ~1.2 nm thick MoS₂ films. Further details of the synthesis conditions and the MoS₂ film specifications are reported in ref. 57 and 65.

TiS_r contact synthesis

Direct thermal ALD was employed for the growth of TiS_x contacts with various thicknesses at 100 °C. The deposition took place in the FlexAL reactor. Tetrakis (dimethyl amido) titanium (TDMAT) (Sigma-Aldrich Chemie BV, 99.999% pure) was chosen as the precursor. During the first half cycle of ALD, TDMAT was dosed into the reaction chamber for 4.2 s with Ar carrier gas, at a pressure of 80 mTorr, followed by a 20 s Ar purge step with a flow rate of 300 sccm, at the lowest achievable reaction chamber pressure (~7 mTorr). In the second ALD half cycle, H₂S/Ar gas mixture was introduced as the co-reactant for 30 s, with a flow rate of 10/40 sccm and at 80 mTorr, followed by another Ar purge step (with similar conditions mentioned above). More information regarding the TiS_x synthesis on SiO₂ or on 2D TMDC substrates as well as TiS_r film specifications (e.g. TiS_x chemical composition, plane orientation, morphology, electrical resistivity and etc.) can be found in previous studies.56,66

Film thickness measurements

During the PE-ALD of MoO_x and thermal ALD of TiS_x , the film thicknesses were measured by in situ spectroscopic ellipsometry (SE) (J. A. Woollam Co., Inc. M-2000FI, 0.75-5 eV). From the obtained data, the growth per cycle (GPC) was determined. The final MoS₂ film thickness was also verified using ex situ SE (J. A. Woollam Co., Inc. M-2000D, 1.25–6.5 eV). All the collected data were analyzed using complete EASE software and its embedded B-spline oscillator model.

Device fabrication

Standard electron beam lithography (EBL) was carried out for the fabrication of back-gate MoS₂ FETs, and PMMA was used as the electron sensitive resist. Details of the device fabrication are described in ref. 67. During the first EBL step, contact regions were defined on the PMMA coated MoS2. Various thicknesses of TiS_x were then grown by thermal ALD on the PMMA opening areas, in the FlexAL reactor and at 100 °C. Choosing such a low deposition temperature ensures that PMMA does not evaporate during the growth of TiS_x. Immediately after the TiS_x growth, the samples were transferred into an electron beam (e-beam) evaporation chamber, where an Au layer of maximum 95 nm

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was deposited. The Au deposition is to facilitate probing the contacts during the electrical measurements. For the reference case, 5/95 nm of Ti/Au⁶⁷ was e-beam evaporated in the contact openings (with similar conditions as of the Au layer on the ALD grown TiS_x). Next, the lift-off process was carried out by submerging the samples in acetone overnight. For defining the channel regions and isolating the individual blocks, a second EBL step was required, followed by MoS₂ dry etching from the opened areas using SF₆/O₂ plasma gas mixture in an Oxford Instruments Reactive Ion Etching (RIE) reactor. Finally, PMMA was removed in acetone, and the fabricated devices were immediately capped with 5/25 nm of thermal ALD AlO_x⁶⁸/PE-ALD HfO_x, 69 both processed at 100 °C.

Electrical characterization

Current-voltage (I-V) measurements were performed in a cryogenic probe station (Janis ST-500) at a base pressure of \sim 1.9 \times 10⁻⁴ mbar and with a Keithley 4200-SCS parameter analyzer.

Device simulations

Technology computer-aided design (TCAD) simulations were carried out using SILVACO, and standard semiconductor physics transport equation solutions were obtained by the Newton method. The simulation parameters were selected in accordance with the experimental data.

Results and discussion

Series of TiS_x thicknesses ranging from \sim 20 nm down to ~1.2 nm were grown as the contacts to MoS₂ using thermal ALD at 100 °C. For the ease of probing the contacts during the I-V measurements, an Au layer of maximum 95 nm was evaporated on top of TiS_x. The electrical performance of the fabricated devices were assessed and compared to a reference device, for which 5/95 nm of Ti/Au was employed as the contacts. As per a previous report,67 this thickness combination is found to be the most optimal for the Ti/Au stacks contacted to the ALD-

based MoS₂ films. Fig. 1(a) shows the schematics of the fabricated devices.

The transfer curves $(I_{DS}-V_{GS})$ of the MoS₂ FETs with different TiS_x contact thicknesses are also provided in Fig. 1(b) and compared to the reference device. In all the cases, the current is measured on 500 nm long MoS2 channels and normalized to the device width (1 µm). At first sight, it is explicit that the entire series of TiS_r-contacted devices outperform the reference case. In addition, reducing the TiSx thickness from 20 nm down to 1.2 nm improves the overall electrical performance. The maximum ON-state current (I_{ON}) increases to \sim 35 μ A μ m⁻¹ for the MoS₂ FETs with the thinnest TiS_x contacts, which is nearly three times higher than that of the reference. Furthermore, the threshold voltage (V_T) shifts negatively with reducing the TiS_x thickness, and the OFF-state current (I_{OFF}) increases for the thinnest TiS_x contacts of \sim 1.2 nm, both implying an increase in the MoS₂ doping level. 12,70 Such doping effects have also been observed in previous studies where other 2D metallic contacts (e.g. graphene, 36,38 NbS₂ 40 or VSe₂ 39 have been utilized. We note that in general, any kind of metal (2D or 3D bulk) dopes MoS₂ or other 2D semiconductors up to a certain extent. 41,61,70,71

For verifying the repeatability of the observations shown in Fig. 1(b), another set of MoS_2 FETs with series of TiS_x contact thicknesses were fabricated and characterized. Similar trends were observed for the second set upon reducing the TiS_x contact thickness. See Fig. S1(a) and (b) in the ESI† and the associated discussion.

To further gain insight into the overall electrical performance of the MoS₂ FETs with TiS_x contacts, the average statistical data of I_{ON} , maximum μ_{FE} , I_{OFF} and ON/OFF current ratio are provided in Fig. 2(a)-(d), respectively. The presented data were obtained by measuring three-four devices on each studied sample.

In Fig. 2(a) and (b), I_{ON} and the maximum μ_{FE} show a monotonically increasing trend with reducing the TiS_x thickness. In addition, the devices with the thickest TiS_x contacts (\sim 20 nm) still outperform the reference devices of \sim 5 nm Ti. Both values (I_{ON} and maximum μ_{FE}) also increase nearly twice

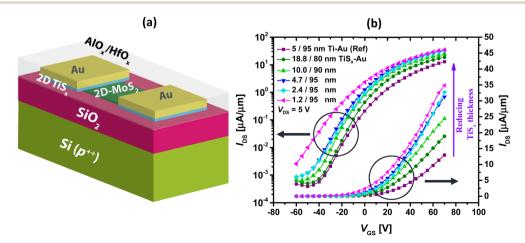


Fig. 1 (a) Schematics of the fabricated MoS₂ FETs, (b) measured transfer curves of the devices with series of TiS_x contact thicknesses, in both semilog and linear scales. Data for the reference sample is also provided.

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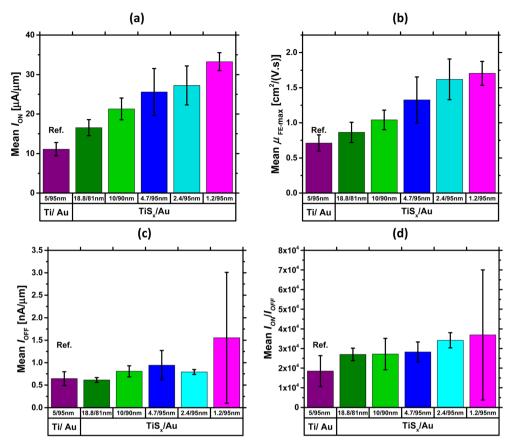


Fig. 2 Average statistical data of (a) I_{ON_*} (b) maximum μ_{FE_*} (c) I_{OFF} and (d) ON/OFF current ratio for the MoS₂ FETs with various TiS_x thicknesses, all obtained at $V_{DS} = 5$ V. Data for the reference case with Ti/Au contacts is also included.

on average when the TiS_x thickness reduces to \sim 1.2 nm. These observed device performance improvements by reducing the TiS_x contact thickness can be mainly attributed to the reduction of the interfacial tunneling barrier within TiSx, which leads to a reduction in the TiS_r overall interlayer resistance.⁶¹ In fact, because layers in 2D TMDCs are generally held by weak vdW forces, a gap is always present in between the individual layers. This gap acts as an interfacial tunneling barrier that scatters carriers, degrades the current and contributes to R_c . 41,61,72,73 Therefore, when the number of layers in a 2D metallic TiS_x reduces, the interfacial tunneling barrier and the resulting interlayer resistance are expected to reduce. In addition to that, the Au electrodes get closer to the MoS₂ active layers, altogether leading to a more efficient carrier injection/collection and hence improvements in the ON-state device characteristics. The overall superior performance of the TiSx-contacted MoS2 FETs to the reference devices can also be associated with the reduced perturbance of the MoS₂ electronic band-structure, 45 when the 3D bulk metallic contacts (Ti) are replaced with the 2D TiS_x counterparts.

Fig. 2(c) shows the average trend for I_{OFF} . As can be seen, there is no significant change in this metric with reducing the TiS_x thickness, except for when ~ 1.2 nm TiS_x contacts are employed. The rise of I_{OFF} in this case can be associated with the increased electrostatic doping in MoS_2 , such that higher back-

gate voltages are required to fully deplete the channel in the OFF-state regime. However, because $I_{\rm OFF}$ is maintained well below 2 μ A μ m⁻¹, a similar ON/OFF current ratio in the range of 10^4 (Fig. 2(d)) is achieved for all the studied cases. It is worthwhile mentioning that $I_{\rm OFF}$ can be further controlled if a thinner back-gate oxide (*e.g.* 30 nm SiO₂) is employed, as thinner SiO₂ typically leads to improved electrostatic control over the MoS₂ channel.

Based on the analyses provided so far, the MoS_2 FETs with ~ 1.2 nm thick TiS_x contacts were found to be the most optimally operating devices. To confirm this further, the electrical performance of the second set of MoS_2 devices with various TiS_x contact thicknesses were also statistically evaluated. See Fig. S1(a)–(d) in the ESI.† Our analyses verify that the MoS_2 devices with ~ 1.2 nm thick TiS_x contacts still lead to the most optimal performance. Therefore, they were selected for further electrical characterization.

The $R_{\rm c}$ of such devices were evaluated in the next step and compared to that of the reference case. To extract $R_{\rm c}$, transfer length method (TLM) structures⁷⁴ of various MoS₂ channel lengths (ranging from 0.5–5 μ m) were electrically measured, and the total resistance ($R_{\rm tot}$) of both TiS_x and Ti contacts were extracted from the transfer curves. Fig. 3(a) and (b) show the layout TLM design and the optical image of the probed TLM structures used for the I-V measurements, respectively.

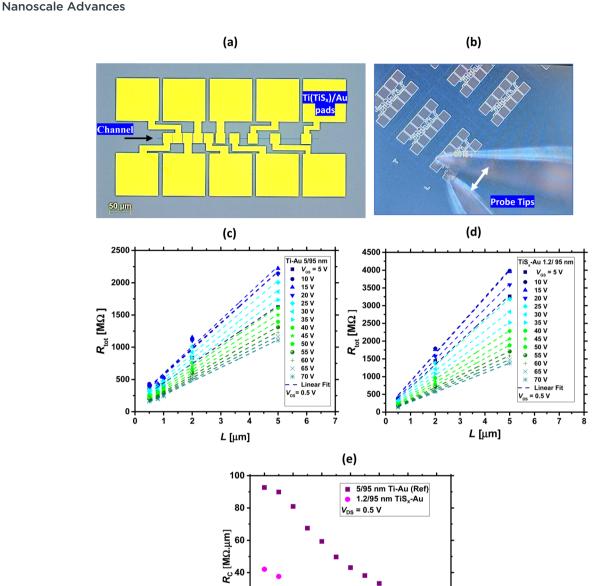


Fig. 3 (a) Top-view TLM layout design used for R_c extractions, with a channel width of 1 μ m and various channel lengths ranging from 0.5–50 μ m. As indicated, yellow squares are the Ti(TiS_x)/Au contact pads. (b) Optical image of the probed TLM structures used for the I-V measurements. The white arrow indicates the probe tips. (c) and (d) R_{tot} versus L for the MoS₂ FETs with TiS_x/Au and Ti/Au contacts, respectively (at $V_{DS}=0.5$ V and for different V_{GS}). (e) Extracted R_c as a function of V_{GS} for the TiS_x/Au and Ti/Au cases.

40 V_{GS} [V] 50

20

30

10

A low $V_{\rm DS}$ voltage ($V_{\rm DS}=0.5$ V) was applied for the $R_{\rm c}$ evaluations. This was to minimize the errors occurring during the $R_{\rm c}$ extraction, as the application of high $V_{\rm DS}$ ($V_{\rm DS}>1$ V) resulted in negative $R_{\rm c}$ and its underestimation. The $R_{\rm c}$ values were obtained using the following formula, where the dependence of the individual parameters on the applied $V_{\rm GS}$ is also included: 74,75

$$R_{\text{tot}}(V_{\text{GS}}) = 2 \times R_{\text{c}}(V_{\text{GS}}) + R_{\text{sh}}(V_{\text{GS}}) \times (L/W) \tag{1}$$

20

Here, $R_{\rm sh}$ is the channel sheet resistance, and L and W are channel length and width, respectively.

70

. 60

Fig. 3(c) and (d) display $R_{\rm tot}$ as a function of L for the TiS $_x$ contacts and the reference case, respectively, at $V_{\rm DS}=0.5$ V and for different $V_{\rm GS}$ values. Using these plots, $R_{\rm c}$ can be extracted. This is provided in Fig. 3(e). As can be seen, at $V_{\rm GS}=70$ V, the $R_{\rm c}$ for the TiS $_x$ -contacted MoS $_2$ FETs is ~ 5.0 M Ω μm and nearly four times smaller than that of the reference (which is 21.4 M Ω μm). These values of $R_{\rm c}$ are still higher than what is obtained for FETs fabricated from exfoliated/CVD grown highly crystalline MoS $_2$, which may be due to the nanocrystalline nature of our films and

their average grain size of 70 nm. 57 However, the replacement of 3D bulk Ti contacts with the 2D TiS_x counterparts is overall beneficial in reducing R_c of the ALD-based MoS_2 FETs.

One might also wonder about the $R_{\rm sh}$ of the ALD-based MoS₂. It is worthwhile mentioning that for having an accurate estimation of $R_{\rm sh}$, 4-wire measurements⁷⁶ as well as models specifically tailored for 2D polycrystalline materials^{77,78} need to be employed.

The SBH is another important factor for evaluating the contact quality in 2D-based FETs. The carrier transport across a Schottky junction can be described by thermionic emission equation modified for 2D materials:^{21,79}

$$I_{\rm DS} = AA_{\rm 2D}^* T^{3/2} \exp((-q\varphi_{\rm Bn})/(k_{\rm B}T))[1 - \exp((-qV_{\rm DS})/(\eta K_{\rm B}T))]$$
(2)

in this equation, A is the contact area, $A_{\rm 2D}^*$ is the 2D equivalent Richardson constant, T is temperature, q is the elementary charge magnitude, $k_{\rm B}$ is the Boltzmann constant, $\varphi_{\rm Bn}$ is the effective barrier height for electrons and η is the ideality factor. To determine $\varphi_{\rm Bn}$ of both ${\rm TiS}_x$ and ${\rm Ti}$ contacts to ${\rm MoS}_2$, low-temperature I-V measurements were carried out. The output

data $(I_{\rm DS}-V_{\rm DS})$ were obtained for various $V_{\rm GS}$ (ranging from -10 V to +60 V), at seven different temperatures (180–290 K). A first order approximation of eqn (2) was used, which is expressed as the following:

$$I_{\rm DS} \approx A A_{\rm 2D}^* T^{3/2} \exp[(-q/(k_{\rm B}T))(\varphi_{\rm Bn} - V_{\rm DS}/\eta)]$$
 (3)

For a fixed $V_{\rm GS}$, $\ln(I_{\rm DS}/T^{3/2})$ versus 1000/T is first plotted at each measured $V_{\rm DS}$, and a series of Arrhenius plots are obtained. This is shown in Fig. 4(a) and (b) for both Ti/Au and TiS_x/Au cases, respectively. As can be seen, the acquired data are linear in each $V_{\rm DS}$. If the slope of the individual fitted lines are plotted as a function of $V_{\rm DS}$, as illustrated in Fig. 4(c) and (d) for both the TiS_x and Ti cases, the interception point with the vertical axis (S_0) yields $\varphi_{\rm Bn}$ for a fixed $V_{\rm GS}$. So, Si related to $\varphi_{\rm Bn}$ through the following equation:

$$S_0 = (-q\varphi_{\rm Bn})/(1000k_{\rm B}) \tag{4}$$

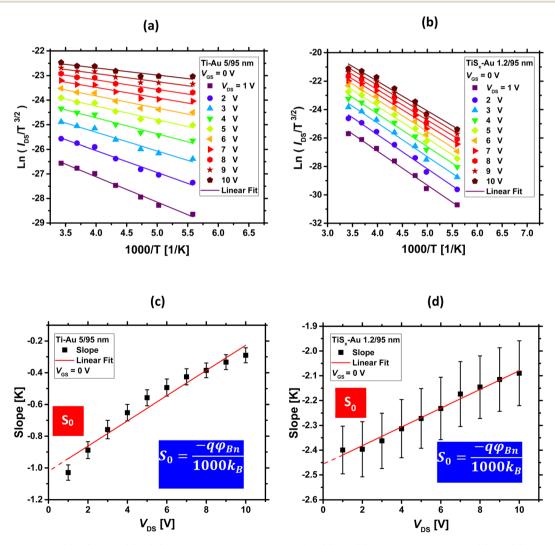


Fig. 4 Arrhenius plots for (a) Ti/Au and (b) TiS_x/Au contacts to MoS₂ at $V_{GS}=0$ V, (c) and (d) extracted slopes from (a) and (b) as a function of V_{DS} , at $V_{GS}=0$ V. The vertical axis-intercept (S₀) yields φ_{Bn} at $V_{GS}=0$ V.

If the above-mentioned extractions are repeated for each measured $V_{\rm GS}$, $\varphi_{\rm Bn}$ as a function of $V_{\rm GS}$ can be obtained. The final results are shown in Fig. 5(a). As evidenced from this figure, $\varphi_{\rm Bn}$ varies linearly at low $V_{\rm GS}$ ranges (the fitted straight line). Then, it starts to deviate from the linearity at a certain $V_{\rm GS}$. In fact, the thermionic emission equation is valid only for $V_{\rm GS}$ below the flat-band potential $(V_{\rm FB})$. Above $V_{\rm FB}$, in addition to the thermionic emission, the tunneling emission contributes to the total current, leading to the observed deviation from the linear trend. 14

Fig. 5(a) also compares the contact metal types. For all the measured $V_{\rm GS}$, $\varphi_{\rm Bn}$ is higher for the ${\rm TiS}_x$ contacts (270–132 meV) than for the Ti counterparts (123–35 meV). This is expected, as ${\rm TiS}_x$ is theoretically predicted to have higher WF than Ti. To illustrate this concept, the energy band diagrams of Ti, ${\rm TiS}_x$ and Au are schematically depicted in Fig. 5(b).

Despite a higher φ_{Bn} , the TiS_x contacts to MoS₂ exhibit lower R_c than the Ti counterparts (Fig. 3(e)). Therefore, the observed R_c reduction in the TiS_x-contacted devices is mostly associated with an increase in the MoS₂ electrostatic doping level, which could be due to the achievement of a flat/clean interface at the junction upon replacing the 3D Ti contacts with the 2D TiS_r counterparts, such that the carriers are injected more efficiently into the MoS₂ channel. The increase in the MoS₂ n-type doping level is evidenced from the negative shift of $V_{\rm T}$ (Fig. 1(b)) and the slight increase of I_{OFF} (Fig. 1(b) and 2(c)), once the Ti contacts are replaced with ~ 1.2 nm of TiS_x. For high doping levels, the width of the SB reduces,²³ and the carrier tunneling towards MoS2 further facilitates, leading to an overall increase in the current. In such a situation, the height of the SB (φ_{Bn}) will have a smaller effect on the overall device performance.

Considering TiS_x contacted MoS_2 FETs, one might find the observations contradictory to the energy band diagram analysis, as high WF metals (such as TiS_x) are expected to dope MoS_2 to p-

type. Recent density functional theory (DFT) calculations by Gao et al. 61 have addressed this controversy and have shown that TiS_2 can act as both p- or n-type contact to MoS_2 , depending on the TiS_2 number of layers and the doping concentration of both materials. TiS_2 can also tune the barrier height at the junction, and it is predicted that for n-type 2L-TiS $_2$ (\sim 1.2 nm) contacts to MoS_2 , the barrier height for electrons is two-times smaller than for holes. Hence, in contrast to the current band theory, it is possible to ignore p-type doping of MoS_2 by TiS_x contacts. Similar experimental observations were also reported by Bark et al. 40 when replacing 3D Mo contacts (WF \sim 4.5 eV) with high WF 2D NbS $_2$ contacts (WF \sim 6.1 eV) 45 in MoS_2 FETs. These studies indicate that there is a clear distinction between 3D and 2D metals contacting 2D semiconductors.

It is also worthwhile mentioning that for ultrathin layers of 2D TiS_x , quantum confinement effects start to play a role, which can affect the TiS_x electronic band structure and its alignment to that of MoS_2 at the interface. Hence, providing a more realistic picture of the $\mathrm{TiS}_x/\mathrm{MoS}_2$ energy band diagrams may require additional DFT simulations.

To further understand the discrepancies between the 2D and 3D metals contacting a 2D TMDC semiconductor and to verify our experimental results, TCAD simulations were also performed for the 2D TiS_2 and 3D Ti contacts to MoS_2 . The simulated device had a channel length of 500 nm and consisted of 1.2 nm of MoS_2 , 5 nm of Ti and 1.2 nm of TiS_2 . Selection of 1.2 nm TiS_2 was because this thickness led to the most optimally performing MoS_2 FETs in our experiments. The biasing conditions were $V_{\mathrm{DS}}=1$ V and $V_{\mathrm{GS}}=30$ V, to ensure that both device types are fully in their ON-state regime. Further details regarding the simulation parameters are provided in the ESI, Section S.3.† The resultant 2D contour plots of the gate-field-induced charge carrier density and the current density are displayed in Fig. 6(a)–(d), respectively. We note that Au contact pads are not shown in these figures.

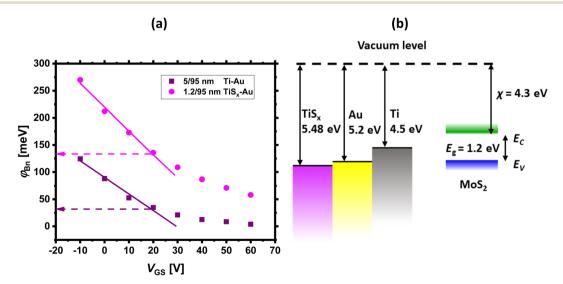


Fig. 5 (a) Effective Schottky barrier height (φ_{Bn}) as a function of V_{GS} for both Ti/Au and TiS_x/Au contacts to MoS₂ FETs. Below the flat band condition, φ_{Bn} reduces linearly with increasing V_{GS} , which is marked by the fitted straight lines in both contact cases, (b) energy band diagrams of Ti, sulfur-deficient TiS_x⁶² and Au with respect to MoS₂.

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(a) (b) Source Drain Drain n (cm-2) TiS Ti 10^{13} MoS MoS Si/SiO₂ Si/SiO₂ (d) (c) Source Drain J (A/cm²) Ti 106 MoS, Mo: Si/SiO, Si/SiO-

Fig. 6 (a and b) 2D contour plots of the induced charge carrier density and (c and d) current density for Ti and TiS₂ contacts to MoS₂, respectively. Simulations were performed for 1.2 nm of MoS₂, 5 nm of Ti and 1.2 nm of TiS₂ at $V_{DS} = 1$ V and $V_{GS} = 30$ V.

As can be seen from Fig. 6(a) and (b), when TiS₂ is in contact with MoS₂, more charges are induced into the channel and MoS₂ is doped to a higher extent, compared to the Ti case. The increase in the induced charge carrier density close to the contact regions can be distinguished by the dark orange color. As anticipated, the transverse field-induced carrier formation can be observed at the TiS2-MoS2 interface as well. Comparing Fig. 6(c) and (d), an increase in the MoS₂ current density can be noted for the case of TiS₂. In fact, only a small portion of TiS₂ is actively in contact with MoS₂, which facilitates the charge transport and subsequently leads to improved current density as well as the overall reduction of R_c . The provided simulation results well confirm the experimental observations and highlight the importance of integrating 2D metallic contacts with 2D semiconductors in 2D-based FETs.

Conclusions

To conclude, in this study, we have investigated the integration of 2D metallic TiSx with semiconducting 2D MoS2 by using atomic layer deposition (ALD). We have shown that ALD grown TiS_x (x ~ 1.8) contacts can improve the overall electrical performance of ALD-based MoS₂ FETs, when employed as the contacts to polycrystalline MoS2 films. Based on our analyses, only \sim 1.2 nm of ALD grown TiS_x is sufficient for unleashing the most from the electrical capabilities of ALD-based MoS₂ FETs. Utilization of TiS_x contacts improved the average ON-state device characteristics and led to the achievement of I_{ON} as high as $\sim 35 \, \mu A \, \mu m^{-1}$. In addition and despite a higher Schottky barrier height, TiS_x contacts reduced the R_c of the fabricated MoS₂ FETs down to \sim 5.0 M Ω µm, which is nearly a quarter of what was obtained for the bulk Ti contacts (21.4 M Ω µm).

Data availability

The data that support the findings of this study are available from the corresponding author upon request.

Conflicts of interest

The authors declare no competing financial interest.

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