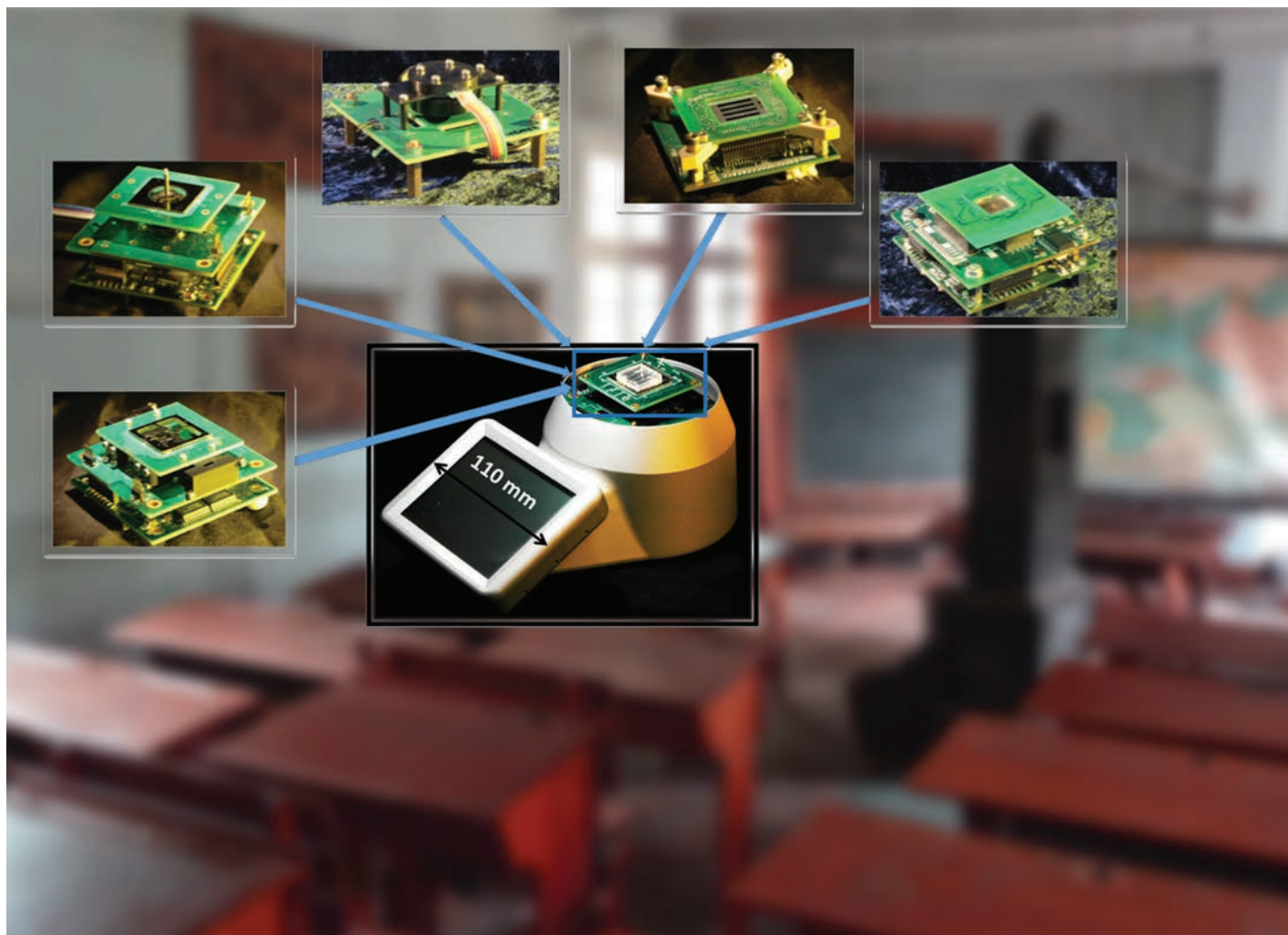




Featuring work from KIST-Europe, Germany and Institute of Microelectronics, Singapore.

Title: From chip-in-a-lab to lab-on-a-chip: towards a single handheld electronic system for multiple application-specific lab-on-a-chip (ASLOC)

Portable modular application specific lab-on-a-chip system was developed for point-of-care applications. Due to its low cost and modularity it is expected to be a popular tool in high schools and universities.

As featured in:



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From chip-in-a-lab to lab-on-a-chip: towards a single handheld electronic system for multiple application-specific lab-on-a-chip (ASLOC)

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We present a portable, battery-operated and application-specific lab-on-a-chip (ASLOC) system that can be easily configured for a wide range of lab-on-a-chip applications. It is based on multiplexed electrical current detection that serves as the sensing system. We demonstrate different configurations to perform most detection schemes currently in use in LOC systems, including some of the most advanced such as nanowire-based biosensing, surface plasmon resonance sensing, electrochemical detection and real-time PCR. The complete system is controlled by a single chip and the collected information is stored *in situ*, with the option of transferring the data to an external display by using a USB interface. In addition to providing a framework for truly portable real-life developments of LOC systems, we envisage that this system will have a significant impact on education, especially since it can easily demonstrate the benefits of integrated microanalytical systems.

Introduction

The first modern microfluidics devices¹ were introduced more than 20 years ago, starting an entirely new field known interchangeably as either micro total analysis systems (μ TAS) or lab-on-a-chip (LOC). Even though the devices were made by micromachining and their total area was typically between a few square millimeters to centimeters, they required bulky supporting instrumentation such as pumps and water-cooled Argonne lasers for fluorescence excitation and controls; thus, they could also be called chips-in-a-lab. With the advent of modern technology, electronics and light emitting diodes (LED) among many others, these devices are getting smaller and smaller. The area of LOC has continued to grow extensively, as reflected in the number of commercial products and in the rapidly growing number of published scientific papers.²

A large area within microfluidics is dedicated to molecular diagnostics. There are several reviews studying different

aspects of LOC development^{2–4} as well as specific applications,^{5–8} which predict a burgeoning fate to the field. One of the major requirements for LOC devices to be able to make a significant impact on diagnostics is their portability. This would open another area of applications in point-of-care diagnostics (POC), as so far only dip-stick assays have managed to make a commercial impact, due to the challenging cost limitations of the field. Numerous attempts to develop portable instruments have been presented.^{9–16} They are typically restricted to a single diagnostic method, such as PCR or LAMP,¹⁷ and process either a single analyte or a complex sample.

Here we show a versatile LOC system (Fig. 1) that is completely portable and battery-operated, with the unique advantage of enabling different sensing modalities on a single platform.

As the field of microfluidics grows and its applications emerge in the commercial arena, it is essential to introduce its fundamentals and capabilities to the general public, students in particular. Interestingly, the economics and requirements for an educational application are similar to that for POC systems.¹⁸ For example, teaching labs conducting molecular biology or analytical chemistry classes¹⁹ usually offer students a variety of different techniques, generally based on separate equipments that are only used during a short time each year. This is economically demanding as the tools and equipment are costly. Therefore, testament to the inventiveness of educators, innovative use of common materials has enabled the few first forays into the teaching labs,²⁰ while more complex strategies have been used to demonstrate

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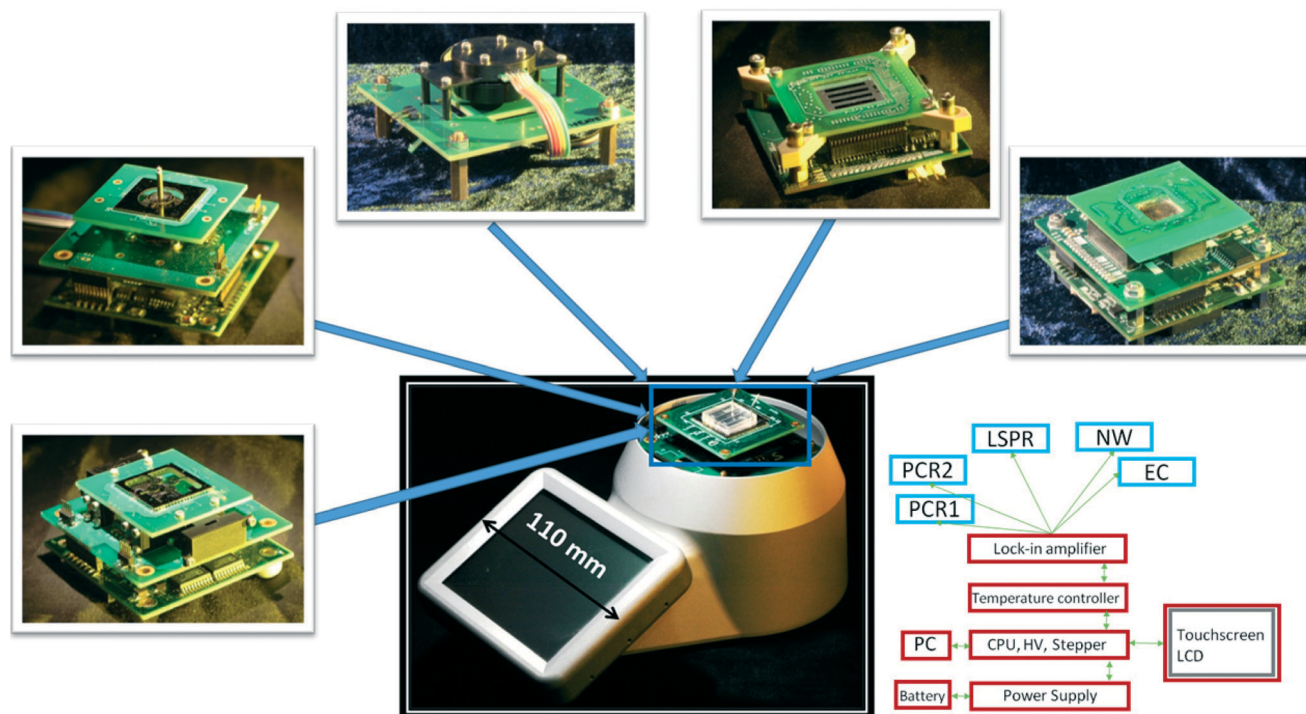


Fig. 1 Schematic of the handheld, battery-operated application specific lab-on-a-chip (ASLOC) system. The entire system is placed inside a machined aluminum housing, here showing a nanowire sensing application top chip. It consists of a stack of 4 printed circuit boards to perform functions common to all platforms. Starting from the bottom: the power management board; a board that contains the central processor unit, the high voltage power supply, the stepper motor and touch screen display controller, the USB communication and flash card memory; a board with four independent temperature controllers; a board with four lock-in amplifiers. Other boards are plugged in on-demand to easily configure the LOC to a particular application. Shown clockwise: time domain real-time PCR, space domain real-time PCR (optical parts were removed to show the internals), a localized surface plasmon resonance (LSPR) system, an array of 64 nanowire biosensors and an array of 36 electrochemical sensors.

specialized techniques.²¹ The introduction of new methods to the teaching lab is usually delayed due to the high costs and expertise required, which has strongly limited the number of universities that feature LOC experiments in their undergraduate teaching courses.

The portable system described here suits such an educational demand. It is based on the natural expansion of a previously developed system for a POC real-time polymerase chain reaction (PCR) diagnostics platform.^{22–27} The platform is built on a common body which contains a set of controlled voltage sources, a multichannel low-current detection system, a multichannel temperature control system, a regulated high voltage power supply, a stepper motor controller, a display unit and recording media and is powered by a Li-ion battery that can be replaced by a 12 V power supply when suitable. The battery is recharged through a USB interface that is also used to transfer measurement data. It can be customized by selecting a top module matching specific applications from the wide range we have developed, which cover most detection schemes that are utilized in current LOC applications. Application-specific integrated circuits (ASIC) are customized for a specific use. In this context, our system can be called an application-specific LOC (or ASLOC). These applications include real-time PCR in time domain, real-time PCR in space domain, localized surface plasmon resonance (LSPR),

impedance measurement of multiple nanowire-based biosensors, and multiplexed electrochemical measurements.

One of the main advantages of the system's design is its ease-of-use. In particular, it is the simplicity of changing application modules, which can be performed with minimal training. As indicated, the specific function of the system is customized by plugging in the required top block, consisting of a stack of up to three PCBs. The operator unscrews four screws, removes the unwanted stack that is plugged in, replaces it with the correct one and tightens the screws back. The software corresponding to the stack is then uploaded into the main system processor (CPU) *via* a USB interface. We are currently developing a new generation where the user interface is further simplified by having the main processor automatically identifying the boards loaded on the data bus and adjusting its functions accordingly.

Although some of the results of the current work were described in previous studies (see Table 1 for more details), as discussed in the next sections, we include them here to demonstrate the versatility of the platform while focusing on the newly designed systems for NW and electrochemical sensing.

The range of ASLOC that is shown could be easily augmented with new functionalities by designing new boards at the top (*e.g.* for photonic and acoustics).



These could also include different microfluidic structures for biological sample manipulation and its delivery to different sensing areas while maintaining portability of the ASLOC and translating microfluidics to the field.

Platform development

The sensing elements (optical or electrical) presented here are biased by a signal modulated at a suitable frequency, f_0 , typically around 1 kHz, and synchronously demodulated at its output. The resulting signal is a mixture of a DC component and higher harmonics. These higher harmonics originate from Fourier transforms of interrogating and detecting signals, and they have to be subsequently removed by a low pass filter with a cut-off frequency, f_1 . The entire system behaves like a band-pass filter “locked” onto the modulating frequency f_0 , *i.e.* only signals within the bandwidth from $f_0 - f_1$ to $f_0 + f_1$ pass the system and can be recorded.

accounts for the majority of currently developed diagnostic targets, such as infectious diseases.³⁰ The requirement to limit the cross talk between the channels, to attain specificity, is achieved by imposing stringent constraints on the modulating frequency f_0 . All frequencies have to be selected to avoid the matching of higher harmonics up to at least 8th as per our system performance calculation using Fourier transforms. The schematic diagram of the four-channel lock-in amplifier is shown in Fig. 2.

It consists of four voltage pulse generators, each running at its own unique frequency. The voltage pulses are converted into a current by the bipolar junction transistors (BJTs) Q_1 – Q_4 with amplitude set by the emitter resistors R_1 – R_4 . The pulsed current signal is fed into another block that can be either directly used to power a set of LEDs or lasers or to run current-to-voltage converters used for electrical measurements, such as electrochemical cells or nanowire sensors. The current to be detected, originating either from a photodiode or from a set of electrochemical sensors or nanowires, is then converted into voltage by a transconductance amplifier. The DC component is separated by capacitive coupling, amplified again and individually demodulated. The signals are then filtered by a low pass filter of the 4th order.

The major part of the four-channel lock-in amplifier (Fig. 2a) is the core of the LOC system described in this paper. It is placed on a single printed circuit board (PCB). The actual function of the system is then determined by another PCB stacked on the lock-in amplifier PCB. Here, we demonstrate two essential systems for optical (Fig. 2b) and electrical measurements (Fig. 2c). Taken together, they cover a large range of the LOC applications currently in use. The overall power consumption of the system of only 4 W makes it a suitable candidate for POC diagnostics as well as for field operations, where the portability and power consumption play an important role. A conventional laptop battery with 1.8 A h capacity can power this LOC for nine hours.



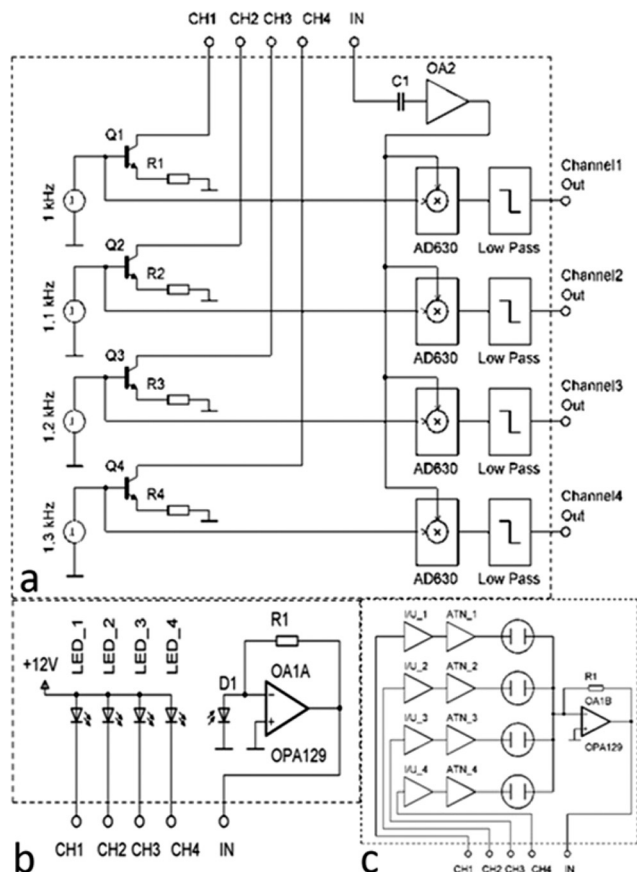


Fig. 2 Diagram of a four-channel lock-in amplifier. (a) The core of the system, consisting of four pulse voltage generators and voltage to current converters (Q_1 – Q_4 BJTs) generating controlled current pulses in four channels for the subsequent block (b or c). The composite current from the block is demodulated by an AD630, followed by a low pass filter. (b) Current powered optical block with 4 LEDs and current to voltage converter. (c) Voltage biased block for electrochemical or impedance measurement with virtual ground and current to voltage converter.

Current-based interrogation

Optical measurement

Our original optical measurement system was only capable of carrying a fluorescence measurement in reflection mode at one spot for real-time PCR.²⁴ However, as stated, this strictly limits the applicability of the system and, in view of an educational application, does not provide the necessary biological positive and negative controls. Here, we have expanded the system to four channels.

These have been implemented into two fundamentally different systems, for time domain and space domain measurements. Whilst in both cases the optical signal was processed by an identical electrical circuit, the systems primarily differed in their mechanical construction, and in the path of the optical signal, as described below.

The electrical current pulses generated by the core of the lock-in amplifier CH_1 – CH_4 are directly connected to four LEDs illuminating the sample. The light, either reflected off

the sample or generated by the sample itself, interacts with the photodetector and its photocurrent is converted into a voltage and demodulated. The LED can illuminate the sample directly or *via* a filter. The detection can be performed in reflection, transmission or scattering mode.

Time domain PCR

Fig. 3 shows the implementation of the time domain PCR system. The optics, electronics and silicon micromachined chip were as previously described.²⁷ The PCR was conducted by varying the heaters' temperatures. The fluorescence was simultaneously recorded in a real-time set-up, where an increase in fluorescence intensity is linked to positive amplification.

Space domain PCR

Here, a sample containing paramagnetic particles was moved by a magnet placed below the silicon chip on top of the system, while heaters were set at different but constant temperatures. Moving the sample from heater to heater thus resulted in the temperature cycling required for PCR (see Fig. 4). Contrary to our previously published designs,^{25,26} the magnet is integrated into the handheld instrument, significantly increasing the ease-of-use. The magnet was placed on a circular platform and enabled the rotational motion below the silicon chip. As there was no space for the optical system below, it was placed above the silicon chip as shown in Fig. 4 (top).

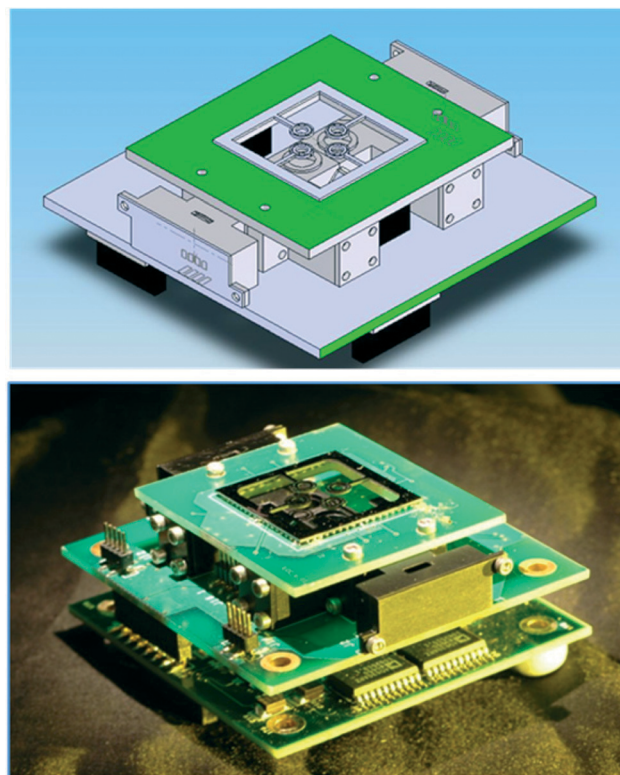
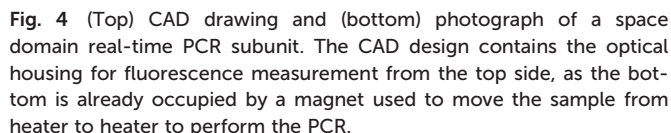


Fig. 3 (Top) CAD drawing and (bottom) photograph of a time domain real-time PCR subunit, where the sample is stationary and the temperature cycled by changing the heaters' temperature.





In this paper, the LSPR system has been enhanced by a low profile optical head (Fig. 1) to reduce the device size. A scanning electron microscopy (SEM) image of a chip used for localized surface plasmon resonance measurements, the LSPR head as well as the results are shown in Fig. 5.

We have used the system in optical measurement mode (Fig. 5b-c). The signal generators' outputs were connected to four LEDs with four different colors (wavelengths), and the composite signal was detected by a single photodiode, which was then demultiplexed by four AD630 ICs. This setup is capable of detecting the reflection from the surface of the LSPR chip at four different wavelengths simultaneously.

Nanowire sensors have received much attention recently due to their attractive surface-to-volume ratio, which promises significant shifts in analytical performance towards ultrasensitive devices. In a systems perspective, NW can be considered as resistors, the resistance of which varies with external factors such as bending³¹ or the presence of biomolecules.³² The challenge in their interrogation lies within the small currents available.

Here, we modified the blocks described above for electrical current measurement by adding a current-to-voltage converter and thus interrogated the device of interest by voltage. We created a whole family of systems to measure 4 NWs, 64 NWs, and 160 NWs. A CAD design of a 64 NW sensor is shown in Fig. 6. The high number of multiplexing can be a challenge in ASIC solutions,³³ and we show here that the handheld system can be used to address array-based applications, such as pathogen typing (*e.g.* influenza).

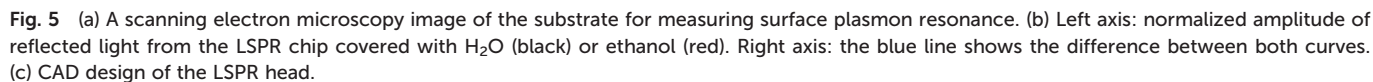


Fig. 6 (a) CAD design of the 64 NW sensor with microfluidics channel. The inset shows the microchannel crossing over the nanowire array. (b) A microscope photograph of fabricated 4 NW sensors. (c) A TEM image of a NW cross section. Inset scale bar is 20 nm long. (d) A circuit diagram of voltage-based interrogation with an option to measure 64 NW sensors.

The NW measurement is based on generating voltage pulses by four signal generators controlled by a single chip controller at selected frequencies. The voltage pulses are converted into a current by a bipolar junction transistor (BJT) with the maximum current set by resistors R_1 – R_4 . The pulses are converted into voltage by I/V converters, attenuated to 200 mV by ATN1_1–ATN_4 (Fig. 6d), and connected to the first terminal of each NW sensor. The second terminal of all nanowires is common and connected to the trans-impedance amplifier with a high value resistor in a feedback loop. The amplifier output is connected *via* capacitive coupling to a second amplifier (OA₂) with gain set to 100×. The amplified voltage output is fed into a set of demultiplexers together with the original pulses from the signal generators. Only the coherent fraction of the inputs passes the filter. The AC component is filtered out by a low pass filter of the 4th order.

The system has a gain of:

$$V_{\text{OUT}} = \frac{T^* G^* V_{\text{GEN}}}{R} \quad (1)$$

where T is the transfer function coefficient, G is the gain and V_{GEN} is the amplitude of the biasing voltage. The overall gain is up to 10^8 , which enables the measurement of an electrical current as small as tens of pA. Such a small current is typically an amplitude of a photocurrent as a response from a fluorescence measurement or current flowing through NWs with resistance of up to a few $\text{G}\Omega$.³⁴ Before the measurement, we conducted a system calibration. This was done by subsequently plugging in test boards with different resistors in surface mounted device format from $10\text{ M}\Omega$ to $200\text{ M}\Omega$ to record the output voltage.

Electrochemical sensors

The principle of electrochemical sensing is very similar to the NW measurement, with the added requirement of a DC bias for reference/counterelectrode. We added a sawtooth signal

into the circuit at the nodes between current-to-voltage (I/U) converters and attenuators (ATNs) in Fig. 6d.

Each sensing unit has a working electrode and a combined reference/counterelectrode. The use of combined electrodes, as well as its working principle, was described earlier.³⁵ In this paper, we present a design containing 36 (see Fig. 7) or 64 blocks of four sensing units connected to a common ground. The sawtooth signal was generated using a signal generator 33120A (Agilent GmbH), while the system has the capability of programming an internal voltage generator to perform cyclic voltammetry autonomously. This signal was used in the lock-in amplifier to bias one of the reference electrodes. The resulting signal was then passed through four analog multiplexers 1×16 . An application for a personal computer was developed to either select a single unit (of 4 sensors) or scan the entire chip (9 or 16 units) with a predefined time per unit. The output voltage from the lock-in amplifier can be correlated with the sawtooth signal to extract a differential scanning voltammogram. The communication between the signal generator and the board was conducted *via* an RS232 interface.

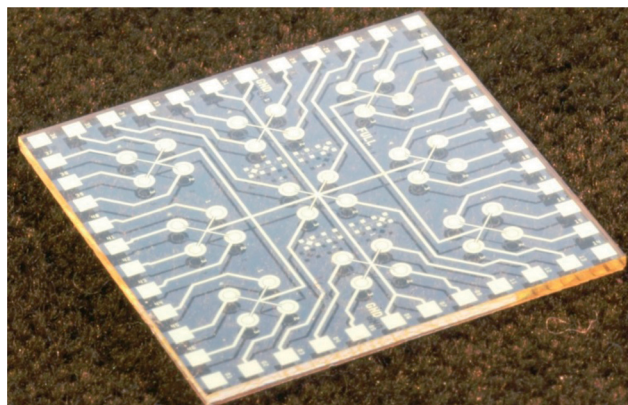


Fig. 7 A photograph of a 36-cell electrochemical chip.

All working electrodes were connected. The common node was connected to a current/voltage converter *via* a virtual ground. The converted output voltage was consequently proportional to the sum of currents flowing into all working electrodes. This composite current was then processed by demodulators and split into four individual channels. The amplitude of the DC signal in each channel corresponded to the current in the respective electrochemical cell.

Results and discussion

Nanowire-based sensing system

Fig. 8 shows the system setup for biosensing applications. The NW chip contained 64 sensors clustered into 16 groups, each containing 4 individual NW sensors. The chip was connected electrically and mechanically to the PCB by using a bespoke soldering tool. It consisted of a PCB holder, a pneumatically actuated heated soldering head and a timer. The PCB was placed underneath the head, which was heated up to a temperature of *ca.* 240 °C and its motion was activated. It then went down, pressed the chip against the PCB for 2 s and moved back into its original position. During those 2 s, the silicon chip was heated up and soldered onto the PCB.

The PCB with the nanowire chip was then plugged into the LOC. The analog switches were controlled from a PC set for 5 s per measurement of a single unit. The chip was sequentially read out. The measurement of all sensors took 85 s in total (Fig. 9). As all the four channels of our oscilloscope were occupied by NW measurements, we designed the program to control the switches so that no switch was active at the start.

We used this feature as a marker to establish the switching sequence, which can then be set for all subsequent measurements. The PCB with the soldered nanowire chip was plugged into the LOC system, and the conductivity was recorded as a function of time. During the recording, we manually covered the chip with 1 μ L of pH 10 calibration buffer for 60 s and changed the buffer to pH 7 for about a

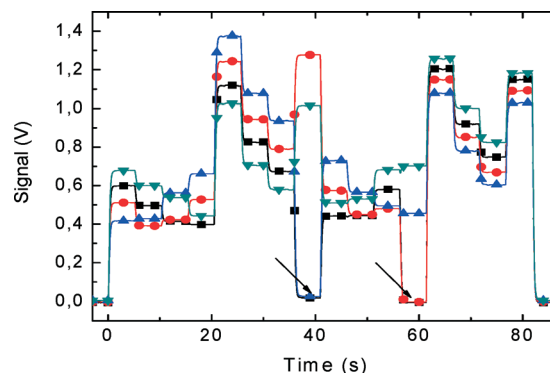


Fig. 9 Real-time measurements taken from the 64 nanowire (NW) chip in 85 s. The NWs were clustered into 16 groups, each containing 4 NWs, and were simultaneously measured. Electrical output signal corresponding to each demultiplexer (Fig. 6d) has a different color. This is a static measurement that shows the status of all 64 NWs. We have measured 4 NWs from group 1, addressed by the demultiplexer for about 3 s, switched to group 2 etc. until all 16 groups were measured. The arrows show four defective NWs, two from group 8 and two from group 12. A buffer solution with pH 7 was inside the microchannel.

minute, to be followed with pH 4 buffer. The system response to the pH value is shown in Fig. 10.

We also used an older device with only 4 NW sensors to test the pH of different solutions (Fig. 10) in sequence, with real-time detection.

Electrochemical detector

We used the chip as shown in Fig. 7 to demonstrate the functionality of this platform for electrochemistry applications. The chip was attached in the same way as the PCR and NW chip by soldering to the PCB. The glass substrate of this chip has a thermal conductivity about 100 times lower than silicon, and the soldering time was adapted to this.

First, we measured the oxidation/reduction of ferro/ferricyanide redox couple,³⁵ a widely-used calibration reaction to evaluate the behavior of the electrochemical sensors.

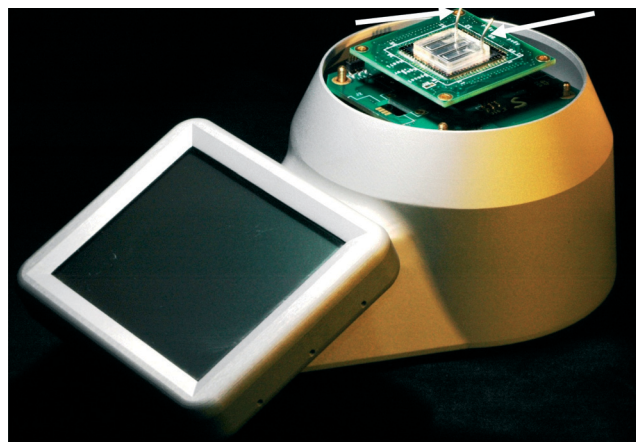


Fig. 8 A photograph of the LOC system in configuration to test the nanowire based sensors with microfluidics channel as per the design in Fig. 6. The fluid was delivered to the system by the tubes connected to the chip through the PDMS cap as shown by the arrows.

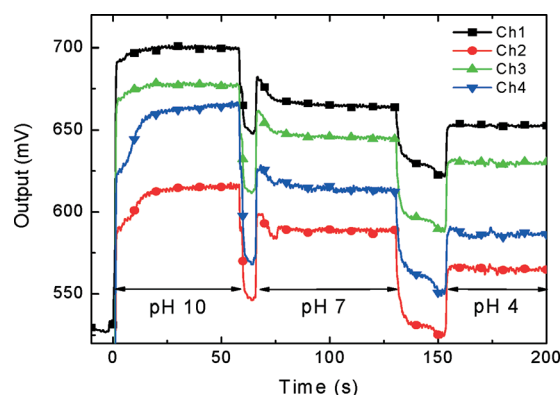


Fig. 10 Application of the 4 nanowire sensor to detect pH variations. 1 μ L of buffer solution was manually dispensed for about 60 s and removed by capillary force before the new buffer was dispensed.



We applied voltage pulses identical to the ones used in the NW measurements, while all electrochemical cells were biased with a sawtooth signal. The signal generator was connected between the I/U converter and the attenuator (Fig. 6d). The measurement was conducted by the pulse technique,³⁵ and the lock-in amplifier automatically provided a signal derivation with respect to time. This method enabled us to directly obtain a curve similar to that obtained by differential pulse cyclic voltammetry (DPV) technique.³⁶

In a DPV measurement, the increasing and decreasing curves should be almost identical. The difference observed in Fig. 11 could be attributed to voltammetry/pulse conditions, where pulse duration and the phase shift for each signal generator (Fig. 2) may benefit from a complete optimisation.³⁶

Temperature measurement

The number of channels available for temperature measurement has been increased to four in this system, using an AC biased Wheatstone bridge with a lock-in amplification technique for the signal enhancement (Fig. 12).

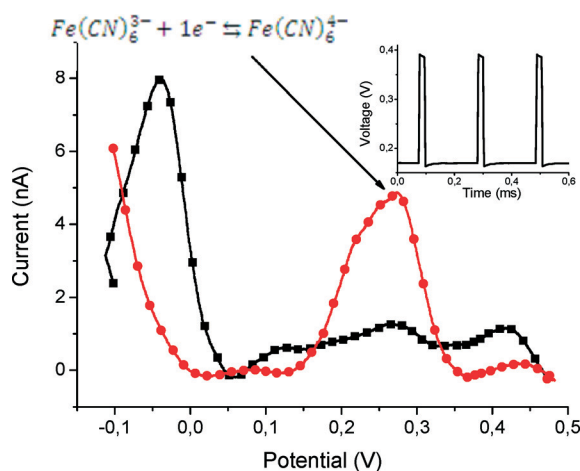


Fig. 11 Response of an electrochemical chip to pulsed voltage. The system behavior is similar to differential pulse voltammetry. The figure shows the current when increasing (red) and decreasing (black) the potential. The curves enable the identification of the redox peaks of the ferro/ferricyanide oxidation–reduction reaction. (Inset) Details of the biasing pulses superimposed to the sawtooth signal with peak amplitudes of 0.6 V, an offset of 0.2 V and a slope of 100 mV s^{−1}.

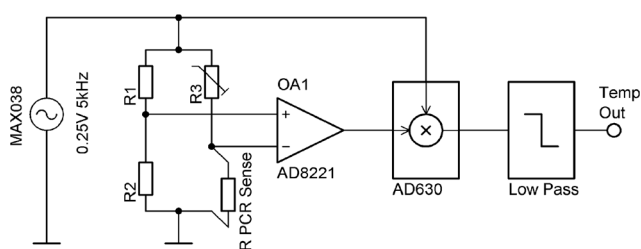


Fig. 12 Single channel ASLOC temperature sensing used for PCR. AC powered Wheatstone bridge with lock-in based amplification technique.

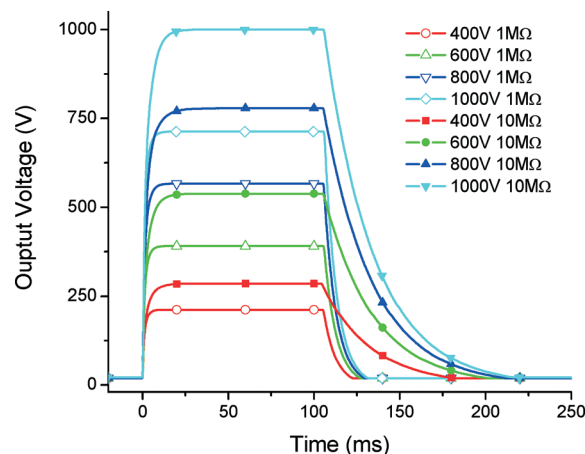


Fig. 13 High voltage power supply with 1000 V, 800 V, 600 V and 400 V setting with two different loading resistors, 10 MΩ and 1 MΩ. It shows the load influence on the output voltage as well as on the discharging time.

High voltage controller

For this LOC instrument, we have also developed a high voltage (HV) power supply controllable up to 1000 V programmed with 100 ms long pulses (Fig. 13). The HV output was loaded with 10 MΩ and 1 MΩ resistors. The load value lowers the total voltage by making the discharging faster. In the future, the designed HV system will be able to accommodate pulse width modulation, independent of the load impedance.

Conclusion

A versatile instrument platform for lab-on-a-chip devices was developed. The overall power consumption of the system is 4 W, making it a suitable candidate for the point-of-care diagnostics, field operations and primarily for educational purposes. The system can be simply changed from one application to another.

We have tested two types of systems: current- and voltage-based interrogations. Taken together, they cover the majority of LOC applications currently in use or in research. In the first category, we demonstrated real-time PCR and LSPR detections, while the measurement of NWs and electrochemical cells illustrated the second. The voltage interrogation scheme specifically enables the measurements of significant arrays of sensors, here represented by 64 units, which were carried out in 85 s.

The platform presented here has the potential to open a new chapter in LOC development, as it is easily adaptable for many different applications and can be used in remote locations and for educational purposes, such as undergraduate or college teaching labs.

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