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## Directing SEI formation on Si-based electrodes using atomic layer deposition†

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**The design of artificial solid electrolyte interphase is an important task to minimize capacity losses in Li-ion batteries. Herein, TiO<sub>2</sub> created through atomic layer deposition was used as an artificial SEI on Si nanoparticles. Such coating led to substantial improvement of cycling stability when evaluated with FEC-free electrolyte.**

Since the commercialization of Li-ion batteries (LIBs) by Sony in early 1990s,<sup>1</sup> it has been one of the most predominant energy storage solutions for a wide variety of applications such as electric vehicles, grid storage, and multiple portable electronic devices.<sup>2–5</sup> However, the energy density of modern LIBs is not adequate to completely satisfy the growing demand for energy storage. Therefore, there is a necessity for low-cost LIBs that can safely deliver high power and energy density with a long cycle life. This need drives the global search for new active materials and electrolytes to replace those that are currently utilized in commercially produced LIBs. Within this search, the quest for suitable high-capacity negative electrodes for high-performance LIBs has led to the investigation of various materials that can outperform state-of-the-art graphite with a capacity of 372 mA h g<sup>−1</sup>.<sup>6</sup> Considerable interest has been devoted to alloying-type anode materials, as they can accommodate multiple Li-ions per alloying atom.<sup>7</sup>

Among these alloying materials, silicon (Si) has gained a great traction attributed to its low working potential, light weight, high abundance and theoretical capacity almost ten times higher (3579 mA h g<sup>−1</sup>) than that of graphite.<sup>8–10</sup>

However, the grand challenge of developing Si-based anodes is to mitigate the massive volume changes (~300%) during Li-ion insertion (lithiation) and extraction (delithiation) processes, which lead to severe capacity decay during continuous cycling. This most intractable problem causes pulverization of Si resulting in loss of electrical connection within the electrodes, and a continuous growth of the solid electrolyte interphase (SEI) layer due to the constant changes of the Si surface area. As a result, these anode materials typically show inferior cycling stability and low initial Coulombic efficiency. Moreover, the continuously growing SEI constantly consumes the electrolyte components, which ultimately results in cell failure.<sup>11</sup> In addition, aggregation of Si particles through electrochemical sintering results in poor kinetics of the electrodes.<sup>12</sup> These limitations hold back the potential application of Si-based anodes for efficient, long lasting, and safe LIBs.

The design of stable SEI on Si-based electrodes represents one of the possible routes for improving their cyclability.<sup>13–15</sup> The common strategy for this task is to use electrolyte additives, *e.g.* fluoroethylene carbonate (FEC) which promotes formation of relatively stable SEI.<sup>16,17</sup> However, additives such as FEC are continuously consumed during electrochemical cycling, and their enhancing effect disappears when their reservoir in the electrolyte is depleted.<sup>18</sup> In addition, electrolyte additives add weight and cost to a battery.

Alternative strategies such as surface coating and hybridization with carbon-based materials have been shown to improve cyclability. Particularly, employing a suitable surface coating on nanostructured Si has been observed to improve cycling performance by reducing the reactivity of Si with electrolytes, although the challenge with huge volume changes cannot be eradicated entirely.<sup>19</sup>

Among multiple coating techniques, atomic layer deposition (ALD) has been demonstrated to be a very versatile method for interfacial engineering. Unlike other thin film deposition methods (*e.g.* PVD, CVD), ALD enables an excellent control over the uniformity, conformality and thickness of thin films at the atomic level due to its surface-controlled nature. In addition,

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ALD could be used for modification of particles and is not limited to only flat film surfaces. Such coating could be used to enhance the battery performance by providing an artificial SEI layer preventing detrimental side reactions at the electrolyte/electrode interface.<sup>20</sup> Several types of ALD coating have been explored for their use in modification of battery materials, such as transition metal oxides ( $\text{Al}_2\text{O}_3$ ,  $\text{TiO}_2$ ,  $\text{ZrO}_2$ ,  $\text{ZnO}$  and  $\text{SiO}_2$ ), metal nitride ( $\text{TiN}$ ) and aluminum oxynitride ( $\text{AlO}_x\text{N}_y$ ).<sup>19,21–25</sup> Among these,  $\text{TiO}_2$  has been proven as one of the most promising coating materials to enhance the cycle life and rate capability of Si-based electrodes due to its small volume change during cycling (<4%), structural stability, low polarization and good electrochemical reversibility.<sup>25–27</sup> In this study we coated Si nanoparticles with  $\text{TiO}_2$  using ALD to examine the influence of the coating on electrochemical performance in LIBs.

The pristine nanoparticles of amorphous Si (a-Si) with a diameter of  $\sim 40$  nm were obtained from commercial sources and stored in inert atmosphere. However, an oxide layer of  $\sim 2$  nm was still found on the surface, as was evidenced by transmission electron microscopy (TEM), energy dispersive X-ray spectroscopy (EDS) and electron energy loss spectroscopy (EELS) (Fig. 1 top panel, and Fig. S1, ESI†). This oxide layer could have been formed during sample preparation for TEM analysis, where the sample was exposed to air for  $\sim 5$  min.

These Si nanoparticles were successfully coated with  $\text{TiO}_2$  by ALD after several trials during the optimization of the process (Table S1, ESI†). The initial attempts, with a deposition temperature of  $150^\circ\text{C}$  (samples denoted as a-Si-Tx, where  $x = 1–3$ ), resulted in an uneven  $\text{TiO}_2$  coating with a raspberry-like morphology (TEM images are shown in Fig. S2, ESI†). The uneven deposition of  $\text{TiO}_2$  was hypothesized to result from the absence of sufficient number of OH groups on the Si surface originating from a lack of exposure to moisture. Adding extra cycles with

$\text{H}_2\text{O}$  pulsing in the beginning of the depositions (a-Si-T4, Fig. S2, ESI†) slightly improved the uniformity of the  $\text{TiO}_2$  coating. The best results in this study were obtained by additionally increasing the deposition temperature to  $170^\circ\text{C}$  (a-Si-T5, Fig. 1 and Fig. S3, ESI†), where the a-Si nanoparticles were conformally coated with a layer of amorphous  $\text{TiO}_2$ .

Fig. 1 and Fig. S1 (ESI†) presents the EDS elemental mapping for identifying the surface composition (Ti, O, C, Si) for pristine Si (a-Si) and optimized  $\text{TiO}_2$ -coated Si (a-Si-T5). The detailed analysis revealed that the coated particles were encapsulated by  $\sim 2$  nm  $\text{SiO}_x$  layer and  $\text{TiO}_2$  was coated on top of this layer. The thickness of the  $\text{TiO}_2$  coating was found to be  $\sim 3$  nm. XPS analysis of Ti 2p further confirmed the presence of a  $\text{TiO}_2$  coating (Fig. S5, ESI†).

To understand the effect of the  $\text{TiO}_2$  deposition on the Li diffusion kinetics, electrochemical impedance spectroscopy (EIS) measurements of a-Si and a-Si-T5 were performed at open circuit voltage after cell assembly and after 1 charge–discharge cycle (Fig. 2). Two types of electrolytes were used for the evaluation of coating effect – an electrolyte with 10 wt% FEC (Fig. 2A) and FEC-free electrolyte (Fig. 2B), both with 1.2 M

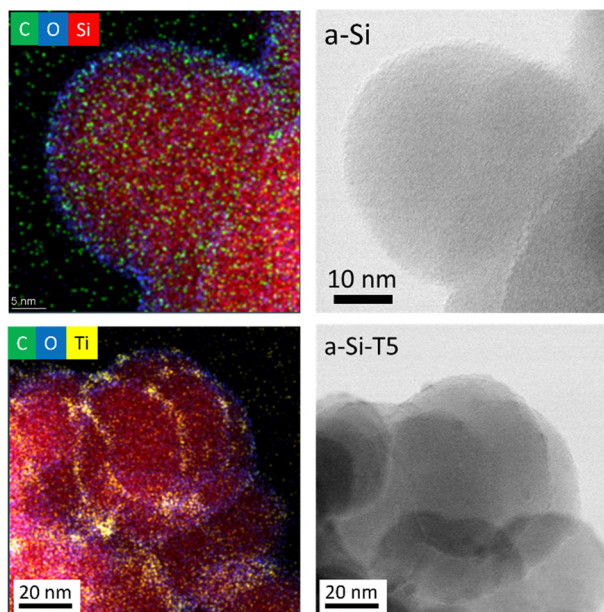


Fig. 1 EDS mapping and TEM images of pristine amorphous silicon (a-Si, top) and  $\text{TiO}_2$ -coated silicon (a-Si-T5, bottom).

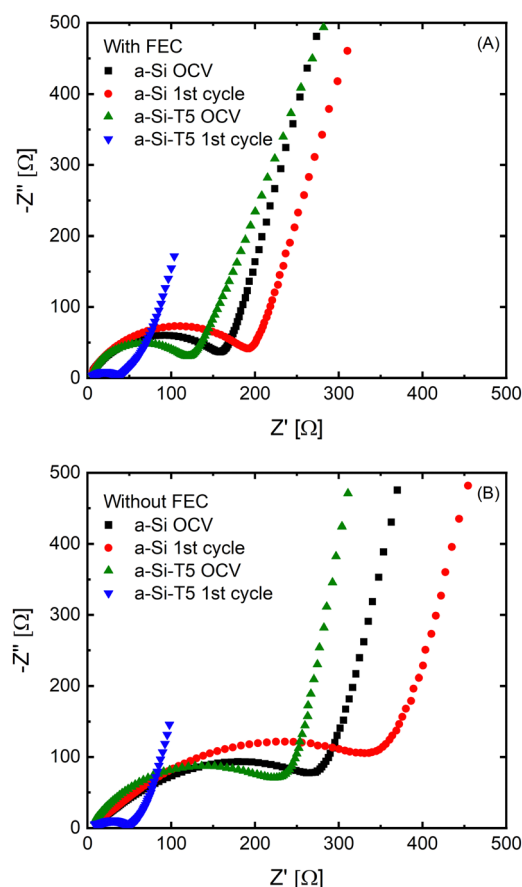


Fig. 2 EIS of coin cells with Li-metal counter electrode assembled with active electrodes based on pristine amorphous silicon (a-Si) and  $\text{TiO}_2$ -coated amorphous silicon (a-Si-T5). The electrolytes consisted of 1.2 M  $\text{LiPF}_6$  in EC : EMC (7 : 3 vol%) + 2 wt% VC with 10 wt% FEC (A) and without FEC (B). Cells were measured right after cell assembly and after 1 charge–discharge cycle between 0.05–1.00 V vs.  $\text{Li/Li}^+$  at C/20.



LiPF<sub>6</sub> in EC:EMC (7:3 vol%) and 2 wt% VC. All EIS plots are composed of a compressed semicircle in the middle frequency region and an inclined line in low frequency region indicating the charge transfer process and Li-ion diffusion process, respectively. The intercept at the high frequency region is attributed to the total resistance of the electrolyte, separator and the electrical contacts. The EIS data showed that the pristine electrodes based on TiO<sub>2</sub>-coated Si (a-Si-T5) had lower impedance than the uncoated samples (a-Si) with both electrolytes. The difference was largest for the FEC-free electrolyte (Fig. 2B). In addition, after one charge-discharge cycle the impedance of electrodes based on a-Si-T5 was significantly lower than the pristine electrodes, while the a-Si samples showed the opposite trend. This shows that the TiO<sub>2</sub> coating improves the kinetics in the FEC-free electrolyte, which should lead to better performance during electrochemical cycling.

This difference in performance was further confirmed by galvanostatic cycling in coin cells vs. Li-metal reference electrode (Fig. 3 and Fig. S7–S10, ESI†). As expected, cells cycled with FEC-containing electrolyte retain relatively stable performance over the course of 200 cycles due to the improved SEI

formation properties of the FEC.<sup>28–30</sup> Predictably, the uncoated Si (a-Si) anode without FEC additive in the electrolyte suffers from fast capacity decay. However, the coating with 3 nm TiO<sub>2</sub> (a-Si-T5) provides an improvement in capacity retention without the addition of FEC additive in the electrolyte. A careful examination revealed that the a-Si electrodes without FEC experience a fast degradation after only a few cycles (18 cycles) whereas a-Si-T5 exhibits relatively stable performance for approximately 60 cycles. Noteworthy, the initial Coulombic efficiency is significantly lower for a-Si-T5 than a-Si for both electrolytes, which is most likely due to irreversible lithiation of the TiO<sub>2</sub>-coating during the first cycle.

To gain further insights into the difference in cycling behavior, *ex situ* scanning electron microscopy (SEM) were performed on electrodes extracted after the 23rd delithiation (Fig. 4). These SEM images showed that the electrodes with uncoated a-Si (Fig. 4A) were denser, compared to the coated sample (Fig. 4B). This could be a result of electrochemical sintering where the Si nanoparticles in the uncoated sample are agglomerating as a function of electrochemical cycling, while the TiO<sub>2</sub>-coated sample maintains a more porous electrode structure.<sup>31</sup>

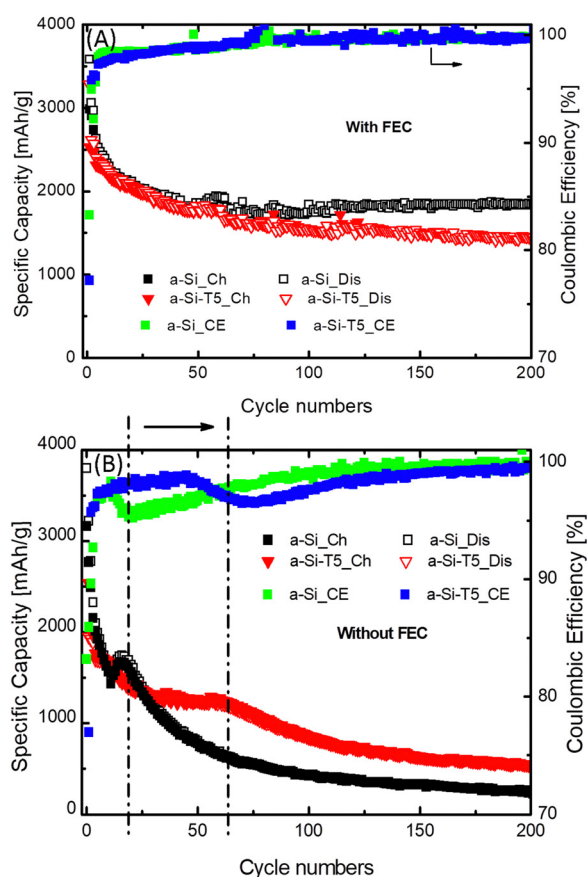


Fig. 3 Specific capacity and Coulombic efficiency during galvanostatic cycling of pristine amorphous silicon (a-Si) and TiO<sub>2</sub>-coated amorphous silicon (a-Si-T5). The electrolytes consisted of 1.2 M LiPF<sub>6</sub> in EC:EMC (7:3 vol%) + 2 wt% VC with 10 wt% FEC (A) and without FEC (B). The dashed lines highlights the region where the difference in cycling stability is largest between the coated and uncoated sample.

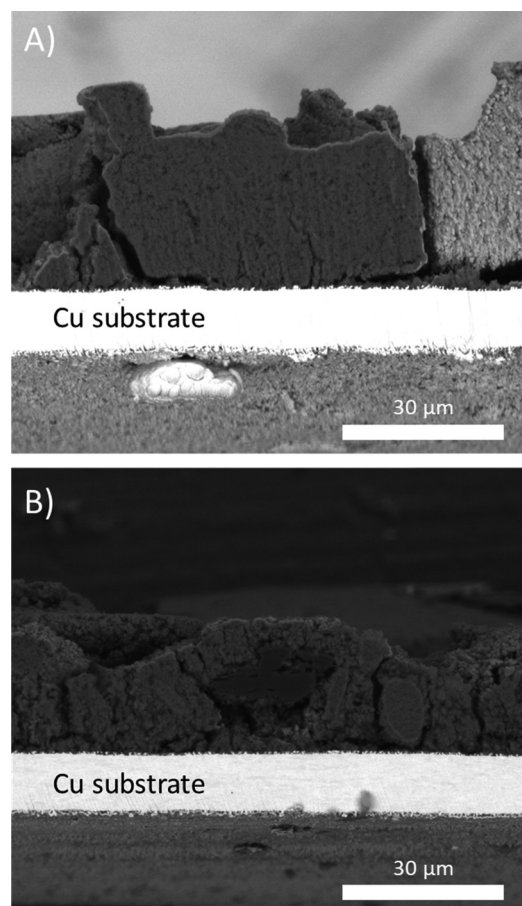


Fig. 4 SEM images of electrodes extracted after the 23rd delithiation from coin cells that had been cycled vs. Li-metal counter electrode with 1.2 M LiPF<sub>6</sub> in EC:EMC (7:3 vol%) + 2 wt% VC as electrolyte. (A) uncoated sample (a-Si) and (B) TiO<sub>2</sub>-coated sample (a-Si-T5).





This difference in behavior is likely linked to the TiO<sub>2</sub> coating and its influence on the formation of the SEI layer, which was further investigated through *ex situ* X-ray photoelectron spectroscopy (XPS) on pristine electrodes and electrodes extracted after the 23rd delithiation (Fig. S4–S6, ESI†). Due to the thickness of the SEI layer and the limited penetration depth of the X-ray probe, the XPS signals from Si and Ti after cycling were too weak to allow for detailed analysis of the delithiated samples (Fig. S4 and S5, ESI†).

However, XPS of F 1s showed a relative difference in the F-species on the surface of the coated vs. uncoated samples (Fig. S6, ESI†). In the delithiated uncoated sample (a-Si), the peak assigned to LiPF<sub>6</sub> (and Li<sub>x</sub>PF<sub>y</sub>O<sub>2</sub>) is higher than the peak assigned to LiF.<sup>32,33</sup> However, in the delithiated a-Si-T5 sample the intensity of LiF is significantly higher than that of LiPF<sub>6</sub>. This indicates formation of more LiF in the SEI of the TiO<sub>2</sub>-coated sample, which is considered the key inorganic component of the SEI layer.<sup>14</sup> This difference in the composition of the SEI layer could explain the different densities of the delithiated electrodes observed by SEM (Fig. 4). The SEI layer together with the TiO<sub>2</sub> coating might act as a barrier between the Si nanoparticles, thus mitigating electrochemical sintering and volume change of Si during lithiation and delithiation, which can explain the enhanced cycling stability.

In conclusion, we have been able to develop a method for coating of Si nanoparticles with a thin TiO<sub>2</sub> layer, which showed improved cycling stability in Li-ion half cells compared to the uncoated sample, when cycled with FEC-free electrolyte. This advancement can be attributed to the enhanced formation of stable SEI layer on the surface of the coated Si particles, thereby mitigating the effects of the volume changes during (de)lithiation and avoiding electrochemical sintering of the particles. As a result, the coating serves as an artificial SEI at least during initial cycling. While there are still improvements to be made, continuing to improve the ALD coating of the Si particles may lead to even better cycling performance and thus become an alternative to the use of FEC as an additive in Si-based electrodes.

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## Data availability

The data supporting this article have been included as part of the ESI.†

## Conflicts of interest

There are no conflicts to declare.

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